Yunsi Fei

List of Publications by Year
in descending order

36 papers	480 citations	1307594 7 h-index	1199594 12 g-index
36 all docs	36 docs citations	36 times ranked	501 citing authors

#	Article	IF	Citations
1	Vehicle Speed Prediction by Two-Level Data Driven Models in Vehicular Networks. IEEE Transactions on Intelligent Transportation Systems, 2017, 18, 1793-1801.	8.0	119
2	Side-channel power analysis of a GPU AES implementation. , 2015, , .		46
3	MURAO: A multi-level routing protocol for acoustic-optical hybrid underwater wireless sensor networks. , 2012, , .		45
4	Traffic and vehicle speed prediction with neural network and Hidden Markov model in vehicular networks. , 2015, , .		34
5	A statistics-based success rate model for DPA and CPA. Journal of Cryptographic Engineering, 2015, 5, 227-243.	1.8	24
6	Evaluating Fault Resiliency of Compressed Deep Neural Networks. , 2019, , .		18
7	Algebraic Fault Analysis of SHA-3 Under Relaxed Fault Models. IEEE Transactions on Information Forensics and Security, 2018, 13, 1752-1761.	6.9	16
8	Power Analysis Attack of an AES GPU Implementation. Journal of Hardware and Systems Security, 2018, 2, 69-82.	1.3	15
9	Differential Fault Analysis of SHA3-224 and SHA3-256. , 2016, , .		14
10	A delay-aware probability-based MAC protocol for underwater acoustic sensor networks. , 2015, , .		12
11	Exploiting Bank Conflict-based Side-channel Timing Leakage of GPUs. Transactions on Architecture and Code Optimization, 2019, 16, 1-24.	2.0	11
12	A Novel GPU Overdrive Fault Attack. , 2020, , .		11
13	Power analysis attack on hardware implementation of MAC-Keccak on FPGAs. , 2014, , .		10
14	Side-channel power analysis of different protection schemes against fault attacks on AES. , 2014, , .		10
15	Designing and implementing a Malicious 8051 processor. , 2012, , .		8
16	Algebraic fault analysis of SHA-3. , 2017, , .		8
17	A PHEV Power Management Cyber-Physical System for On-Road Applications. IEEE Transactions on Vehicular Technology, 2017, 66, 5797-5807.	6.3	8
18	Decentralized scheduling of PEV on-street parking and charging for smart grid reactive power compensation. , 2013, , .		7

#	Article	IF	Citations
19	Side-channel power analysis of XTS-AES. , 2017, , .		7
20	Differential Fault Analysis of SHA-3 Under Relaxed Fault Models. Journal of Hardware and Systems Security, 2017, 1, 156-172.	1.3	7
21	Towards secure cryptographic software implementation against side-channel power analysis attacks. , 2015, , .		6
22	Testbed-based performance evaluation of handshake-free MAC protocols for underwater acoustic sensor networks. , 2016, , .		6
23	SMARP: A Stochastic MAC Protocol with Randomized Power Control for Underwater Sensor Networks. , 2016, , .		6
24	An adaptive routing protocol based on connectivity prediction for underwater disruption tolerant networks. , 2013, , .		5
25	Guest Editorial: Special Section on Embedded System Security. IEEE Embedded Systems Letters, 2015, 7, 1-2.	1.9	5
26	Balance power leakage to fight against side-channel analysis at gate level in FPGAs. , 2015, , .		4
27	Large Delay Analog Trojans: A Silent Fabrication-Time Attack Exploiting Analog Modalities. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2021, 29, 124-135.	3.1	4
28	DSH-MAC: Medium Access Control based on Decoupled and Suppressed Handshaking for long-delay Underwater Acoustic Sensor Networks. , 2013, , .		3
29	Efficient 2nd-order power analysis on masked devices utilizing multiple leakage. , 2015, , .		3
30	Comprehensive Side-Channel Power Analysis of XTS-AES. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 2019, 38, 2191-2200.	2.7	2
31	Nacre: Durable, Secure and Energy-efficient Non-Volatile Memory Utilizing Data Versioning. IEEE Transactions on Emerging Topics in Computing, 2019, , 1-1.	4.6	2
32	Efficient Nonprofiling 2nd-Order Power Analysis on Masked Devices Utilizing Multiple Leakage Points. IEEE Transactions on Dependable and Secure Computing, 2019, 16, 843-855.	5.4	2
33	HATI: Hardware Assisted Thread Isolation for Concurrent C/C++ Programs. , 2014, , .		1
34	Compiler-Assisted Threshold Implementation against Power Analysis Attacks. , 2017, , .		1
35	Leakage evaluation on power balance countermeasure against side-channel attack on FPGAs. , 2015, , .		0
36	An Ultra-low Power and Lower Area Current-Mode based Physically Unclonable Function with less than 100nW Power Consumption and a Native Instability of 0.6875% for IoT Applications. , 2020, , .		0