

# Vivek Chidambaram

## List of Publications by Year in descending order

Source: <https://exaly.com/author-pdf/4796079/publications.pdf>

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4  
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citations

2258059

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2550090

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31  
citing authors

#	ARTICLE	IF	CITATIONS
1	Heterogeneous System Level Integration Using Active Si Interposer. IEEE Journal of the Electron Devices Society, 2019, 7, 1209-1216.	2.1	9
2	Novel Solution for High-Temperature Dielectric Application to Encapsulate High-Voltage Power Semiconductor Devices. IEEE Transactions on Components, Packaging and Manufacturing Technology, 2019, 9, 3-9.	2.5	6
3	Development of 2.5D high density device on large ultra-thin active interposer. , 2019, , .		2
4	Cyanate Ester-Based Encapsulation Material for High-Temperature Applications. Journal of Electronic Materials, 2013, 42, 2803-2812.	2.2	12