

Yograj Singh Duksh

List of Publications by Year in descending order

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citing authors

#	ARTICLE	IF	CITATIONS
1	Self-heating and Negative Differential Conductance Improvement by Substrate Bias Voltage in Tri-gate Junctionless Transistor. Silicon, 2022, 14, 2219-2224.	1.8	1
2	Subthreshold Modeling of Graded Channel Double Gate Junctionless FETs. Silicon, 2021, 13, 1231-1238.	1.8	8
3	FDTD technique based crosstalk analysis of bundled SWCNT interconnects. Journal of Semiconductors, 2015, 36, 055002.	2.0	13
4	Analysis of propagation delay and power with variation in driver size and number of shells in multi walled carbon nanotube interconnects. Journal of Engineering, Design and Technology, 2013, 11, 19-33.	1.1	4
5	Effect of Driver Size and Number of Shells on Propagation Delay in MWCNT Interconnects. , 2011, , .		0
6	Performance comparison of carbon nanotube, nickel silicide nanowire and copper VLSI interconnects. Journal of Engineering, Design and Technology, 2010, 8, 334-353.	1.1	0