

# Shun Suzuki

## List of Publications by Year in descending order

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8  
papers

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2682572  
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times ranked

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#	ARTICLE	IF	CITATIONS
1	BER Evaluation System Considering Device Characteristics of TLC and QLC NAND Flash Memories in Hybrid SSDs with Real Storage Workloads. , 2021, , .		6
2	Write and Read Frequency-Based Word-Line Batch $V_{\mathrm{TH}}$ Modulation for 2-D and 3-D-TLC NAND Flash Memories. IEEE Journal of Solid-State Circuits, 2018, 53, 2917-2926.	5.4	5
3	Privacy Protection NAND Flash System With Flexible Data-Lifetime Control by In-3-D Vertical Cell Processing. IEEE Journal of Solid-State Circuits, 2020, 55, 2802-2809.	5.4	5
4	Data Pattern & Memory Variation Aware Fine-Grained ECC Optimized by Neural Network for 3D-TLC NAND Flash Memories with 2.0x Data-Retention Time Extension and 30% Parity Overhead Reduction. , 2019, , .		4
5	Extremely Biased Error Correction Method to Reduce Read Disturb Errors of 3D-TLC NAND Flash Memories by 60%. , 2020, , .		3
6	Computational Approximate Storage with Neural Network-based Error Patrol of 3D-TLC NAND Flash Memory for Machine Learning Applications. , 2020, , .		3
7	Error elimination ECC by horizontal error detection and vertical-LDPC ECC to increase data-retention time by 230% and acceptable bit-error rate by 90% for 3D-NAND flash SSDs. , 2018, , .		1
8	Spatial Color-Perceived Data Control of 3D-TLC NAND Flash for Image Dectection. , 2020, , .		0