

Chi-Ray Huang

List of Publications by Year in descending order

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papers

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2682572

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9
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9
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citing authors

#	ARTICLE	IF	CITATIONS
1	Ultra-Low Standby Power SRAM with Adaptive Data-Retention-Voltage-Regulating Scheme. , 2018, , .		9
2	Single bitâ€line 8T SRAM cell with asynchronous dual wordâ€line control for bitâ€interleaved ultraâ€low voltage operation. IET Circuits, Devices and Systems, 2018, 12, 713-719.	1.4	6
3	A power-efficient pulse-based in-situ timing error predictor for PVT-variation sensitive circuits. , 2014, , .		5
4	An Energy-Efficient Conditional Biasing Write Assist With Built-In Time-Based Write-Margin-Tracking for Low-Voltage SRAM. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2021, 29, 1586-1590.	3.1	4
5	Minimum convertible voltage analysis for ratioless and robust subthreshold level conversion. , 2012, , .		2
6	A 300mV Sub-1pj differential 6T sub-threshold SRAM with low energy and variability resilient local assist circuit. , 2013, , .		2
7	Design of A 0.3Vâ€1.2V wide input range solar energy harvesting circuit with high converting power efficiency. , 2016, , .		2
8	A limited-contention cross-coupled level shifter for energy-efficient subthreshold-to-superthreshold voltage conversion. , 2014, , .		1
9	An ultra-low-power adaptive-body-bias control for subthreshold circuits. , 2014, , .		1