

# Sapna Rani Ghimiray

## List of Publications by Year in descending order

Source: <https://exaly.com/author-pdf/4754559/publications.pdf>

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7  
papers

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citations

2682572

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2917675

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g-index

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docs citations

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14  
citing authors

#	ARTICLE	IF	CITATIONS
1	An improved charge-sharing elimination pseudo-domino logic. International Journal of Circuit Theory and Applications, 2020, 48, 1346-1362.	2.0	6
2	Noise Immune Pseudo-Stacked-wide keeper Domino technique for Ultra Deep Submicron Technology. International Journal of Engineering and Advanced Technology, 2019, 8, 3001-3009.	0.3	0
3	Strength-Varying keeper based 4:1 MUX for low-leakage and low jitter at 500 MHz Clock. , 2018, , .		0
4	Ultralow power, noise immune stacked-double stage clocked-inverter domino technique for ultradeep submicron technology. International Journal of Circuit Theory and Applications, 2018, 46, 1953-1967.	2.0	3
5	A leakage-tolerant 16-bit diode footed domino comparator using lector technique. , 2017, , .		1
6	Energy efficient, noise immune 4 $\times$ 4 Vedic multiplier using semi-domino logic style. , 2017, , .		2
7	Performance analysis of a 4-bit comparator circuit using different adiabatic logics. , 2017, , .		6