

Sapna Rani Ghimiray

List of Publications by Year in descending order

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Version: 2024-02-01

7
papers

18
citations

2682572

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2917675

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g-index

7
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7
docs citations

7
times ranked

14
citing authors

#	ARTICLE	IF	CITATIONS
1	Performance analysis of a 4-bit comparator circuit using different adiabatic logics. , 2017, , .		6
2	An improved charge-sharing elimination pseudo-domino logic. International Journal of Circuit Theory and Applications, 2020, 48, 1346-1362.	2.0	6
3	Ultralow power, noise immune stacked-double stage clocked-inverter domino technique for ultradeep submicron technology. International Journal of Circuit Theory and Applications, 2018, 46, 1953-1967.	2.0	3
4	Energy efficient, noise immune 4 \times 4 Vedic multiplier using semi-domino logic style. , 2017, , .		2
5	A leakage-tolerant 16-bit diode footed domino comparator using lector technique. , 2017, , .		1
6	Strength-Varying keeper based 4:1 MUX for low-leakage and low jitter at 500 MHz Clock. , 2018, , .		0
7	Noise Immune Pseudo-Stacked-wide keeper Domino technique for Ultra Deep Submicron Technology. International Journal of Engineering and Advanced Technology, 2019, 8, 3001-3009.	0.3	0