

Narayanan Raghavan

List of Publications by Year in descending order

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3
papers

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3311381
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citing authors

#	ARTICLE	IF	CITATIONS
1	Modeling and simulation of static power dissipation in CMOS with SELBOX structure. , 2017, , .		2
2	Static power characteristics of selective buried oxide CMOS devices. International Journal of Numerical Modelling: Electronic Networks, Devices and Fields, 2019, 32, e2460.	1.9	2
3	TCAD Simulation and Analysis of Selective Buried Oxide MOSFET Dynamic Power. Journal of Low Power Electronics and Applications, 2019, 9, 29.	2.0	2