

Yao Wang

List of Publications by Year in descending order

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#	ARTICLE	IF	CITATIONS
1	A Wideband Differential Linear Low-Noise Transconductance Amplifier With Active-Combiner Feedback in Complementary MGTR Configurations. IEEE Transactions on Circuits and Systems I: Regular Papers, 2021, 68, 224-237.	5.4	22
2	A 300 nW 10 kHz Relaxation Oscillator with 105 ppm/°C Temperature Coefficient. Circuits, Systems, and Signal Processing, 2021, 40, 5264-5279.	2.0	3
3	Optimizing the electron leakage of deep ultraviolet laser diode by V-shaped electron blocking layer. , 2021, , .		0
4	Optimization of deep ultraviolet laser diode using thickness gradient multiple-quantum-well. , 2021, , .		2
5	Modeling Attack Resistant Arbiter PUF with Time-Variant Obfuscation Scheme. , 2021, , .		0
6	An Improved LVTSCR Device with an Embedded BJT for ESD Protection. , 2021, , .		0
7	A Programmable Frequency Divider With a Full Modulus Range and 50% Output Duty Cycle. IEEE Access, 2020, 8, 102032-102039.	4.2	10
8	Low-Frequency Noise in CMOS Switched-gm Mixers: A Quasi-Analytical Model. IEEE Access, 2020, 8, 191219-191230.	4.2	12
9	A low power trimming-free CMOS relaxation oscillator with process and temperature compensation. Modern Physics Letters B, 2020, 34, 2050176.	1.9	1
10	A 0.2-3.3 GHz 2.4 dB NF 45 dB Gain CMOS current-mode receiver front-end. Modern Physics Letters B, 2020, 34, 2050226.	1.9	8
11	Novel prioritized LRU circuits for shared cache in computer systems. Modern Physics Letters B, 2020, 34, 2050242.	1.9	0
12	A Low-Power High-Speed Dynamic Comparator With a Transconductance-Enhanced Latching Stage. IEEE Access, 2019, 7, 93396-93403.	4.2	53
13	A 0.2-3.3 GHz 2.4 dB NF 45 dB Gain Current-Mode Front-End for SAW-less Receivers in 180 nm CMOS. , 2019, , .		2
14	A low power trimming-free relaxation oscillator with process and temperature compensation. , 2019, , .		2
15	Low-power High-speed Dynamic Comparator Using a New Regenerative Stage. , 2018, , .		4
16	One-Step Sneak-Path Free Read Scheme for Resistive Crossbar Memory. ACM Journal on Emerging Technologies in Computing Systems, 2017, 13, 1-18.	2.3	2
17	Switching scheme with 98.4% switching energy reduction and high accuracy for SAR ADCs. Analog Integrated Circuits and Signal Processing, 2017, 90, 681-686.	1.4	14
18	Wideband SIW H-plane dual-ridged end-fire antenna for conformal application. Microwave and Optical Technology Letters, 2017, 59, 286-292.	1.4	4

#	ARTICLE	IF	CITATIONS
19	A low-voltage high-swing colpitts VCO with Inherent tapped capacitors based dynamic body bias technique. , 2017, , .		2
20	Wideband cavity-backed log-periodic-slot end-fire antenna with vertical polarization for conformal application. International Journal of RF and Microwave Computer-Aided Engineering, 2017, 27, e21067.	1.2	4
21	COMPACT MULTI-LAYER FOUR-WAY SIW POWER COMBINERS/DIVIDERS OPERATING AT W-BAND. Progress in Electromagnetics Research C, 2016, 61, 185-193.	0.9	2
22	Polarization conversion of metasurface for the application of wide band low-profile circular polarization slot antenna. Applied Physics Letters, 2016, 109, .	3.3	106
23	Compact W-band multi-layer four-way SIW power combiner/divider. , 2016, , .		2
24	Wideband high gain circularly polarized UHF RFID reader antenna. , 2016, , .		2
25	A wideband complementary noise cancelling CMOS LNA. , 2016, , .		11
26	A 255nW 138kHz RC oscillator for ultra-low power applications. , 2016, , .		1
27	Design of prioritized LRU circuit for shared cache in real-time computer systems. , 2016, , .		0
28	A Low-Power Edge Detection Technique for Sensor Wake-Up Applications. Journal of Circuits, Systems and Computers, 2015, 24, 1550157.	1.5	4
29	An all-CMOS self-compensated relaxation oscillator. Analog Integrated Circuits and Signal Processing, 2015, 82, 241-249.	1.4	2
30	Design Techniques for Ultra-Low Voltage Comparator Circuits. Journal of Circuits, Systems and Computers, 2015, 24, 1550013.	1.5	2
31	Design and implementation of a passive UHF RFID tag with a temperature and process compensation oscillator. International Journal of RF Technologies: Research and Applications, 2015, 6, 171-184.	0.7	1
32	A 1-V DTMOS-Based fully differential telescopic OTA. , 2014, , .		3
33	A low power CMOS voltage reference generator with temperature and process compensation. Analog Integrated Circuits and Signal Processing, 2014, 81, 313-324.	1.4	4
34	Energy-efficient hybrid capacitor switching scheme for SAR ADC. Electronics Letters, 2014, 50, 22-23.	1.0	75
35	A novel envelope edge detector for ultra-low power sensor wake-up circuit. , 2013, , .		0
36	A low power baseband processor with clock variance-tolerant for UHF RFID transponder. , 2013, , .		1

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37	Design of a semi-passive UHF RFID tag IC with freight security monitoring function. International Journal of RF Technologies: Research and Applications, 2013, 4, 93-105.	0.7	1
38	Current reference with temperature compensation for low power applications. , 2012, , .		2
39	Design of a passive UHF RFID tag for the ISO18000-6C protocol. Journal of Semiconductors, 2011, 32, 055009.	3.7	11