

Jaeyoung Kim

List of Publications by Year in descending order

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5
papers

45
citations

2682572

2
h-index

2917675

2
g-index

5
all docs

5
docs citations

5
times ranked

61
citing authors

#	ARTICLE	IF	CITATIONS
1	A robust 12T SRAM cell with improved write margin for ultra-low power applications in 40 nm CMOS. The Integration VLSI Journal, 2017, 57, 1-10.	2.1	33
2	Energy-Efficient Hardware Architecture of Self-Organizing Map for ECG Clustering in 65-nm CMOS. IEEE Transactions on Circuits and Systems II: Express Briefs, 2017, 64, 1097-1101.	3.0	9
3	Ultra low-power filter bank for hearing aid speech processor. , 2012, , .		1
4	A 250mV sub-threshold asynchronous 8051microcontroller with a novel 16T SRAM cell for improved reliability in 40nm CMOS. , 2013, , .		1
5	A low-power reconfigurable CMOS power amplifier for wireless sensor network applications. , 2014, , .		1