

# Amila Akagic

## List of Publications by Year in descending order

Source: <https://exaly.com/author-pdf/4691532/publications.pdf>

Version: 2024-02-01

22  
papers

218  
citations

2258059

3  
h-index

2053705

5  
g-index

22  
all docs

22  
docs citations

22  
times ranked

128  
citing authors

#	ARTICLE	IF	CITATIONS
1	LW-FIRE: A Lightweight Wildfire Image Classification with a Deep Convolutional Neural Network. Applied Sciences (Switzerland), 2022, 12, 2646.	2.5	12
2	Unsupervised Method for Wildfire Flame Segmentation and Detection. IEEE Access, 2022, 10, 55213-55225.	4.2	6
3	Multilevel image thresholding based on Rao algorithms and Kapur's Entropy. , 2022, , .		1
4	Deep Reinforcement Learning in Smart Grid: Progress and Prospects. , 2022, , .		0
5	Multiple Object Trackers in OpenCV: A Benchmark. , 2021, , .		6
6	Single Object Trackers in OpenCV: A Benchmark. , 2020, , .		16
7	Low cost UGV platform for autonomous 2D navigation and map-building based on a single sensory input. , 2020, , .		1
8	Two approaches in solving Rubik's cube with Hardware-Software Co-design. , 2020, , .		0
9	Forest Fire Detection based on Color Spaces Combination. , 2019, , .		16
10	Image-Based Crack Detection Using Sub-image Technique. , 2019, , .		0
11	OpenCL Superpixel Implementation on a General Purpose Multi-core CPU. , 2018, , .		3
12	Pavement crack detection using Otsu thresholding for image segmentation. , 2018, , .		67
13	Superpixel Accelerator for Computer Vision Applications on Arria 10 SoC. , 2018, , .		10
14	A hybrid method for prediction of protein secondary structure based on multiple artificial neural networks. , 2017, , .		11
15	Pothole detection: An efficient vision based method using RGB color space image segmentation. , 2017, , .		38
16	Unsupervised method for detection of high severity distresses on asphalt pavements. , 2017, , .		8
17	Design and Implementation of IP-based iSCSI Offload Engine on an FPGA. IPSJ Transactions on System LSI Design Methodology, 2013, 6, 112-121.	0.8	1
18	High-Speed Fully-Adaptable CRC Accelerators. IEICE Transactions on Information and Systems, 2013, E96.D, 1299-1308.	0.7	3

#	ARTICLE	IF	CITATIONS
19	Performance analysis of fully-adaptable CRC accelerators on an FPGA. , 2012, , .		1
20	A study of adaptable co-processors for Cyclic Redundancy Check on an FPGA. , 2012, , .		8
21	Performance evaluation of multiple lookup tables algorithms for generating CRC on an FPGA. , 2011, , .		4
22	High speed CRC with 64-bit generator polynomial on an FPGA. Computer Architecture News, 2011, 39, 72-77.	2.5	6