Jaime Ramirez-Angulo

List of Publications by Year in Descending Order

115 papers	1,120 citations	17 h-index	28 g-index
140 ext. papers	1,400 ext. citations	2 avg, IF	4.37 L-index

#	Paper	IF	Citations
115	Energy-Efficient Amplifiers Based on Quasi-Floating Gate Techniques. *Applied Sciences (Switzerland)*, **2021**, 11, 3271	2.6	0
114	Super-Gain-Boosted AB-AB Fully Differential Miller Op-Amp With 156dB Open-Loop Gain and 174MV/V MHZ pF/W Figure of Merit in 130nm CMOS Technology. *IEEE Access*, **2021**, 1-1	3.5	2
113	An Enhanced Gain-Bandwidth Class-AB Miller op-amp With 23,800 MHz pF/mW FOM, 11-16 Current Efficiency and Wide Range of Resistive and Capacitive Loads Driving Capability. *IEEE Access*, **2021**, 9, 69783-69797	3.5	0
112	Gain-Boosted Super Class AB OTAs Based on Nested Local Feedback. *IEEE Transactions on Circuits and Systems I: Regular Papers*, **2021**, 68, 3562-3573	3.9	3
111	CMOS Analog AGC for Biomedical Applications. *Electronics (Switzerland)*, **2020**, 9, 878	2.6	0
110	360 nW Gate-Driven Ultra-Low Voltage CMOS Linear Transconductor With 1 MHz Bandwidth and Wide Input Range. *IEEE Transactions on Circuits and Systems II: Express Briefs*, **2020**, 67, 2332-2336	3.5	7
109	1-V 15-W 130-nm CMOS Super Class AB OTA **2020**,		1
108	Power Efficient Simple Technique to Convert a Reset-and-Hold Into a True-Sample-and-Hold Using an Auxiliary Output Stage. *IEEE Access*, **2020**, 8, 66508-66516	3.5	3
107	Low-Voltage 0.81mW, 1 2 CMOS VGA With 5% Bandwidth Variations and 8dB DC Rejection. *IEEE Access*, **2020**, 8, 106310-106321	3.5	4
106	A High-Frequency Small-Signal Model for Four-Port Network MOSFETs **2020**,		1
105	Pseudo-Three-Stage Miller Op-Amp With Enhanced Small-Signal and Large-Signal Performance. *IEEE Transactions on Very Large Scale Integration (VLSI) Systems*, **2019**, 27, 2246-2259	2.6	9
104	Class AB amplifier with enhanced slew rate and GBW. *International Journal of Circuit Theory and Applications*, **2019**, 47, 1199	2	7
103	A compact four quadrant CMOS analog multiplier. *AEU - International Journal of Electronics and Communications*, **2019**, 108, 53-61	2.8	6
102	Analysis, Comparison, and Experimental Validation of a Class AB Voltage Follower With Enhanced Bandwidth and Slew Rate. *IEEE Transactions on Very Large Scale Integration (VLSI) Systems*, **2019**, 27, 1353-1364	2.6	8
101	Gain and Bandwidth Enhanced Class-AB OTAs **2019**,		2
100	CMOS First-Order All-Pass Filter With 2-Hz Pole Frequency. *IEEE Transactions on Very Large Scale Integration (VLSI) Systems*, **2019**, 27, 294-303	2.6	3
99	0.25-V Class-AB CMOS Capacitance Multiplier and Precision Rectifiers. *IEEE Transactions on Very Large Scale Integration (VLSI) Systems*, **2019**, 27, 830-842	2.6	3

98	An Op-Amp Approach for Bandpass VGAs With Constant Bandwidth. *IEEE Transactions on Circuits and Systems II: Express Briefs*, **2018**, 65, 1144-1148	3.5	2
97	Bandwidth-Enhanced High Current Efficiency Class-AB Buffer With Very Low Output Resistance. *IEEE Transactions on Circuits and Systems II: Express Briefs*, **2018**, 65, 1544-1548	3.5	8
96	An Amplified Offset Compensation Scheme and Its Application in a Track and Hold Circuit. *IEEE Transactions on Circuits and Systems II: Express Briefs*, **2018**, 65, 416-420	3.5	7
95	Enhanced Single-Stage Folded Cascode OTA Suitable for Large Capacitive Loads. *IEEE Transactions on Circuits and Systems II: Express Briefs*, **2018**, 65, 441-445	3.5	19
94	0.18-V supply voltage gate-driven PGA with 0.7-Hz to 2-kHz constant bandwidth and 0.15-W power dissipation. *International Journal of Circuit Theory and Applications*, **2018**, 46, 272-279	2	7
93	Folded Cascode OTA with 5540 MHzpF/mA FoM **2018**,		2
92	Modular Discrete and CMOS Integrated Implementations of High-Speed Analog Rank-Order Filters. *Circuits, Systems, and Signal Processing*, **2018**, 37, 5637-5646	2.2	
91	On the Optimal Current Followers for Wide-Swing Current-Efficient Amplifiers **2018**,		4
90	0.5 V 15 mu W Recycling Folded Cascode Amplifier With 34767 MHz pF/mA FOM. *IEEE Solid-State Circuits Letters*, **2018**, 1, 170-173	2	12
89	Ultra-Low Power Subthreshold Quasi Floating Gate CMOS Logic Family for Energy Harvesting **2018**,		2
88	A Highly Efficient Composite Class-AB AB Miller Op-Amp With High Gain and Stable From 15 pF Up To Very Large Capacitive Loads. *IEEE Transactions on Very Large Scale Integration (VLSI) Systems*, **2018**, 26, 2061-2072	2.6	19
87	Class AB flipped voltage follower with very low output resistance and no additional power. *IEICE Electronics Express*, **2018**, 15, 20171170-20171170	0.5	5
86	Super Class-AB Recycling Folded Cascode OTA. *IEEE Journal of Solid-State Circuits*, **2018**, 53, 2614-2623	5.5	37
85	Super class AB OTA without open-loop gain degradation based on dynamic cascode biasing. *International Journal of Circuit Theory and Applications*, **2017**, 45, 2111-2118	2	18
84	High current efficiency class-AB OTA with high open loop gain and enhanced bandwidth. *IEICE Electronics Express*, **2017**, 14, 20170719-20170719	0.5	8
83	A Simple Miller Compensation With Essential Bandwidth Improvement. *IEEE Transactions on Very Large Scale Integration (VLSI) Systems*, **2017**, 25, 3186-3192	2.6	3
82	Low-Power Analog Channel Selection Filtering Techniques. *Circuits, Systems, and Signal Processing*, **2017**, 36, 895-915	2.2	1
81	A super class-AB OTA with high output current and no open loop gain degradation **2017**,		2

80	FVF-Based Low-Dropout Voltage Regulator with Fast Charging/Discharging Paths for Fast Line and Load Regulation. *ETRI Journal*, **2017**, 39, 373-382	1.4	1
79	A Noise-Robust Positive-Feedback Floating-Gate Logic. *IEICE Transactions on Electronics*, **2016**, E99.C, 452-457	0.4	1
78	Free class AB□AB Miller opamp with high current enhancement. *Electronics Letters*, **2015**, 51, 215-217	1.1	10
77	Low-power CMOS variable gain amplifier based on a novel tunable transconductor. *IET Circuits, Devices and Systems*, **2015**, 9, 105-110	1.1	13
76	A 1.2-V 450-□W G_{m} - C Bluetooth Channel Filter Using a Novel Gain-Boosted Tunable Transconductor. *IEEE Transactions on Very Large Scale Integration (VLSI) Systems*, **2015**, 23, 1572-1576	2.6	17
75	Highly accurate CMOS second generation current conveyor and transconductor **2015**,		2
74	Energy harvesting microsystems based on the QFG MOS transistors **2015**,		2
73	Rail to rail CMOS complementary input stage with only one active differential pair at a time. *IEICE Electronics Express*, **2014**, 11, 20140392-20140392	0.5	1
72	Realistic model for the multiple-input floating-gate transistor. *IEEJ Transactions on Electrical and Electronic Engineering*, **2014**, 9, 692-694	1	2
71	Highly linear micropower class AB current mirrors using Quasi-Floating Gate transistors. *Microelectronics Journal*, **2014**, 45, 1261-1267	1.8	12
70	Power Efficient Class AB Op-Amps With High and Symmetrical Slew Rate. *IEEE Transactions on Very Large Scale Integration (VLSI) Systems*, **2014**, 22, 943-947	2.6	11
69	Highly linear wide-swing continuous tuning of CMOS transconductors. *International Journal of Circuit Theory and Applications*, **2014**, 42, 831-841	2	4
68	Improved technique for continuous tuning of CMOS transconductor **2013**,		2
67	Design of micropower class AB transconductors: A systematic approach. *Microelectronics Journal*, **2013**, 44, 920-929	1.8	8
66	FGMOS flip-flop for low-power signal processing. *International Journal of Electronics*, **2013**, 100, 1683-1689	1.2	1
65	. *IEEE Transactions on Circuits and Systems I: Regular Papers*, **2013**, 60, 1300-1309	3.9	26
64	Micropower Class-AB VGA With Gain-Independent Bandwidth. *IEEE Transactions on Circuits and Systems II: Express Briefs*, **2013**, 60, 397-401	3.5	17
63	On the optimal choice of the output stage in CMOS transconductors **2013**,		1

62	The Flipped Voltage Follower: Theory and Applications. *Lecture Notes in Electrical Engineering*, **2013**, 269-287	0.2	4
61	Three novel improved CMOS C-multipliers. *International Journal of Circuit Theory and Applications*, **2012**, 40, 607-616	2	18
60	Power-efficient analog design based on the class AB super source follower. *International Journal of Circuit Theory and Applications*, **2012**, 40, 1143-1163	2	28
59	Micropower class AB voltage followers with simple quiescent current control **2012**,		5
58	Low Voltage Lazzaros WTA with enhanced loop gain. *IEICE Electronics Express*, **2012**, 9, 648-653	0.5	1
57	Using Floating Gate and Quasi-Floating Gate Techniques for Rail-to-Rail Tunable CMOS Transconductor Design. *IEEE Transactions on Circuits and Systems I: Regular Papers*, **2011**, 58, 1604-1614	3.9	41
56	Design of Two-Stage Class AB CMOS Buffers: A Systematic Approach. *ETRI Journal*, **2011**, 33, 393-400	1.4	7
55	Noise margin and short-circuit current in FGMOS logics. *IEICE Electronics Express*, **2011**, 8, 1967-1971	0.5	2
54	Current-mode CMOS multiplier/divider circuit operating in linear/saturation regions. *Analog Integrated Circuits and Signal Processing*, **2011**, 66, 299-302	1.2	11
53	Micropower high current-drive class AB CMOS current-feedback operational amplifier. *International Journal of Circuit Theory and Applications*, **2011**, 39, 893-903	2	14
52	Simple improvement stage for low voltage WTA and Rank Order circuits **2011**,		2
51	Three novel improved CMOS capacitance scaling schemes **2010**,		4
50	Tunable rail-to-rail FGMOS transconductor **2010**,		3
49	Compact low-voltage CMOS current-mode multiplier/divider **2010**,		8
48	Class AB CMOS tunable transconductor **2010**,		3
47	Low-voltage gm-enhanced CMOS differential pairs using positive feedback **2010**,		3
46	200 W CMOS class AB unity-gain buffers with accurate quiescent current control **2010**,		2
45	Low-Voltage Tunable Pseudo-Differential Transconductor with High Linearity. *ETRI Journal*, **2009**, 31, 576-584	1.4	4

44	Versatile multi-decade CMOS voltage-controlled oscillator with accurate amplitude and pulse width control. *Analog Integrated Circuits and Signal Processing*, **2009**, 60, 83-92	1.2	2
43	Low-Voltage MOS Translinear Analog Signal Processing. *Circuits, Systems, and Signal Processing*, **2009**, 28, 795-804	2.2	2
42	A tunable highly linear CMOS transconductor with 80 dB of SFDR. *The Integration VLSI Journal*, **2009**, 42, 277-285	1.4	6
41	Highly Linear Tunable CMOS $Gm{hbox{-}}C$ Low-Pass Filter. *IEEE Transactions on Circuits and Systems I: Regular Papers*, **2009**, 56, 2145-2158	3.9	51
40	**2009**,		1
39	Micropower class AB CMOS current conveyor based on quasi-floating gate techniques **2009**,		2
38	Low Voltage Differential Input Stage With Improved CMRR and True Rail-to-Rail Common Mode Input Range. *IEEE Transactions on Circuits and Systems II: Express Briefs*, **2008**, 55, 1229-1233	3.5	8
37	A CMOS linear tunable transconductor for continuous-time tunable Gm-C filters **2008**,		6
36	High slew rate two stage A/AB and AB/AB op-amps with phase lead compensation at output node and local common mode feedback **2008**,		4
35	Linear-enhanced V to I converters based on MOS resistive source degeneration **2008**,		2
34	CMOS Transconductors With Continuous Tuning Using FGMOS Balanced Output Current Scaling. *IEEE Journal of Solid-State Circuits*, **2008**, 43, 1313-1323	5.5	36
33	Class-AB Fully Differential Voltage Followers. *IEEE Transactions on Circuits and Systems II: Express Briefs*, **2008**, 55, 131-135	3.5	5
32	A 72 dB @ 2 MHz IM3 CMOS tunable pseudo-differential transconductor **2008**,		2
31	Comparison of programmable linear resistors based on quasi-floating gate MOSFETs **2008**,		5
30	A power efficient and simple scheme for dynamically biasing cascode amplifiers and telescopic op-amps. *The Integration VLSI Journal*, **2008**, 41, 539-543	1.4	2
29	Highly Linear V/I Converter with Programmable Current Mirrors **2007**,		12
28	Super Class-AB OTAs With Adaptive Biasing and Dynamic Output Current Scaling. *IEEE Transactions on Circuits and Systems Part 1: Regular Papers*, **2007**, 54, 449-457		69
27	A Very Linear OTA with V-I Conversion based on Quasi-Floating MOS Resistor **2007**,		4

26	Class AB Pseudo-Differential CMOS Squarer Circuit **2007**,		2
25	Low-voltage, low-power rail-to-rail two stage op-amp with dynamic biasing and no Miller compensation. *Midwest Symposium on Circuits and Systems*, **2007**,	1	2
24	Single Transistor High-Impedance Tail Current Source With Extended Common-Mode Input Range and Reduced Supply Requirements. *IEEE Transactions on Circuits and Systems Part 2: Express Briefs*, **2007**, 54, 581-585		3
23	A High-Swing, High-Speed CMOS WTA Using Differential Flipped Voltage Followers. *IEEE Transactions on Circuits and Systems II: Express Briefs*, **2007**, 54, 668-672	3.5	16
22	Versatile multidecade CMOS voltage controlled oscillator with accurate amplitude and PWM control. *Midwest Symposium on Circuits and Systems*, **2007**,	1	1
21	A Very Low-Power Class AB/AB Op-amp based Sigma-Delta Modulator for Biomedical Applications. *Midwest Symposium on Circuits and Systems*, **2006**,	1	1
20	A low-voltage low-power QFG-based Sigma-Delta modulator for electroencephalogram applications **2006**,		4
19	New Gain Programmable Current Mirrors Based on Current Steering. *Midwest Symposium on Circuits and Systems*, **2006**,	1	1
18	Low-Voltage Universal Cell (LVUC): A Compact Analog/Digital Logic Block for Mixed Signal FPGAs **2006**,		1
17	Winner-Take-All Class AB Input Stage. *Analog Integrated Circuits and Signal Processing*, **2006**, 46, 149-152	1.2	5
16	Low-Voltage Super class AB CMOS OTA cells with very high slew rate and power efficiency. *IEEE Journal of Solid-State Circuits*, **2005**, 40, 1068-1077	5.5	169
15	A CMOS transconductor with multidecade tuning using balanced current scaling in moderate inversion. *IEEE Journal of Solid-State Circuits*, **2005**, 40, 1078-1083	5.5	41
14	New low-Voltage fully programmable CMOS triangular/trapezoidal function Generator circuit. *IEEE Transactions on Circuits and Systems Part 1: Regular Papers*, **2005**, 52, 2033-2042		19
13	A proposal for high-performance CCII-based analogue CMOS design. *International Journal of Circuit Theory and Applications*, **2005**, 33, 379-391	2	14
12	Novel Architectures of Class AB CMOS Mirrors with Programmable Gain. *Analog Integrated Circuits and Signal Processing*, **2005**, 42, 197-202	1.2	7
11	1.5-V current-mode CMOS true RMS-DC converter based on class-AB transconductors. *IEEE Transactions on Circuits and Systems Part 2: Express Briefs*, **2005**, 52, 376-379		23
10	Very Low Voltage MOS Translinear Loops Based on Flipped Voltage Followers. *Analog Integrated Circuits and Signal Processing*, **2004**, 40, 71-74	1.2	12
9	New compact CMOS continuous-time low-Voltage analog rank-order filter architecture. *IEEE Transactions on Circuits and Systems Part 2: Express Briefs*, **2004**, 51, 257-261		12

8	A fully parallel CMOS analog median filter. *IEEE Transactions on Circuits and Systems Part 2: Express Briefs*, **2004**, 51, 116-123		12
7	Biasing CMOS amplifiers using MOS transistors in subthreshold region. *IEICE Electronics Express*, **2004**, 1, 339-345	0.5	30
6	Analog Adaptive Median Filters. *Analog Integrated Circuits and Signal Processing*, **2003**, 36, 207-213	1.2	3
5	Low-Voltage Analog Circuits Based on Wideband Capacitive Coupling. *Analog Integrated Circuits and Signal Processing*, **2003**, 37, 253-257	1.2	3
4	Very Low Voltage Rail-to-Rail Programmable-Gain CMOS Amplifier. *Analog Integrated Circuits and Signal Processing*, **2003**, 37, 269-273	1.2	4
3	Innovative Built-In Self-Test Schemes for On-Chip Diagnosis, Compliant with the IEEE 1149.4 Mixed-Signal Test Bus Standard. *Journal of Electronic Testing: Theory and Applications (JETTA)*, **2003**, 19, 21-28	0.7	3
2	A CMOS Four Quadrant Current/Transconductance Multiplier. *Analog Integrated Circuits and Signal Processing*, **1999**, 19, 163-168	1.2	3
1	Low-Voltage High-Frequency Continuous-Time Filters Based on Simple Transconductors and Miller Integrators. *Analog Integrated Circuits and Signal Processing*, **1997**, 13, 295-301	1.2	0