

Jaime Ramirez-Angulo

List of Publications by Citations

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The third column is the impact factor (IF) of the journal, and the fourth column is the number of citations of the article.

115
papers

1,120
citations

17
h-index

28
g-index

140
ext. papers

1,400
ext. citations

2
avg, IF

4.37
L-index

#	Paper	IF	Citations
115	Low-Voltage Super class AB CMOS OTA cells with very high slew rate and power efficiency. <i>IEEE Journal of Solid-State Circuits</i> , 2005 , 40, 1068-1077	5.5	169
114	Super Class-AB OTAs With Adaptive Biasing and Dynamic Output Current Scaling. <i>IEEE Transactions on Circuits and Systems Part 1: Regular Papers</i> , 2007 , 54, 449-457		69
113	Highly Linear Tunable CMOS G_m Low-Pass Filter. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , 2009 , 56, 2145-2158	3.9	51
112	Using Floating Gate and Quasi-Floating Gate Techniques for Rail-to-Rail Tunable CMOS Transconductor Design. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , 2011 , 58, 1604-1614	3.9	41
111	A CMOS transconductor with multidecade tuning using balanced current scaling in moderate inversion. <i>IEEE Journal of Solid-State Circuits</i> , 2005 , 40, 1078-1083	5.5	41
110	Super Class-AB Recycling Folded Cascode OTA. <i>IEEE Journal of Solid-State Circuits</i> , 2018 , 53, 2614-2623	5.5	37
109	CMOS Transconductors With Continuous Tuning Using FG MOS Balanced Output Current Scaling. <i>IEEE Journal of Solid-State Circuits</i> , 2008 , 43, 1313-1323	5.5	36
108	Biasing CMOS amplifiers using MOS transistors in subthreshold region. <i>IEICE Electronics Express</i> , 2004 , 1, 339-345	0.5	30
107	Power-efficient analog design based on the class AB super source follower. <i>International Journal of Circuit Theory and Applications</i> , 2012 , 40, 1143-1163	2	28
106	. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , 2013 , 60, 1300-1309	3.9	26
105	1.5-V current-mode CMOS true RMS-DC converter based on class-AB transconductors. <i>IEEE Transactions on Circuits and Systems Part 2: Express Briefs</i> , 2005 , 52, 376-379		23
104	Enhanced Single-Stage Folded Cascode OTA Suitable for Large Capacitive Loads. <i>IEEE Transactions on Circuits and Systems II: Express Briefs</i> , 2018 , 65, 441-445	3.5	19
103	New low-Voltage fully programmable CMOS triangular/trapezoidal function Generator circuit. <i>IEEE Transactions on Circuits and Systems Part 1: Regular Papers</i> , 2005 , 52, 2033-2042		19
102	A Highly Efficient Composite Class-AB Miller Op-Amp With High Gain and Stable From 15 pF Up To Very Large Capacitive Loads. <i>IEEE Transactions on Very Large Scale Integration (VLSI) Systems</i> , 2018 , 26, 2061-2072	2.6	19
101	Super class AB OTA without open-loop gain degradation based on dynamic cascode biasing. <i>International Journal of Circuit Theory and Applications</i> , 2017 , 45, 2111-2118	2	18
100	Three novel improved CMOS C-multipliers. <i>International Journal of Circuit Theory and Applications</i> , 2012 , 40, 607-616	2	18
99	A 1.2-V 450- μ W G_m - C Bluetooth Channel Filter Using a Novel Gain-Boosted Tunable Transconductor. <i>IEEE Transactions on Very Large Scale Integration (VLSI) Systems</i> , 2015 , 23, 1572-1576	2.6	17

98	Micropower Class-AB VGA With Gain-Independent Bandwidth. <i>IEEE Transactions on Circuits and Systems II: Express Briefs</i> , 2013 , 60, 397-401	3.5	17
97	A High-Swing, High-Speed CMOS WTA Using Differential Flipped Voltage Followers. <i>IEEE Transactions on Circuits and Systems II: Express Briefs</i> , 2007 , 54, 668-672	3.5	16
96	Micropower high current-drive class AB CMOS current-feedback operational amplifier. <i>International Journal of Circuit Theory and Applications</i> , 2011 , 39, 893-903	2	14
95	A proposal for high-performance CCI-based analogue CMOS design. <i>International Journal of Circuit Theory and Applications</i> , 2005 , 33, 379-391	2	14
94	Low-power CMOS variable gain amplifier based on a novel tunable transconductor. <i>IET Circuits, Devices and Systems</i> , 2015 , 9, 105-110	1.1	13
93	Highly linear micropower class AB current mirrors using Quasi-Floating Gate transistors. <i>Microelectronics Journal</i> , 2014 , 45, 1261-1267	1.8	12
92	Highly Linear V/I Converter with Programmable Current Mirrors 2007 ,		12
91	Very Low Voltage MOS Translinear Loops Based on Flipped Voltage Followers. <i>Analog Integrated Circuits and Signal Processing</i> , 2004 , 40, 71-74	1.2	12
90	New compact CMOS continuous-time low-Voltage analog rank-order filter architecture. <i>IEEE Transactions on Circuits and Systems Part 2: Express Briefs</i> , 2004 , 51, 257-261		12
89	A fully parallel CMOS analog median filter. <i>IEEE Transactions on Circuits and Systems Part 2: Express Briefs</i> , 2004 , 51, 116-123		12
88	0.5 V 15 μ W Recycling Folded Cascode Amplifier With 34767 MHz μ F/mA FOM. <i>IEEE Solid-State Circuits Letters</i> , 2018 , 1, 170-173	2	12
87	Power Efficient Class AB Op-Amps With High and Symmetrical Slew Rate. <i>IEEE Transactions on Very Large Scale Integration (VLSI) Systems</i> , 2014 , 22, 943-947	2.6	11
86	Current-mode CMOS multiplier/divider circuit operating in linear/saturation regions. <i>Analog Integrated Circuits and Signal Processing</i> , 2011 , 66, 299-302	1.2	11
85	Free class AB Miller opamp with high current enhancement. <i>Electronics Letters</i> , 2015 , 51, 215-217	1.1	10
84	Pseudo-Three-Stage Miller Op-Amp With Enhanced Small-Signal and Large-Signal Performance. <i>IEEE Transactions on Very Large Scale Integration (VLSI) Systems</i> , 2019 , 27, 2246-2259	2.6	9
83	Analysis, Comparison, and Experimental Validation of a Class AB Voltage Follower With Enhanced Bandwidth and Slew Rate. <i>IEEE Transactions on Very Large Scale Integration (VLSI) Systems</i> , 2019 , 27, 1353-1364	2.6	8
82	High current efficiency class-AB OTA with high open loop gain and enhanced bandwidth. <i>IEICE Electronics Express</i> , 2017 , 14, 20170719-20170719	0.5	8
81	Bandwidth-Enhanced High Current Efficiency Class-AB Buffer With Very Low Output Resistance. <i>IEEE Transactions on Circuits and Systems II: Express Briefs</i> , 2018 , 65, 1544-1548	3.5	8

80	Design of micropower class AB transconductors: A systematic approach. <i>Microelectronics Journal</i> , 2013 , 44, 920-929	1.8	8
79	Compact low-voltage CMOS current-mode multiplier/divider 2010 ,		8
78	Low Voltage Differential Input Stage With Improved CMRR and True Rail-to-Rail Common Mode Input Range. <i>IEEE Transactions on Circuits and Systems II: Express Briefs</i> , 2008 , 55, 1229-1233	3.5	8
77	Class AB amplifier with enhanced slew rate and GBW. <i>International Journal of Circuit Theory and Applications</i> , 2019 , 47, 1199	2	7
76	360 nW Gate-Driven Ultra-Low Voltage CMOS Linear Transconductor With 1 MHz Bandwidth and Wide Input Range. <i>IEEE Transactions on Circuits and Systems II: Express Briefs</i> , 2020 , 67, 2332-2336	3.5	7
75	An Amplified Offset Compensation Scheme and Its Application in a Track and Hold Circuit. <i>IEEE Transactions on Circuits and Systems II: Express Briefs</i> , 2018 , 65, 416-420	3.5	7
74	±0.18-V supply voltage gate-driven PGA with 0.7-Hz to 2-kHz constant bandwidth and 0.15- μ W power dissipation. <i>International Journal of Circuit Theory and Applications</i> , 2018 , 46, 272-279	2	7
73	Design of Two-Stage Class AB CMOS Buffers: A Systematic Approach. <i>ETRI Journal</i> , 2011 , 33, 393-400	1.4	7
72	Novel Architectures of Class AB CMOS Mirrors with Programmable Gain. <i>Analog Integrated Circuits and Signal Processing</i> , 2005 , 42, 197-202	1.2	7
71	A compact four quadrant CMOS analog multiplier. <i>AEU - International Journal of Electronics and Communications</i> , 2019 , 108, 53-61	2.8	6
70	A tunable highly linear CMOS transconductor with 80 dB of SFDR. <i>The Integration VLSI Journal</i> , 2009 , 42, 277-285	1.4	6
69	A CMOS linear tunable transconductor for continuous-time tunable Gm-C filters 2008 ,		6
68	Micropower class AB voltage followers with simple quiescent current control 2012 ,		5
67	Class-AB Fully Differential Voltage Followers. <i>IEEE Transactions on Circuits and Systems II: Express Briefs</i> , 2008 , 55, 131-135	3.5	5
66	Comparison of programmable linear resistors based on quasi-floating gate MOSFETs 2008 ,		5
65	Winner-Take-All Class AB Input Stage. <i>Analog Integrated Circuits and Signal Processing</i> , 2006 , 46, 149-152	1.2	5
64	Class AB flipped voltage follower with very low output resistance and no additional power. <i>IEICE Electronics Express</i> , 2018 , 15, 201711170-201711170	0.5	5
63	On the Optimal Current Followers for Wide-Swing Current-Efficient Amplifiers 2018 ,		4

62	Highly linear wide-swing continuous tuning of CMOS transconductors. <i>International Journal of Circuit Theory and Applications</i> , 2014 , 42, 831-841	2	4
61	Three novel improved CMOS capacitance scaling schemes 2010 ,		4
60	Low-Voltage Tunable Pseudo-Differential Transconductor with High Linearity. <i>ETRI Journal</i> , 2009 , 31, 576-584	1.4	4
59	High slew rate two stage A/AB and AB/AB op-amps with phase lead compensation at output node and local common mode feedback 2008 ,		4
58	A Very Linear OTA with V-I Conversion based on Quasi-Floating MOS Resistor 2007 ,		4
57	A low-voltage low-power QFG-based Sigma-Delta modulator for electroencephalogram applications 2006 ,		4
56	Very Low Voltage Rail-to-Rail Programmable-Gain CMOS Amplifier. <i>Analog Integrated Circuits and Signal Processing</i> , 2003 , 37, 269-273	1.2	4
55	The Flipped Voltage Follower: Theory and Applications. <i>Lecture Notes in Electrical Engineering</i> , 2013 , 269-287	0.2	4
54	Low-Voltage 0.81mW, 1B2 CMOS VGA With 5% Bandwidth Variations and 88dB DC Rejection. <i>IEEE Access</i> , 2020 , 8, 106310-106321	3.5	4
53	A Simple Miller Compensation With Essential Bandwidth Improvement. <i>IEEE Transactions on Very Large Scale Integration (VLSI) Systems</i> , 2017 , 25, 3186-3192	2.6	3
52	Tunable rail-to-rail FG MOS transconductor 2010 ,		3
51	Class AB CMOS tunable transconductor 2010 ,		3
50	Low-voltage gm-enhanced CMOS differential pairs using positive feedback 2010 ,		3
49	Single Transistor High-Impedance Tail Current Source With Extended Common-Mode Input Range and Reduced Supply Requirements. <i>IEEE Transactions on Circuits and Systems Part 2: Express Briefs</i> , 2007 , 54, 581-585		3
48	Analog Adaptive Median Filters. <i>Analog Integrated Circuits and Signal Processing</i> , 2003 , 36, 207-213	1.2	3
47	Low-Voltage Analog Circuits Based on Wideband Capacitive Coupling. <i>Analog Integrated Circuits and Signal Processing</i> , 2003 , 37, 253-257	1.2	3
46	Innovative Built-In Self-Test Schemes for On-Chip Diagnosis, Compliant with the IEEE 1149.4 Mixed-Signal Test Bus Standard. <i>Journal of Electronic Testing: Theory and Applications (JETTA)</i> , 2003 , 19, 21-28	0.7	3
45	A CMOS Four Quadrant Current/Transconductance Multiplier. <i>Analog Integrated Circuits and Signal Processing</i> , 1999 , 19, 163-168	1.2	3

44	Power Efficient Simple Technique to Convert a Reset-and-Hold Into a True-Sample-and-Hold Using an Auxiliary Output Stage. <i>IEEE Access</i> , 2020 , 8, 66508-66516	3.5	3
43	CMOS First-Order All-Pass Filter With 2-Hz Pole Frequency. <i>IEEE Transactions on Very Large Scale Integration (VLSI) Systems</i> , 2019 , 27, 294-303	2.6	3
42	±0.25-V Class-AB CMOS Capacitance Multiplier and Precision Rectifiers. <i>IEEE Transactions on Very Large Scale Integration (VLSI) Systems</i> , 2019 , 27, 830-842	2.6	3
41	Gain-Boosted Super Class AB OTAs Based on Nested Local Feedback. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , 2021 , 68, 3562-3573	3.9	3
40	An Op-Amp Approach for Bandpass VGAs With Constant Bandwidth. <i>IEEE Transactions on Circuits and Systems II: Express Briefs</i> , 2018 , 65, 1144-1148	3.5	2
39	Folded Cascode OTA with 5540 MHzpF/mA FoM 2018 ,		2
38	Improved technique for continuous tuning of CMOS transconductor 2013 ,		2
37	A super class-AB OTA with high output current and no open loop gain degradation 2017 ,		2
36	Highly accurate CMOS second generation current conveyor and transconductor 2015 ,		2
35	Energy harvesting microsystems based on the QFG MOS transistors 2015 ,		2
34	Realistic model for the multiple-input floating-gate transistor. <i>IEEJ Transactions on Electrical and Electronic Engineering</i> , 2014 , 9, 692-694	1	2
33	Noise margin and short-circuit current in FGMOS logics. <i>IEICE Electronics Express</i> , 2011 , 8, 1967-1971	0.5	2
32	200 mW CMOS class AB unity-gain buffers with accurate quiescent current control 2010 ,		2
31	Simple improvement stage for low voltage WTA and Rank Order circuits 2011 ,		2
30	Versatile multi-decade CMOS voltage-controlled oscillator with accurate amplitude and pulse width control. <i>Analog Integrated Circuits and Signal Processing</i> , 2009 , 60, 83-92	1.2	2
29	Low-Voltage MOS Translinear Analog Signal Processing. <i>Circuits, Systems, and Signal Processing</i> , 2009 , 28, 795-804	2.2	2
28	Micropower class AB CMOS current conveyor based on quasi-floating gate techniques 2009 ,		2
27	Linear-enhanced V to I converters based on MOS resistive source degeneration 2008 ,		2

26	A 72 dB @ 2 MHz IM3 CMOS tunable pseudo-differential transconductor 2008 ,		2
25	A power efficient and simple scheme for dynamically biasing cascode amplifiers and telescopic op-amps. <i>The Integration VLSI Journal</i> , 2008 , 41, 539-543	1.4	2
24	Class AB Pseudo-Differential CMOS Squarer Circuit 2007 ,		2
23	Low-voltage, low-power rail-to-rail two stage op-amp with dynamic biasing and no Miller compensation. <i>Midwest Symposium on Circuits and Systems</i> , 2007 ,	1	2
22	Gain and Bandwidth Enhanced Class-AB OTAs 2019 ,		2
21	Super-Gain-Boosted AB-AB Fully Differential Miller Op-Amp With 156dB Open-Loop Gain and 174MV/V MHZ pF/W Figure of Merit in 130nm CMOS Technology. <i>IEEE Access</i> , 2021 , 1-1	3.5	2
20	Ultra-Low Power Subthreshold Quasi Floating Gate CMOS Logic Family for Energy Harvesting 2018 ,		2
19	Rail to rail CMOS complementary input stage with only one active differential pair at a time. <i>IEICE Electronics Express</i> , 2014 , 11, 20140392-20140392	0.5	1
18	FGMOS flip-flop for low-power signal processing. <i>International Journal of Electronics</i> , 2013 , 100, 1683-1689		1
17	Low-Power Analog Channel Selection Filtering Techniques. <i>Circuits, Systems, and Signal Processing</i> , 2017 , 36, 895-915	2.2	1
16	FVF-Based Low-Dropout Voltage Regulator with Fast Charging/Discharging Paths for Fast Line and Load Regulation. <i>ETRI Journal</i> , 2017 , 39, 373-382	1.4	1
15	On the optimal choice of the output stage in CMOS transconductors 2013 ,		1
14	Low Voltage Lazzaro's WTA with enhanced loop gain. <i>IEICE Electronics Express</i> , 2012 , 9, 648-653	0.5	1
13	2009 ,		1
12	A Very Low-Power Class AB/AB Op-amp based Sigma-Delta Modulator for Biomedical Applications. <i>Midwest Symposium on Circuits and Systems</i> , 2006 ,	1	1
11	Versatile multidecade CMOS voltage controlled oscillator with accurate amplitude and PWM control. <i>Midwest Symposium on Circuits and Systems</i> , 2007 ,	1	1
10	New Gain Programmable Current Mirrors Based on Current Steering. <i>Midwest Symposium on Circuits and Systems</i> , 2006 ,	1	1
9	Low-Voltage Universal Cell (LVUC): A Compact Analog/Digital Logic Block for Mixed Signal FPGAs 2006 ,		1

8	A Noise-Robust Positive-Feedback Floating-Gate Logic. <i>IEICE Transactions on Electronics</i> , 2016 , E99.C, 452-457	0.4	1
7	1-V 15- μ W 130-nm CMOS Super Class AB OTA 2020 ,		1
6	A High-Frequency Small-Signal Model for Four-Port Network MOSFETs 2020 ,		1
5	CMOS Analog AGC for Biomedical Applications. <i>Electronics (Switzerland)</i> , 2020 , 9, 878	2.6	0
4	Low-Voltage High-Frequency Continuous-Time Filters Based on Simple Transconductors and Miller Integrators. <i>Analog Integrated Circuits and Signal Processing</i> , 1997 , 13, 295-301	1.2	0
3	Energy-Efficient Amplifiers Based on Quasi-Floating Gate Techniques. <i>Applied Sciences (Switzerland)</i> , 2021 , 11, 3271	2.6	0
2	An Enhanced Gain-Bandwidth Class-AB Miller op-amp With 23,800 MHz μ F/mW FOM, 11-16 Current Efficiency and Wide Range of Resistive and Capacitive Loads Driving Capability. <i>IEEE Access</i> , 2021 , 9, 69783-69797	3.5	0
1	Modular Discrete and CMOS Integrated Implementations of High-Speed Analog Rank-Order Filters. <i>Circuits, Systems, and Signal Processing</i> , 2018 , 37, 5637-5646	2.2	