

# Chung-Chieh Fang

## List of Publications by Year in descending order

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28  
papers

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citations

687220

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839398

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g-index

31  
all docs

31  
docs citations

31  
times ranked

249  
citing authors

#	ARTICLE	IF	CITATIONS
1	Sampled-data modeling and analysis of one-cycle control and charge control. IEEE Transactions on Power Electronics, 2001, 16, 345-350.	5.4	28
2	Unified Discrete-Time Modeling of Buck Converter in Discontinuous Mode. IEEE Transactions on Power Electronics, 2011, 26, 2335-2342.	5.4	27
3	Sampled-data modelling and analysis of the power stage of PWM DC-DC converters. International Journal of Electronics, 2001, 88, 347-369.	0.9	26
4	Critical conditions for a class of switched linear systems based on harmonic balance: applications to DC-DC converters. Nonlinear Dynamics, 2012, 70, 1767-1789.	2.7	26
5	Subharmonic Instability Limits for the Peak-Current-Controlled Buck Converter With Closed Voltage Feedback Loop. IEEE Transactions on Power Electronics, 2015, 30, 1085-1092.	5.4	26
6	Robust Feedback Stabilization of Limit Cycles in PWM DC-DC Converters. Nonlinear Dynamics, 2002, 27, 295-309.	2.7	23
7	Subharmonic Stability Limits for the Buck Converter With Ripple-Based Constant On-Time Control and Feedback Filter. IEEE Transactions on Power Electronics, 2014, 29, 2135-2142.	5.4	22
8	Sampled-data modeling and analysis of closed-loop PWM DC-DC converters. , 0, , .		19
9	Closed-Form Critical Conditions of Subharmonic Oscillations for Buck Converters. IEEE Transactions on Circuits and Systems I: Regular Papers, 2013, 60, 1967-1974.	3.5	19
10	Subharmonic Instability Limits for the Peak-Current-Controlled Boost, Buck, Boost, Flyback, and SEPIC Converters With Closed Voltage Feedback Loop. IEEE Transactions on Power Electronics, 2017, 32, 4048-4055.	5.4	18
11	Sampled-data poles, zeros, and modeling for current-mode control. International Journal of Circuit Theory and Applications, 2013, 41, 111-127.	1.3	17
12	Instability conditions for a class of switched linear systems with switching delays based on sampled-data analysis: applications to DC-DC converters. Nonlinear Dynamics, 2014, 77, 185-208.	2.7	17
13	Closed-Form Critical Conditions of Instabilities for Constant On-Time Controlled Buck Converters. IEEE Transactions on Circuits and Systems I: Regular Papers, 2012, 59, 3090-3097.	3.5	16
14	Saddle-node bifurcation in the buck converter with constant current load. Nonlinear Dynamics, 2012, 69, 1739-1750.	2.7	15
15	Switching Frequency Determination of DC-DC Converters With Hysteretic Control. IEEE Transactions on Power Electronics, 2018, 33, 2723-2729.	5.4	11
16	Bifurcation boundary conditions for current programmed PWM DC-DC converters at light loading. International Journal of Electronics, 2012, 99, 1365-1393.	0.9	10
17	Using Nyquist or Nyquist-like plot to predict three typical instabilities in DC-DC converters. Journal of the Franklin Institute, 2013, 350, 3293-3312.	1.9	10
18	Subharmonic Instability Limits for $m$ -Controlled Buck Converter With Outer Loop Closed/Open. IEEE Transactions on Power Electronics, 2016, 31, 1657-1664.	5.4	10

#	ARTICLE	IF	CITATIONS
19	Output regulation of DC-DC switching converters using discrete-time integral control. , 1999, , .		9
20	Exact orbital stability analysis of static and dynamic ramp compensations in DC-DC converters. , 0, , .		8
21	Asymmetric Instability Conditions for Peak and Valley Current Programmed Converters at Light Loading. IEEE Transactions on Circuits and Systems I: Regular Papers, 2014, 61, 922-929.	3.5	7
22	Critical conditions of saddle-node bifurcations in switching DC-DC converters. International Journal of Electronics, 2013, 100, 1147-1174.	0.9	6
23	Exact sampled-data analysis of quasi-resonant converters with finite filter inductance and capacitance. International Journal of Circuit Theory and Applications, 2002, 30, 49-63.	1.3	5
24	Limit cycle stabilization in PWM DC-DC converters. , 0, , .		4
25	Unified subharmonic oscillation conditions for peak or average current mode control. International Journal of Circuit Theory and Applications, 2015, 43, 995-1014.	1.3	4
26	Prediction of Subharmonic Oscillation in $\frac{1}{m^2}$ Controlled Buck Converters in CCM. IEEE Transactions on Power Electronics, 2015, 30, 4035-4036.	5.4	3
27	Sampled-data poles and zeros of buck and boost converters. , 0, , .		0
28	Sampled-data modeling and analysis of one-cycle control and charge control. , 0, , .		0