

Jae-Gon Kim

List of Publications by Year in descending order

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15
papers

135
citations

1478505

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1199594

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15
all docs

15
docs citations

15
times ranked

126
citing authors

#	ARTICLE	IF	CITATIONS
1	A Study on PFâuml;IFF-Based Diagnosis Model of Plant Equipment Failure. Applied Sciences (Switzerland), 2022, 12, 347.	2.5	0
2	Minimising total tardiness for the identical parallel machine scheduling problem with splitting jobs and sequence-dependent setup times. International Journal of Production Research, 2020, 58, 1628-1643.	7.5	31
3	Minimizing Tardiness Penalty Costs in Job Shop Scheduling under Maximum Allowable Tardiness. Processes, 2020, 8, 1398.	2.8	4
4	Dominance Conditions for Optimal Order-Lot Matching in the Make-To-Order Production System. Processes, 2020, 8, 255.	2.8	1
5	Liner Ship Routing with Speed and Fleet Size Optimization. KSCE Journal of Civil Engineering, 2019, 23, 1341-1350.	1.9	6
6	Daily prediction of solar power generation based on weather forecast information in Korea. IET Renewable Power Generation, 2017, 11, 1268-1273.	3.1	39
7	Multidimensional Dynamic Programming Algorithm for<mml:math xmlns:mml="http://www.w3.org/1998/Math/MathML" id="M1"><mml:mrow><mml:mi>N</mml:mi></mml:mrow></mml:math>-Level Batching with Hierarchical Clustering Structure. Mathematical Problems in Engineering, 2017, 2017, 1-12.	1.1	1
8	Framework of Lot Order Pegging for Semiconductor Manufacturing. , 2016, , .		0
9	Lot-Order Assignment Applying Priority Rules for the Single-Machine Total Tardiness Scheduling with Nonnegative Time-Dependent Processing Times. Mathematical Problems in Engineering, 2015, 2015, 1-11.	1.1	2
10	Optimal three-level presort loading of commercial bulk mail in the postal service industry. Journal of the Operational Research Society, 2015, 66, 1007-1022.	3.4	5
11	Simultaneous order-lot pegging and wafer release planning for semiconductor wafer fabrication facilities. International Journal of Production Research, 2014, 52, 3710-3724.	7.5	4
12	Optimizing ship speed to minimize fuel consumption. Transportation Letters, 2014, 6, 109-117.	3.1	26
13	A two-phase heuristic algorithm for the fixed-charge capacitated network design problem with turn penalties. KSCE Journal of Civil Engineering, 2011, 15, 1125-1132.	1.9	3
14	Linear programming and Lagrangian relaxation heuristics for designing a material flow network on a block layout. International Journal of Production Research, 2009, 47, 5185-5202.	7.5	7
15	A three-phase heuristic algorithm for fixed-charge capacitated material flow network design with input/output points location. International Journal of Production Research, 2008, 46, 4963-4980.	7.5	6