

Anton Korniienko

List of Publications by Year in descending order

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Version: 2024-02-01

32
papers

143
citations

1478458

6
h-index

1474186

9
g-index

32
all docs

32
docs citations

32
times ranked

75
citing authors

#	ARTICLE	IF	CITATIONS
1	Distributed clock generator for synchronous SoC using ADPLL network. , 2013, , .		16
2	All-digital PLL array provides reliable distributed clock for SOCs. , 2011, , .		14
3	Frequency Design of Lossless Passive Electronic Filters: A State-Space Formulation of the Direct Synthesis Approach. IEEE Transactions on Circuits and Systems I: Regular Papers, 2021, 68, 161-174.	5.4	13
4	Systematic LFT Derivation of Uncertain Electrical Circuits for the Worst-Case Tolerance Analysis. IEEE Transactions on Electromagnetic Compatibility, 2015, 57, 937-946.	2.2	11
5	Performance control for interconnection of identical systems: Application to PLL network design. International Journal of Robust and Nonlinear Control, 2016, 26, 3-27.	3.7	11
6	Hierarchical Robust Performance Analysis of Uncertain Large Scale Systems. IEEE Transactions on Automatic Control, 2018, 63, 2075-2090.	5.7	10
7	Active vibration control in specific zones of smart structures. Control Engineering Practice, 2019, 84, 305-322.	5.5	7
8	A clock network of distributed ADPLLs using an asymmetric comparison strategy. , 2010, , .		6
9	A design approach for networks of Self-Sampled All-Digital Phase-Locked Loops. , 2011, , .		6
10	Optimal identification experiment design for the interconnection of locally controlled systems. Automatica, 2018, 89, 169-179.	5.0	6
11	Embedding of Uncertainty Propagation: Application to Hierarchical Performance Analysis. IFAC Postprint Volumes IPPV / International Federation of Automatic Control, 2013, 46, 190-195.	0.4	5
12	Convex hierarchical analysis for the performances of uncertain large-scale systems. , 2014, , .		5
13	Synchronized Interconnected ADPLLs for Distributed Clock Generation in 65 nm CMOS Technology. IEEE Transactions on Circuits and Systems II: Express Briefs, 2019, 66, 1673-1677.	3.0	5
14	Network internal signal feedback and injection: Interconnection matrix redesign. , 2013, , .		4
15	Phase IQC for the hierarchical performance analysis of uncertain large scale systems. , 2015, , .		3
16	Efficient worst-case analysis of electronic networks in intervals of frequency. International Journal of Numerical Modelling: Electronic Networks, Devices and Fields, 2018, 31, e2249.	1.9	3
17	Frequency Design of Interconnected Dissipative Systems: a Unified LMI Approach. , 2018, , .		3
18	H<sub>g&t;∞</sub> loop shaping control for distributed PLL network. , 2009, , .		2

#	ARTICLE	IF	CITATIONS
19	Robust performance analysis for a range of frequencies. , 2016, , .		2
20	Hierarchical Robust Analysis for Identified Systems in Network. IFAC-PapersOnLine, 2018, 51, 383-389.	0.9	2
21	Systematic Design Method of Passive Ladder Filters using a Generalised Variable. , 2018, , .		2
22	Identification-Based Approach for Electrical Coupling Compensation in a MEMS Gyroscope. , 2019, , .		2
23	An H_{∞} -based approach for robust sensor localization. , 2015, , .		1
24	Robust sensor localization with locally-computed, global H_{∞} -design. , 2016, , .		1
25	Stability Analysis of Time-Varying Systems with Harmonic Oscillations Using IQC Frequency Domain Multipliers. , 2019, , .		1
26	Digital Control of MEMS Gyroscopes: A Robust Approach. , 2021, , .		1
27	Parameter-dependent H_{∞} control for MEMS gyroscopes: synthesis and analysis. IFAC-PapersOnLine, 2020, 53, 7331-7337.	0.9	1
28	Performance enhancement of a closed-loop accelerometer tuned close to its instability regime. , 2009, , .		0
29	Complexity in heterogeneous systems on chips: Design and analysis challenges. , 2014, , .		0
30	LMI based approach for power flow analysis with uncertain power injection. IFAC-PapersOnLine, 2018, 51, 310-315.	0.9	0
31	Multi-variable model reduction of smart structure in active vibration control. IFAC-PapersOnLine, 2018, 51, 441-446.	0.9	0
32	On the input-output approach towards distributed estimation. , 2018, , .		0