Abhijit Karmakar

List of Publications by Year
in descending order

37 papers	362 citations	1163117 8 h-index	940533 16 g-index
38 all docs	38 docs citations	38 times ranked	258 citing authors

#	Article	IF	Citations
1	Speech Enhancement using Spectral Subtraction-type Algorithms: A Comparison and Simulation Study. Procedia Computer Science, 2015, 54, 574-584.	2.0	86
2	2D-3D CNN Based Architectures for Spectral Reconstruction from RGB Images. , 2018, , .		33
3	Design of Optimal Wavelet Packet Trees Based on Auditory Perception Criterion. IEEE Signal Processing Letters, 2007, 14, 240-243.	3.6	29
4	A Multiresolution Model of Auditory Excitation Pattern and Its Application to Objective Evaluation of Perceived Speech Quality. IEEE Transactions on Audio Speech and Language Processing, 2006, 14, 1912-1923.	3.2	21
5	An Improved Multi-Band Spectral Subtraction Algorithm for Enhancing Speech in Various Noise Environments. Procedia Engineering, 2013, 64, 312-321.	1.2	18
6	Design of an Optimal Two-Channel Orthogonal Filterbank Using Semidefinite Programming. IEEE Signal Processing Letters, 2007, 14, 692-694.	3.6	15
7	A High-Performance and Area-Efficient VLSI Architecture for the PRESENT Lightweight Cipher. , 2018, , .		15
8	Smart surveillance system for real-time multi-person multi-camera tracking at the edge. Journal of Real-Time Image Processing, 2021, 18, 1993-2007.	3.5	15
9	Hardware architectures for PRESENT block cipher and their FPGA implementations. IET Circuits, Devices and Systems, 2019, 13, 958-969.	1.4	13
10	CORDIC based implementation of Fast Fourier Transform. , 2011, , .		12
11	Platform-Based Extensible Hardware-Software Video Streaming Module for a Smart Camera System. International Journal of Modeling and Optimization, 0, , 482-487.	0.4	10
12	Single channel speech enhancement utilizing iterative processing of multi-band spectral subtraction algorithm. , 2012, , .		9
13	Efficient integration of coprocessor in LEON3 processor pipeline for System-on-Chip design. Microprocessors and Microsystems, 2017, 51, 56-75.	2.8	9
14	The spectral subtractive-type algorithms for enhancing speech in noisy environments. , 2012, , .		8
15	An Efficient VLSI Architecture for PRESENT Block Cipher and Its FPGA Implementation. Communications in Computer and Information Science, 2017, , 270-278.	0.5	7
16	End-to-end person re-identification: Real-time video surveillance over edge-cloud environment. Computers and Electrical Engineering, 2022, 99, 107824.	4.8	7
17	A perceptually motivated stationary wavelet packet filterbank using improved spectral over-subtraction for enhancement of speech in various noise environments. International Journal of Speech Technology, 2014, 17, 117-132.	2.2	6
18	Platform-Based Design Approach for Embedded Vision Applications. Journal of Image and Graphics(United Kingdom), 2013, 1, 1-6.	3.2	6

#	ARTICLE	IF	CITATIONS
19	Spectral Subtractive-Type Algorithms for Enhancement of Noisy Speech: An Integrative Review. International Journal of Image Graphics and Signal Processing, 2013, 5, 13-22.	1.2	5
20	An embedded architecture for implementation of a video acquisition module of a smart camera system. , 2012, , .		4
21	A High-Performance VLSI Architecture of the Present Cipher and its Implementations for SoCs. , 2018, , .		4
22	Unsupervised image thresholding: hardware architecture and its usage for FPGA-SoC platform. International Journal of Electronics, 2019, 106, 455-476.	1.4	4
23	Synthesis of an Optimal Wavelet Based on Auditory Perception Criterion. Eurasip Journal on Advances in Signal Processing, 2011, 2011, .	1.7	3
24	Homotopy optimisation based NMF for audio source separation. IET Signal Processing, 2018, 12, 1099-1106.	1.5	3
25	An RNS Implementation of the Elliptic Curve Cryptography for IoT Security. , 2019, , .		3
26	A Perceptually Motivated Multi-Band Spectral Subtraction Algorithm for Enhancement of Degraded Speech. , 2012, , .		2
27	An auditory perception based improved multi-band spectral subtraction algorithm for enhancement of speech degraded by non-stationary noises. , 2012, , .		2
28	Closely-Coupled Lifting Hardware for Efficient DWT Computation in an SoC. Journal of Signal Processing Systems, 2020, 92, 225-237.	2.1	2
29	A Unified Architecture for AES/PRESENT Ciphers and its Usage in an SoC Environment. , 2020, , .		2
30	Lowâ€Current Sensing Analogâ€toâ€Digital Converter with Tuneable Resolution for Biomedical Applications. IEEJ Transactions on Electrical and Electronic Engineering, 2021, 16, 1221-1228.	1.4	2
31	Neuromorphic Approach based Current Sensing Analog to Digital Converter for Biomedical Applications. , 2020, , .		2
32	Wireless Sensor Network Based Structural Health Monitoring for Multistory Building. , 2020, , .		2
33	Online Speech Enhancement by Retraining of LSTM Using SURE Loss and Policy Iteration. Neural Processing Letters, 2021, 53, 3237-3251.	3.2	1
34	Design of an Optimal Two-Channel Orthogonal Cyclic Filterbank Using Semidefinite Programming. IEEE Journal on Selected Topics in Signal Processing, 2007, 1, 633-640.	10.8	0
35	A perceptually motivated stationary wavelet packet filter-bank utilizing improved spectral over-subtraction algorithm for enhancing speech in non-stationary environments. , 2012, , .		0
36	A VLSI Architecture for the PRESENT Block Cipher with FPGA and ASIC Implementations. Communications in Computer and Information Science, 2019, , 210-220.	0.5	0

#	Article	IF	Citations
37	Efficient Closely-Coupled Integration of AES Coprocessor with LEON3 Processor. Communications in Computer and Information Science, 2019, , 345-356.	0.5	0