Hoang Anh Du Nguyen

List of Publications by Year in descending order

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2258059 2550090 15 206 3 3 citations h-index g-index papers 15 15 15 167 docs citations times ranked citing authors all docs

| # | Article | IF | CITATIONS |
|----|---|-----|-----------|
| 1 | A Survey on Memory-centric Computer Architectures. ACM Journal on Emerging Technologies in Computing Systems, 2022, 18, 1-50. | 2.3 | 7 |
| 2 | APmap: An Open-Source Compiler for Automata Processors. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 2021, , 1-1. | 2.7 | 0 |
| 3 | The Power of Computation-in-Memory Based on Memristive Devices. , 2020, , . | | 4 |
| 4 | A Classification of Memory-Centric Computing. ACM Journal on Emerging Technologies in Computing Systems, 2020, 16, 1-26. | 2.3 | 37 |
| 5 | A computation-in-memory accelerator based on resistive devices. , 2019, , . | | 3 |
| 6 | Applications of Computation-In-Memory Architectures based on Memristive Devices. , 2019, , . | | 24 |
| 7 | Memristive Device Based Circuits for Computation-in-Memory Architectures. , 2019, , . | | 15 |
| 8 | Time-division Multiplexing Automata Processor. , 2019, , . | | 8 |
| 9 | Enhanced Scouting Logic: A Robust Memristive Logic Design Scheme. , 2019, , . | | 8 |
| 10 | Memristive devices for computation-in-memory. , 2018, , . | | 29 |
| 11 | On the Implementation of Computation-in-Memory Parallel Adder. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2017, 25, 2206-2219. | 3.1 | 29 |
| 12 | On the robustness of memristor based logic gates. , 2017, , . | | 4 |
| 13 | Non-volatile look-up table based FPGA implementations. , 2016, , . | | 10 |
| 14 | Boolean logic gate exploration for memristor crossbar. , 2016, , . | | 11 |
| 15 | Computation-in-memory based parallel adder. , 2015, , . | | 17 |