

Vikas Rana

List of Publications by Year in descending order

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Version: 2024-02-01

22
papers

63
citations

1937685
4
h-index

1872680
6
g-index

22
all docs

22
docs citations

22
times ranked

55
citing authors

#	ARTICLE	IF	CITATIONS
1	A novel DC-DC switched capacitor based voltage multiplier. Analog Integrated Circuits and Signal Processing, 2022, 112, 19-28.	1.4	2
2	Adaptive Forward Body Bias Voltage Generator. , 2021, , .		0
3	Negative Voltage Generator and Current DAC Based Regulator For Flash Memory. , 2021, , .		0
4	Power Efficient Sense Amplifier For Emerging Non Volatile Memories. , 2020, , .		1
5	A 1ÅMHz PVT compensated RC oscillator with $\{8 \text{ ppm}\}^{\circ} \{C\}$ frequency stability. Analog Integrated Circuits and Signal Processing, 2020, 105, 417-428.	1.4	1
6	A 800MHz, 0.21pJ, 1.2V to 6V Level Shifter Using Thin Gate Oxide Devices in 65nm LSTP. , 2020, , .		0
7	Current DAC Based -40dB PSRR Configurable Output LDO in BCD Technology. , 2019, , .		0
8	2-Mb Embedded Phase Change Memory With 16-ns Read Access Time and 5-Mb/s Write Throughput in 90-nm BCD Technology for Automotive Applications. IEEE Solid-State Circuits Letters, 2019, 2, 135-138.	2.0	5
9	2-Mb Embedded Phase Change Memory With 16-ns Read Access Time and 5-Mb/s Write Throughput in 90-nm BCD Technology for Automotive Applications. , 2019, , .		6
10	CMOS Oscillator Having Stable Frequency with Process, Temperature and Voltage Variation. , 2018, , .		2
11	Stress Relaxed Multiple Output High-Voltage Level Shifter. IEEE Transactions on Circuits and Systems II: Express Briefs, 2018, 65, 176-180.	3.0	12
12	Switched Capacitor based High Positive and Negative Voltage Charge-pump using Sample and Hold technique. , 2018, , .		3
13	Switched Capacitor based Area Efficient Positive and Negative Voltage Multiplier. , 2018, , .		1
14	Row decoder for embedded Phase Change Memory using low voltage transistors. Microelectronics Journal, 2018, 81, 117-122.	2.0	5
15	A 32-KB ePCM for Real-Time Data Processing in Automotive and Smart Power Applications. IEEE Journal of Solid-State Circuits, 2018, 53, 2114-2125.	5.4	15
16	Area Efficient NMOS Based Positive and Negative Voltage Multiplier. , 2018, , .		0
17	Single charge-pump generating high positive and negative voltages driving common load. , 2017, , .		3
18	A 32KB 18ns random access time embedded PCM with enhanced program throughput for automotive and smart power applications. , 2017, , .		6

#	ARTICLE	IF	CITATIONS
19	A 10MHz, 42ppm/°C, 69µW PVT Compensated Latch Based Oscillator in BCD9S Technology for PCM. Communications in Computer and Information Science, 2017, , 631-645.	0.5	0
20	A 10MHz, 73ppm/°C, 84µW PVT Compensated Ring Oscillator. Communications in Computer and Information Science, 2017, , 144-152.	0.5	0
21	-1.1V to +1.1V 3:1 Power Switch Architecture for Controlling Body Bias of SRAM Array in 28nm UTBB CMOS FDSOI. , 2016, , .		1
22	A 100MHz, 5.6pJ EPT, 1V to 15V full swing level shifter using low voltage transistors. Analog Integrated Circuits and Signal Processing, 0, , 1.	1.4	0