

Tianyu Jia

List of Publications by Year in descending order

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12
papers

69
citations

1937685
4
h-index

2272923
4
g-index

12
all docs

12
docs citations

12
times ranked

60
citing authors

#	ARTICLE	IF	CITATIONS
1	A Dynamic Timing Enhanced DNN Accelerator With Compute-Adaptive Elastic Clock Chain Technique. IEEE Journal of Solid-State Circuits, 2021, 56, 55-65.	5.4	9
2	An Adaptive Clock Scheme Exploiting Instruction-Based Dynamic Timing Slack for a GPGPU Architecture. IEEE Journal of Solid-State Circuits, 2020, 55, 2259-2269.	5.4	5
3	31.3 A Compute-Adaptive Elastic Clock-Chain Technique with Dynamic Timing Enhancement for 2D PE-Array-Based Accelerators. , 2020, , .		8
4	An Instruction-Driven Adaptive Clock Management Through Dynamic Phase Scaling and Compiler Assistance for a Low Power Microprocessor. IEEE Journal of Solid-State Circuits, 2019, 54, 2327-2338.	5.4	10
5	19.4 An Adaptive Clock Management Scheme Exploiting Instruction-Based Dynamic Timing Slack for a General-Purpose Graphics Processor Unit with Deep Pipeline and Out-of-Order Execution. , 2019, , .		10
6	Holistic Energy Management with $\hat{1}/4$ Processor Co-Optimization in Fully Integrated Battery-Less IoTs. , 2018, , .		1
7	A Fully-integrated LC-Oscillator Based Buck Regulator with Autonomous Resonant Switching for Low-Power Applications. , 2018, , .		0
8	Compiler-guided instruction-level clock scheduling for timing speculative processors. , 2018, , .		1
9	An Instruction Driven Adaptive Clock Phase Scaling with Timing Encoding and Online Instruction Calibration for a Low Power Microprocessor. , 2018, , .		4
10	A Fully Integrated Buck Regulator With 2-GHz Resonant Switching for Low-Power Applications. IEEE Journal of Solid-State Circuits, 2018, 53, 2663-2674.	5.4	13
11	Greybox Design Methodology. , 2017, , .		8
12	(Invited) Software-guided greybox design methodology with integrated power and clock management. , 2017, , .		0