

Kiat Seng Yeo

List of Publications by Year in Descending Order

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The third column is the impact factor (IF) of the journal, and the fourth column is the number of citations of the article.

365
papers

3,697
citations

31
h-index

46
g-index

487
ext. papers

4,898
ext. citations

2.4
avg, IF

5.42
L-index

#	Paper	IF	Citations
365	Nanowatt Receiver for High-Data-Rate Advanced Internet of Things and Microwave Applications: A Novel Exploitation of Body Bias and Stage Ratios in a Dickson Detector.. <i>IEEE Nanotechnology Magazine</i> , 2022 , 2-11	1.7	
364	Current Harmonics Analysis and Design for Load-Independent ZVS Single-Switch Resonant DC/DC Converter. <i>IEEE Transactions on Power Electronics</i> , 2022 , 1-1	7.2	1
363	A Large Dynamic Range Reconfigurable Interpolation Digital Transmitter for NB-IoT Applications. <i>IEEE Microwave and Wireless Components Letters</i> , 2022 , 1-4	2.6	
362	A Monolithically Integrated Single-Input Load-Modulated Balanced Amplifier With Enhanced Efficiency at Power Back-Off. <i>IEEE Journal of Solid-State Circuits</i> , 2021 , 56, 1553-1564	5.5	5
361	Decentralized and Lightweight Approach to Detect Eclipse Attacks on Proof of Work Blockchains. <i>IEEE Transactions on Network and Service Management</i> , 2021 , 18, 1659-1672	4.8	4
360	A 60 GHz 8-Way Combined Power Amplifier in 0.18 μm SiGe BiCMOS. <i>IEEE Transactions on Circuits and Systems II: Express Briefs</i> , 2021 , 68, 1847-1851	3.5	1
359	A digital background calibration scheme for non-linearity of SAR ADC using back-propagation algorithm. <i>Microelectronics Journal</i> , 2021 , 114, 105113	1.8	1
358	Heterogeneous Integration: A Promising Technology to Future Integrated Power Conversion Electronics. <i>IEEE Power Electronics Magazine</i> , 2021 , 8, 37-47	1.5	4
357	A 4 GHz Single-to-Differential Cross-Coupled Variable-Gain Transimpedance Amplifier for Optical Communication. <i>Electronics (Switzerland)</i> , 2021 , 10, 3042	2.6	1
356	92.5% Average Power Efficiency Fully Integrated Floating Buck Quasi-Resonant LED Drivers Using GaN FETs. <i>Electronics (Switzerland)</i> , 2020 , 9, 575	2.6	2
355	Design of a Wideband Variable-Gain Amplifier With Self-Compensated Transistor for Accurate dB-Linear Characteristic in 65 nm CMOS Technology. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , 2020 , 67, 4187-4198	3.9	8
354	Robustness-oriented P-band phased array radar front-end with high phase and gain resolution in 0.18 μm BiCMOS. <i>IET Microwaves, Antennas and Propagation</i> , 2020 , 14, 960-966	1.6	0
353	A data-dependent energy reduction algorithm for SAR ADC using self-adaptive window. <i>Microelectronics Journal</i> , 2020 , 100, 104754	1.8	4
352	Ka-Band Marchand Balun with Edge- and Broadside-Coupled Hybrid Configuration. <i>Electronics (Switzerland)</i> , 2020 , 9, 1116	2.6	1
351	Design of Differential Variable-Gain Transimpedance Amplifier in 0.18 μm SiGe BiCMOS. <i>Electronics (Switzerland)</i> , 2020 , 9, 1058	2.6	3
350	Design of a Ka-Band U-Shaped Bandpass Filter with 20-GHz Bandwidth in 0.13- μm BiCMOS Technology. <i>Electronics (Switzerland)</i> , 2020 , 9, 1608	2.6	3
349	A Two-Stage X-Band 20.7-dBm Power Amplifier in 40-nm CMOS Technology. <i>Electronics (Switzerland)</i> , 2020 , 9, 2198	2.6	1

348	Design and Characterization of Micro-LED Matrix Display With Heterogeneous Integration of GaN and BCD Technologies. <i>IEEE Transactions on Electron Devices</i> , 2019 , 66, 4221-4227	2.9	12
347	. <i>IEEE Transactions on Very Large Scale Integration (VLSI) Systems</i> , 2019 , 27, 2954-2958	2.6	3
346	A Wideband dB-Linear VGA With Temperature Compensation and Active Load. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , 2019 , 66, 3279-3287	3.9	8
345	An Inductorless 6-GHz Variable Gain Differential Transimpedance Amplifier in 0.18- μ m SiGe BiCMOS 2019 ,		1
344	Design of a Voltage-Controlled Programmable-Gain Amplifier in 65-nm CMOS Technology 2019 ,		1
343	Heterogeneous Integration of GaN and BCD Technologies. <i>Electronics (Switzerland)</i> , 2019 , 8, 351	2.6	5
342	A Multi-Mode Multi-Coil Coupled Tuned Inductive Peaking ILFD for Low Injected Power With Compact Size. <i>IEEE Access</i> , 2019 , 7, 59059-59068	3.5	2
341	Design of Reconfigurable dB-Linear Variable-Gain Amplifier and Switchable-Order $g_{\{m\}}$ -C Filter in 65-nm CMOS Technology. <i>IEEE Transactions on Microwave Theory and Techniques</i> , 2019 , 67, 5148-5158 ^{4,1}		8
340	An Inductorless 5-GHz Differential Dual Regulated Cross-Cascode Transimpedance Amplifier using 40 nm CMOS 2019 ,		2
339	A high gain 60 GHz antipodal Fermi-tapered slot antenna based on robust synthesized dielectric. <i>Microwave and Optical Technology Letters</i> , 2019 , 61, 761-765	1.2	2
338	Monolithic Sub-Terahertz SPDT Switches With Low Insertion Loss and Enhanced Isolation. <i>IEEE Transactions on Terahertz Science and Technology</i> , 2018 , 8, 192-200	3.4	15
337	Design and optimization of the ring oscillator based injection locked frequency dividers. <i>Microelectronics Journal</i> , 2018 , 72, 40-48	1.8	1
336	Heterogeneous Integration of GaN LED on CMOS Driver Circuit for Mobile Phone Applications 2018 ,		3
335	A 2.4 mW 2.5 GHz multi-phase clock generator with duty cycle imbalance correction in 0.13 μ m CMOS. <i>The Integration VLSI Journal</i> , 2018 , 63, 87-92	1.4	1
334	Precompliance Test Setup for Pyroelectric Sensor Devices in IoT Applications 2018 ,		1
333	Real-Time Audio Transmission Using Visible Light Communication 2018 ,		3
332	A \mathbb{S} -Band Wide Locking Range Divide-by-4 Injection-Locked Frequency Divider. <i>IEEE Microwave and Wireless Components Letters</i> , 2018 , 28, 1020-1022	2.6	5
331	. <i>IEEE Microwave and Wireless Components Letters</i> , 2018 , 28, 1089-1091	2.6	3

330	A 45-55 GHz Vector Modulator MMIC With Built-In Voltage Converter. <i>IEEE Microwave and Wireless Components Letters</i> , 2017 , 27, 515-517	2.6	2
329	Ultra-Wideband Low-Loss Switch Design in High-Resistivity Trap-Rich SOI With Enhanced Channel Mobility. <i>IEEE Transactions on Microwave Theory and Techniques</i> , 2017 , 65, 3937-3949	4.1	22
328	Wideband millimetre-wave CMOS power amplifier using transistor-based inductive source degeneration and specially shielded transformer. <i>IET Microwaves, Antennas and Propagation</i> , 2017 , 11, 410-416	1.6	2
327	A 30-GHz Power-Efficient PLL Frequency Synthesizer for 60-GHz Applications. <i>IEEE Transactions on Microwave Theory and Techniques</i> , 2017 , 65, 4165-4175	4.1	11
326	A 60-GHz Coplanar Waveguide-Based Bidirectional LNA in SiGe BiCMOS. <i>IEEE Microwave and Wireless Components Letters</i> , 2017 , 27, 742-744	2.6	10
325	DC-30 GHz DPDT Switch Matrix Design in High Resistivity Trap-Rich SOI. <i>IEEE Transactions on Electron Devices</i> , 2017 , 64, 3548-3554	2.9	9
324	A wideband BiCMOS variable gain amplifier with novel continuous dB-linear gain control and temperature compensation. <i>Analog Integrated Circuits and Signal Processing</i> , 2017 , 90, 499-506	1.2	3
323	The Investigation and Optimisation of Phase-Induced Amplitude Attenuation in the Injection-Locked Ring Oscillators-Based Receiver. <i>Circuits, Systems, and Signal Processing</i> , 2017 , 36, 1818-1835	2.2	1
322	Review of high efficiency integrated LED lighting 2017 ,		5
321	Predistortion Linearizer for Wideband AM/PM Cancellation With Left-Handed Delay Line. <i>IEEE Microwave and Wireless Components Letters</i> , 2017 , 27, 794-796	2.6	9
320	Micro-LED arrays for display and communication: Device structure and driver architecture 2017 ,		3
319	A 57-to-64-GHz 0.094-mm ² 5-bit Passive Phase Shifter in 65-nm CMOS. <i>IEEE Transactions on Very Large Scale Integration (VLSI) Systems</i> , 2016 , 24, 1917-1925	2.6	31
318	Design of a 60-GHz Quasi-Yagi Antenna With Novel Ladder-Like Directors for Gain and Bandwidth Enhancements. <i>IEEE Antennas and Wireless Propagation Letters</i> , 2016 , 15, 682-685	3.8	21
317	A Compact High-Performance Patch Antenna Array for 60-GHz Applications. <i>IEEE Antennas and Wireless Propagation Letters</i> , 2016 , 15, 313-316	3.8	37
316	A multi-mode 30 GHz 2 degree RMS power efficient phase-locked loop frequency synthesizer 2016 ,		3
315	A Compact 57-67 GHz Bidirectional LNPA in 65-nm CMOS Technology. <i>IEEE Microwave and Wireless Components Letters</i> , 2016 , 26, 628-630	2.6	15
314	A 60-GHz bi-directional variable gain amplifier with microstrip-line interconnect in 65 nm CMOS 2016 ,		4
313	A VCO phase noise reduction technique to suppress the active device contribution 2016 ,		5

312	A 11.2 mW 48.82 GHz Low Noise Amplifier in 65 nm CMOS Technology. <i>Circuits, Systems, and Signal Processing</i> , 2016 , 35, 1531-1543	2.2	2
311	A Hybrid Pad-Line-Finger De-Embedding Technique for Broadband Modeling of CMOS Transistor. <i>IEEE Microwave and Wireless Components Letters</i> , 2016 , 26, 507-509	2.6	5
310	Millimeter-wave IC design techniques for beam-forming applications 2016 ,		1
309	DC-50 GHz low loss switch matrix design in high resistivity trap-rich SOI 2016 ,		1
308	Concentric Parallel Combining Balun for Millimeter-Wave Power Amplifier in Low-Power CMOS with High-Power Density. <i>Journal of Infrared, Millimeter, and Terahertz Waves</i> , 2016 , 37, 1096-1105	2.2	
307	Novel Defected Ground Structure and Two-Side Loading Scheme for Miniaturized Dual-Band SIW Bandpass Filter Designs. <i>IEEE Microwave and Wireless Components Letters</i> , 2015 , 25, 217-219	2.6	75
306	6.2 GHz 0.5 mW two-dimensional oscillator array-based injection-locked frequency divider in 0.18 μ m CMOS. <i>Electronics Letters</i> , 2015 , 51, 62-63	1.1	2
305	Imparting electroactivity to polycaprolactone fibers with heparin-doped polypyrrole: Modulation of hemocompatibility and inflammatory responses. <i>Acta Biomaterialia</i> , 2015 , 23, 240-249	10.8	22
304	A 3 mW 54 GHz 0.18 μ m BiCMOS voltage controlled oscillator with supply injection locking. <i>Microwave and Optical Technology Letters</i> , 2015 , 57, 1912-1914	1.2	1
303	2.3 A 130-to-180GHz 0.0035mm ² SPDT switch with 3.3dB loss and 23.7dB isolation in 65nm bulk CMOS 2015 ,		3
302	A 65 nm CMOS Power Amplifier With Peak PAE above 18.9% From 57 to 66 GHz Using Synthesized Transformer-Based Matching Network. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , 2015 , 62, 2533-2543	3.9	24
301	A 220-285 GHz SPDT Switch in 65-nm CMOS Using Switchable Resonator Concept. <i>IEEE Transactions on Terahertz Science and Technology</i> , 2015 , 5, 649-651	3.4	16
300	Low-Crosstalk Semi-Trench-Assisted Multicore Flat Fiber 2015 ,		1
299	Design of a hybrid neural spike detection algorithm for implantable integrated brain circuits 2015 ,		1
298	Modified Inductive Peaking Direct Injection ILFD With Multi-Coupled Coils. <i>IEEE Microwave and Wireless Components Letters</i> , 2015 , 25, 379-381	2.6	5
297	A Low Phase Noise and Wide Tuning Range Millimeter-Wave VCO Using Switchable Coupled VCO-Cores. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , 2015 , 62, 554-563	3.9	34
296	A study of CMOS SOI for RF, Microwave and millimeter wave applications 2015 ,		3
295	Generalized multiple coupled tanks for silicon based RF/mm-wave IC (Invited) 2015 ,		2

294	A DC-50 GHz SPDT switch with maximum insertion loss of 1.9 dB in a commercial 0.13- μm SOI technology 2015 ,		5
293	Development of a miniaturized stimulation device for electrical stimulation of cells. <i>Journal of Biological Engineering</i> , 2015 , 9, 14	6.3	18
292	A wideband BiCMOS thermal noise canceling low noise amplifier with temperature compensation. <i>Microwave and Optical Technology Letters</i> , 2015 , 57, 2121-2125	1.2	
291	Novel Q-factor enhancement technique for on-chip spiral inductors and its application to cmos low-noise amplifier designs. <i>Microwave and Optical Technology Letters</i> , 2015 , 57, 2883-2886	1.2	2
290	A 40 GHz 65 nm CMOS Phase-Locked Loop With Optimized Shunt-Peaked Buffer. <i>IEEE Microwave and Wireless Components Letters</i> , 2015 , 25, 34-36	2.6	2
289	A Reconfigurable K-/Ka-Band Power Amplifier With High PAE in 0.18- μm SiGe BiCMOS for Multi-Band Applications. <i>IEEE Transactions on Microwave Theory and Techniques</i> , 2015 , 63, 4395-4405	4.1	17
288	A 32kb 9T SRAM with PVT-tracking read margin enhancement for ultra-low voltage operation 2015 ,		2
287	A 4 GHz 60 dB Variable Gain Amplifier With Tunable DC Offset Cancellation in 65 nm CMOS. <i>IEEE Microwave and Wireless Components Letters</i> , 2015 , 25, 37-39	2.6	12
286	High-Frequency Noise Modeling of MOSFETs for Ultra Low-Voltage RF Applications. <i>IEEE Transactions on Microwave Theory and Techniques</i> , 2015 , 63, 141-154	4.1	8
285	Cell-Based Variable-Gain Amplifiers With Accurate dB-Linear Characteristic in 0.18 μm CMOS Technology. <i>IEEE Journal of Solid-State Circuits</i> , 2015 , 50, 586-596	5.5	55
284	A 26.8 dB Gain 19.7 dBm CMOS Power Amplifier Using 4-way Hybrid Coupling Combiner. <i>IEEE Microwave and Wireless Components Letters</i> , 2015 , 25, 43-45	2.6	8
283	Miniaturized 3-bit Phase Shifter for 60 GHz Phased-Array in 65 nm CMOS Technology. <i>IEEE Microwave and Wireless Components Letters</i> , 2014 , 24, 50-52	2.6	26
282	. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , 2014 , 61, 699-711	3.9	36
281	Analysis and Design of Ultra-Wideband Low-Noise Amplifier With Input/Output Bandwidth Optimization and Single-Ended/Differential-Input Reconfigurability. <i>IEEE Transactions on Industrial Electronics</i> , 2014 , 61, 5672-5680	8.9	15
280	A 35-mW 30-dB Gain Control Range Current Mode Linear-in-Decibel Programmable Gain Amplifier With Bandwidth Enhancement. <i>IEEE Transactions on Microwave Theory and Techniques</i> , 2014 , 62, 3465-3475	4.1	5
279	Miniaturized 40 μm GHz On-Chip Balun With Capacitive Loading Compensation. <i>IEEE Electron Device Letters</i> , 2014 , 35, 434-436	4.4	6
278	A 35 mW 30 dB gain control range current mode programmable gain amplifier with DC offset cancellation 2014 ,		4
277	Substrate-Induced Noise Model and Parameter Extraction for High-Frequency Noise Modeling of Sub-Micron MOSFETs. <i>IEEE Transactions on Microwave Theory and Techniques</i> , 2014 , 62, 1973-1985	4.1	5

276	0.77 fJ/bit/search Content Addressable Memory Using Small Match Line Swing and Automated Background Checking Scheme for Variation Tolerance. <i>IEEE Journal of Solid-State Circuits</i> , 2014 , 49, 1487-1498	5.5	27
275	A 24 GHz low power low phase noise dual-mode phase locked loop frequency synthesizer for 60 GHz applications 2014 ,		1
274	Coupled Dual LC Tanks Based ILFD With Low Injection Power and Compact Size. <i>IEEE Microwave and Wireless Components Letters</i> , 2014 , 24, 105-107	2.6	14
273	Internet of Things: Trends, challenges and applications 2014 ,		15
272	DESIGN AND ANALYSIS OF A 2.4 GHz HYBRID TYPE AUTOMATIC AMPLITUDE CONTROL VCO WITH FORWARD NOISE REDUCTION. <i>Journal of Circuits, Systems and Computers</i> , 2014 , 23, 1450048	0.9	
271	A 60-GHz on-chip antenna over an AMC using a standard 65-nm CMOS technology 2014 ,		2
270	Broadband 60GHz 32-way ring-cavity power combiner 2014 ,		1
269	Temperature-Compensated dB-linear Digitally Controlled Variable Gain Amplifier With DC Offset Cancellation. <i>IEEE Transactions on Microwave Theory and Techniques</i> , 2013 , 61, 2648-2661	4.1	28
268	A Compact Coupling Controllable Elliptical Filter Based on Multilayer LTCC. <i>Microwave and Optical Technology Letters</i> , 2013 , 55, 1789-1792	1.2	2
267	A 1-mW K-band gate AC-coupled VCO with 0.25 V supply voltage. <i>Analog Integrated Circuits and Signal Processing</i> , 2013 , 77, 87-91	1.2	1
266	THRU-Based Cascade De-embedding Technique for On-Wafer Characterization of RF CMOS Devices. <i>IEEE Transactions on Electron Devices</i> , 2013 , 60, 2892-2899	2.9	9
265	Improved inverter-based read-out scheme for low-power ISFET sensing array. <i>Electronics Letters</i> , 2013 , 49, 1517-1518	1.1	3
264	A 12-mW 40-60-GHz 0.18- μ m BiCMOS Oscillator-Less Self-Demodulator for Short-Range Software-Defined Transceivers. <i>IEEE Journal on Emerging and Selected Topics in Circuits and Systems</i> , 2013 , 3, 521-530	5.2	2
263	A New Millimeter-Wave Fixture Deembedding Method Based on Generalized Cascade Network Model. <i>IEEE Electron Device Letters</i> , 2013 , 34, 447-449	4.4	7
262	S-Band High-PAE Wide-Tuning-Range VCO Using Triple-Coupled LC Tanks. <i>IEEE Transactions on Circuits and Systems II: Express Briefs</i> , 2013 , 60, 736-740	3.5	18
261	Class-D Amplifier Power Stage With PWM Feedback Loop. <i>IEEE Transactions on Power Electronics</i> , 2013 , 28, 3870-3881	7.2	17
260	. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , 2013 , 60, 37-50	3.9	10
259	A 2-D Distributed Power Combining by Metamaterial-Based Zero Phase Shifter for 60-GHz Power Amplifier in 65-nm CMOS. <i>IEEE Transactions on Microwave Theory and Techniques</i> , 2013 , 61, 505-516	4.1	38

258	High-frequency low-power fractional frequency multiplier. <i>Microwave and Optical Technology Letters</i> , 2013 , 55, 146-149	1.2	
257	A Miniaturized Millimeter-Wave Standing-Wave Filtering Switch With High P1dB. <i>IEEE Transactions on Microwave Theory and Techniques</i> , 2013 , 61, 1505-1515	4.1	28
256	Design of High-Q Millimeter-Wave Oscillator by Differential Transmission Line Loaded With Metamaterial Resonator in 65-nm CMOS. <i>IEEE Transactions on Microwave Theory and Techniques</i> , 2013 , 61, 1892-1902	4.1	26
255	A Compact 2.189 GHz Self-Biased Low-Noise Amplifier in 65 nm CMOS Technology. <i>IEEE Microwave and Wireless Components Letters</i> , 2013 , 23, 662-664	2.6	15
254	Cross-Coupled Current Conveyor Based CMOS Transimpedance Amplifier for Broadband Data Transmission. <i>IEEE Transactions on Very Large Scale Integration (VLSI) Systems</i> , 2013 , 21, 1516-1525	2.6	31
253	Embedded Transformed Radial Stub Cell for BPF With Spurious-Free Above Ten Octaves. <i>IEEE Transactions on Components, Packaging and Manufacturing Technology</i> , 2013 , 3, 1597-1603	1.7	4
252	Miniaturized 60-GHz On-Chip Multimode Quasi-Elliptical Bandpass Filter. <i>IEEE Electron Device Letters</i> , 2013 , 34, 945-947	4.4	46
251	A High Speed Low Power CAM With a Parity Bit and Power-Gated ML Sensing. <i>IEEE Transactions on Very Large Scale Integration (VLSI) Systems</i> , 2013 , 21, 151-156	2.6	21
250	0.6mW 6.3GHz 40nm CMOS divide-by-2/3 prescaler using heterodyne phase-locking technique. <i>Electronics Letters</i> , 2013 , 49, 471-472	1.1	3
249	A CMOS LOW-POWER TEMPERATURE-ROBUST RSSI USING WEAK-INVERSION LIMITING AMPLIFIERS. <i>Journal of Circuits, Systems and Computers</i> , 2013 , 22, 1340034	0.9	5
248	A LOW POWER PUSH-PUSH VCO USING MULTI-COUPLED LC TANKS. <i>Journal of Circuits, Systems and Computers</i> , 2013 , 22, 1340035	0.9	
247	MODELING AND LAYOUT OPTIMIZATION TECHNIQUES FOR SILICON-BASED SYMMETRICAL SPIRAL INDUCTORS. <i>Progress in Electromagnetics Research</i> , 2013 , 143, 1-18	3.8	7
246	A new field dependent mobility model for high frequency channel thermal noise of deep submicron RFCMOS. <i>Solid-State Electronics</i> , 2012 , 68, 32-37	1.7	8
245	Impact of velocity saturation and hot carrier effects on channel thermal noise model of deep sub-micron MOSFETs. <i>Solid-State Electronics</i> , 2012 , 72, 8-11	1.7	10
244	A 0.6-V high reverse-isolation through feedback self-cancellation for single-stage noncascode CMOS LNA. <i>Microwave and Optical Technology Letters</i> , 2012 , 54, 374-379	1.2	1
243	A 1-V CMOS Ultralow-Power Receiver Front End for the IEEE 802.15.4 Standard Using Tuned Passive Mixer Output Pole. <i>International Federation for Information Processing</i> , 2012 , 1-21		2
242	DGS embedded transformed radial stub for ultra-wide stopband lowpass filter. <i>Electronics Letters</i> , 2012 , 48, 1473	1.1	8
241	Sensing Margin Enhancement Techniques for Ultra-Low-Voltage SRAMs Utilizing a Bitline-Boosting Current and Equalized Bitline Leakage. <i>IEEE Transactions on Circuits and Systems II: Express Briefs</i> , 2012 , 59, 868-872	3.5	9

240	Bidirectional Diode-Triggered Silicon-Controlled Rectifiers for Low-Voltage ESD Protection. <i>IEEE Electron Device Letters</i> , 2012 , 33, 1360-1362	4.4	18
239	Retention time characterization and optimization of logic-compatible embedded DRAM cells 2012 ,		1
238	Phase Compensation of Cascaded Conductor-Backed CPW Periodic Cells. <i>IEEE Transactions on Components, Packaging and Manufacturing Technology</i> , 2012 , 2, 1455-1464	1.7	
237	Design of quarter-wavelength resonator filters with coupling controllable paths 2012 ,		1
236	A Low-Power Single-Phase Clock Multiband Flexible Divider. <i>IEEE Transactions on Very Large Scale Integration (VLSI) Systems</i> , 2012 , 20, 376-380	2.6	23
235	A ROBUST 900 MHz RFID READER CHIP WITH RC-CALIBRATION. <i>Journal of Circuits, Systems and Computers</i> , 2012 , 21, 1240021	0.9	
234	Design of a Ku-band Low-Phase-Noise VCO Using the Dual $\$LC\$$ Tanks. <i>IEEE Transactions on Circuits and Systems II: Express Briefs</i> , 2012 , 59, 262-266	3.5	23
233	A Dual-Loop Clock and Data Recovery Circuit With Compact Quarter-Rate CMOS Linear Phase Detector. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , 2012 , 59, 1156-1167	3.9	12
232	Ultra-wide rejection band lowpass cell. <i>Electronics Letters</i> , 2012 , 48, 99	1.1	7
231	A 60GHz on-chip antenna in standard CMOS silicon Technology 2012 ,		9
230	A 7.9-mW 5.6-GHz Digitally Controlled Variable Gain Amplifier With Linearization. <i>IEEE Transactions on Microwave Theory and Techniques</i> , 2012 , 60, 3482-3490	4.1	11
229	MOSFET Drain Current Noise Modeling With Effective Gate Overdrive and Junction Noise. <i>IEEE Electron Device Letters</i> , 2012 , 33, 1117-1119	4.4	8
228	A low power low phase noise dual-band multiphase VCO. <i>Microelectronics Journal</i> , 2012 , 43, 1016-1022	1.8	4
227	Designs of a free-space white-LED mass-storage transceiver for SD-card file transfer 2012 ,		2
226	Design Exploration of Hybrid CMOS and Memristor Circuit by New Modified Nodal Analysis. <i>IEEE Transactions on Very Large Scale Integration (VLSI) Systems</i> , 2012 , 20, 1012-1025	2.6	56
225	A 60GHz VCO with 25.8% tuning range by switching return-path in 65nm CMOS 2012 ,		5
224	A 1.2 V 2.4 GHz low spur CMOS PLL synthesizer with a gain boosted charge pump for a batteryless transceiver 2012 ,		3
223	Transformer based multiple coupled LC tanks for on-chip VCO design and applications 2012 ,		1

222	A low power millimetre-wave VCO in 0.18 μ m SiGe BiCMOS technology 2012 ,		2
221	A 96 \times 6 1V ultra-low power CMOS image sensor for biomedical application 2012 ,		1
220	A 160 nW 25 kS/s 9-bit SAR ADC for neural signal recording applications 2012 ,		8
219	A COMPACT MULTIMODE BANDPASS FILTER WITH EXTENDED STOPBAND BANDWIDTH. <i>Progress in Electromagnetics Research Letters</i> , 2012 , 32, 177-186	0.5	
218	RADIAL LOADED TRANSFORMED RADIAL STUB FOR LPF STOPBAND EXTENSION. <i>Progress in Electromagnetics Research Letters</i> , 2012 , 30, 125-132	0.5	8
217	Research and development of microwave & millimeter-wave technology in singapore 2012 ,		2
216	High-speed CMOS image sensor for high-throughput lensless microfluidic imaging system 2012 ,		6
215	A Cross-Coupled LPF Topology and Design for Millimeter-Wave RFIC Applications. <i>IEEE Transactions on Electron Devices</i> , 2012 , 59, 2902-2909	2.9	7
214	Transformed radial stub cell embedded resonator for high performance filter applications 2012 ,		1
213	A 1.8-V 3.6-mW 2.4-GHz Fully Integrated CMOS Frequency Synthesizer for the IEEE 802.15.4. <i>International Federation for Information Processing</i> , 2012 , 69-99		
212	Bandwidth-Related Optimization in High-Speed Frequency Dividers using SiGe Technology. <i>Journal of Semiconductor Technology and Science</i> , 2012 , 12, 107-116	1.5	0
211	Ultra-low power series input resonance differential common gate LNA. <i>Electronics Letters</i> , 2011 , 47, 703-704	1.1	9
210	Ultra low-power high-speed flexible Probabilistic Adder for Error-Tolerant Applications 2011 ,		13
209	Ultra low power active 60 GHz Bi-CMOS down-conversion mixer 2011 ,		1
208	SiGe BiCMOS power amplifiers for 60GHz ISM band applications 2011 ,		1
207	A divide-by-two injection-locked frequency divider with 13-GHz locking range in 0.18- μ m CMOS technology 2011 ,		1
206	. <i>IEEE Transactions on Circuits and Systems II: Express Briefs</i> , 2011 , 58, 729-733	3.5	4
205	An 8T Differential SRAM With Improved Noise Margin for Bit-Interleaving in 65 nm CMOS. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , 2011 , 58, 1252-1263	3.9	56

204	Design and Sensitivity Analysis of a New Current-Mode Sense Amplifier for Low-Power SRAM. <i>IEEE Transactions on Very Large Scale Integration (VLSI) Systems</i> , 2011 , 19, 196-204	2.6	23
203	New Ultra-Wide Stopband Low-Pass Filter Using Transformed Radial Stubs. <i>IEEE Transactions on Microwave Theory and Techniques</i> , 2011 , 59, 604-611	4.1	90
202	An Accurate Two-Port De-Embedding Technique for RF/Millimeter-Wave Noise Characterization and Modeling of Deep Submicrometer Transistors. <i>IEEE Transactions on Microwave Theory and Techniques</i> , 2011 , 59, 479-487	4.1	8
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22	1.5 V high-speed electrostatic discharge free BiCMOS digital circuit. <i>Electronics Letters</i> , 1998 , 34, 1306	1.1	1
21	Optimised 0.25 [μm] high performance retrograde well pMOS device for low-power applications. <i>Electronics Letters</i> , 1998 , 34, 1702	1.1	1
20	A precise transient model for delayed input BiCMOS digital circuits. <i>International Journal of Electronics</i> , 1997 , 83, 441-454	1.2	
19	Delay time sensitivity analysis of multi-generation BiCMOS digital circuits. <i>IET Circuits, Devices and Systems</i> , 1997 , 144, 60		1
18	Experimentally-based analytical model of deep-submicron LDD pMOSFETs in a Bi-MOS hybrid-mode environment. <i>IEEE Transactions on Electron Devices</i> , 1997 , 44, 1473-1482	2.9	20
17	Novel low-voltage BiCMOS digital circuits employing a lateral p-n-p BJT in a p-MOS structure. <i>IET Circuits, Devices and Systems</i> , 1996 , 143, 83		2
16	1.1 V Full-swing double bootstrapped BiCMOS logic gates. <i>IET Circuits, Devices and Systems</i> , 1996 , 143, 41		7
15	New complementary BiCMOS digital gates for low-voltage environments. <i>Solid-State Electronics</i> , 1996 , 39, 681-687	1.7	4
14	BiCMOS circuit optimisation technique linking channel width of MOS device to collector design of BJT. <i>Electronics Letters</i> , 1996 , 32, 2300	1.1	
13	1.5 V high speed low power CMOS current sense amplifier. <i>Electronics Letters</i> , 1995 , 31, 1991-1993	1.1	4
12	A PC-based MPEG compressed data decoder. <i>IEEE Transactions on Consumer Electronics</i> , 1995 , 41, 1169-1183	1.8	
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10	A low-power static dual edge-triggered flip-flop using an output-controlled discharge configuration		13
9	A novel methodology for the design of LC tank VCO with low phase noise		7
8	Equivalent circuit model of on-wafer interconnects for CMOS RFICs		3
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6	A broadband CMOS LNA for WLAN applications	6
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