

# Kiat Seng Yeo

## List of Publications by Citations

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The third column is the impact factor (IF) of the journal, and the fourth column is the number of citations of the article.

365  
papers

3,697  
citations

31  
h-index

46  
g-index

487  
ext. papers

4,898  
ext. citations

2.4  
avg, IF

5.42  
L-index

#	Paper	IF	Citations
365	Design of Low-Power High-Speed Truncation-Error-Tolerant Adder and Its Application in Digital Signal Processing. <i>IEEE Transactions on Very Large Scale Integration (VLSI) Systems</i> , <b>2010</b> , 18, 1225-1229	2.6	171
364	A compact size coupling controllable filter with separate electric and magnetic coupling paths. <i>IEEE Transactions on Microwave Theory and Techniques</i> , <b>2006</b> , 54, 1113-1119	4.1	126
363	New Ultra-Wide Stopband Low-Pass Filter Using Transformed Radial Stubs. <i>IEEE Transactions on Microwave Theory and Techniques</i> , <b>2011</b> , 59, 604-611	4.1	90
362	A novel CMOS low-noise amplifier design for 3.1- to 10.6-GHz ultra-wide-band wireless receivers. <i>IEEE Transactions on Circuits and Systems Part 1: Regular Papers</i> , <b>2006</b> , 53, 1683-1692		83
361	Novel Defected Ground Structure and Two-Side Loading Scheme for Miniaturized Dual-Band SIW Bandpass Filter Designs. <i>IEEE Microwave and Wireless Components Letters</i> , <b>2015</b> , 25, 217-219	2.6	75
360	Enhanced low-power high-speed adder for error-tolerant application <b>2010</b> ,		70
359	Broad-Band Design Techniques for Transimpedance Amplifiers. <i>IEEE Transactions on Circuits and Systems Part 1: Regular Papers</i> , <b>2007</b> , 54, 590-600		64
358	Design Exploration of Hybrid CMOS and Memristor Circuit by New Modified Nodal Analysis. <i>IEEE Transactions on Very Large Scale Integration (VLSI) Systems</i> , <b>2012</b> , 20, 1012-1025	2.6	56
357	An 8T Differential SRAM With Improved Noise Margin for Bit-Interleaving in 65 nm CMOS. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , <b>2011</b> , 58, 1252-1263	3.9	56
356	Cell-Based Variable-Gain Amplifiers With Accurate dB-Linear Characteristic in 0.18 $\mu\text{m}$ CMOS Technology. <i>IEEE Journal of Solid-State Circuits</i> , <b>2015</b> , 50, 586-596	5.5	55
355	Compact UWB Bandpass Filter With Ultra Narrow Notched Band. <i>IEEE Microwave and Wireless Components Letters</i> , <b>2010</b> , 20, 145-147	2.6	55
354	A Wideband Low Power Low-Noise Amplifier in CMOS Technology. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , <b>2010</b> , 57, 773-782	3.9	52
353	Design of a CMOS Broadband Transimpedance Amplifier With Active Feedback. <i>IEEE Transactions on Very Large Scale Integration (VLSI) Systems</i> , <b>2010</b> , 18, 461-472	2.6	52
352	Design and Optimization of the Extended True Single-Phase Clock-Based Prescaler. <i>IEEE Transactions on Microwave Theory and Techniques</i> , <b>2006</b> , 54, 3828-3835	4.1	48
351	Miniaturized 60-GHz On-Chip Multimode Quasi-Elliptical Bandpass Filter. <i>IEEE Electron Device Letters</i> , <b>2013</b> , 34, 945-947	4.4	46
350	Compact Dual-Band Bandpass Filters Using Novel Embedded Spiral Resonator (ESR). <i>IEEE Microwave and Wireless Components Letters</i> , <b>2010</b> , 20, 435-437	2.6	45
349	A Subthreshold Low-Noise Amplifier Optimized for Ultra-Low-Power Applications in the ISM Band. <i>IEEE Transactions on Microwave Theory and Techniques</i> , <b>2008</b> , 56, 286-292	4.1	44

348	A Wideband and High Rejection Multimode Bandpass Filter Using Stub Perturbation. <i>IEEE Microwave and Wireless Components Letters</i> , <b>2009</b> , 19, 24-26	2.6	42
347	Compact Ultra-Wideband (UWB) Bandpass Filter With Ultra-Narrow Dual- and Quad-Notched Bands. <i>IEEE Transactions on Microwave Theory and Techniques</i> , <b>2011</b> , 59, 1509-1519	4.1	40
346	RF CMOS low-phase-noise LC oscillator through memory reduction tail transistor. <i>IEEE Transactions on Circuits and Systems II: Express Briefs</i> , <b>2004</b> , 51, 85-90	3.5	40
345	A 2-D Distributed Power Combining by Metamaterial-Based Zero Phase Shifter for 60-GHz Power Amplifier in 65-nm CMOS. <i>IEEE Transactions on Microwave Theory and Techniques</i> , <b>2013</b> , 61, 505-516	4.1	38
344	Establishment and characterization of 12 human colorectal-carcinoma cell lines. <i>International Journal of Cancer</i> , <b>1999</b> , 81, 902-10	7.5	38
343	A Compact High-Performance Patch Antenna Array for 60-GHz Applications. <i>IEEE Antennas and Wireless Propagation Letters</i> , <b>2016</b> , 15, 313-316	3.8	37
342	. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , <b>2014</b> , 61, 699-711	3.9	36
341	Design and Analysis of Ultra Low Power True Single Phase Clock CMOS 2/3 Prescaler. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , <b>2010</b> , 57, 72-82	3.9	36
340	A 1.8-V 2.4/5.15-GHz dual-band LCVCO in 0.18- $\mu$ m CMOS technology. <i>IEEE Microwave and Wireless Components Letters</i> , <b>2006</b> , 16, 194-196	2.6	35
339	A Low Phase Noise and Wide Tuning Range Millimeter-Wave VCO Using Switchable Coupled VCO-Cores. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , <b>2015</b> , 62, 554-563	3.9	34
338	16.6- and 28-GHz Fully Integrated CMOS RF Switches With Improved Body Floating. <i>IEEE Transactions on Microwave Theory and Techniques</i> , <b>2008</b> , 56, 339-345	4.1	34
337	An 8-bit 200-MSample/s Pipelined ADC With Mixed-Mode Front-End S/H Circuit. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , <b>2008</b> , 55, 1430-1440	3.9	34
336	9.3-10.4-GHz-band cross-coupled complementary oscillator with low phase-noise performance. <i>IEEE Transactions on Microwave Theory and Techniques</i> , <b>2004</b> , 52, 1273-1278	4.1	32
335	A 57-to-64-GHz 0.094-mm <sup>2</sup> 5-bit Passive Phase Shifter in 65-nm CMOS. <i>IEEE Transactions on Very Large Scale Integration (VLSI) Systems</i> , <b>2016</b> , 24, 1917-1925	2.6	31
334	Cross-Coupled Current Conveyor Based CMOS Transimpedance Amplifier for Broadband Data Transmission. <i>IEEE Transactions on Very Large Scale Integration (VLSI) Systems</i> , <b>2013</b> , 21, 1516-1525	2.6	31
333	Effect of technology scaling on the 1/f noise of deep submicron PMOS transistors. <i>Solid-State Electronics</i> , <b>2004</b> , 48, 1101-1109	1.7	31
332	An Energy-Aware CMOS Receiver Front End for Low-Power 2.4-GHz Applications. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , <b>2010</b> , 57, 2675-2684	3.9	29
331	Temperature-Compensated dB-linear Digitally Controlled Variable Gain Amplifier With DC Offset Cancellation. <i>IEEE Transactions on Microwave Theory and Techniques</i> , <b>2013</b> , 61, 2648-2661	4.1	28

330	A Miniaturized Millimeter-Wave Standing-Wave Filtering Switch With High P1dB. <i>IEEE Transactions on Microwave Theory and Techniques</i> , <b>2013</b> , 61, 1505-1515	4.1	28
329	A 38 GHz Low-Noise CMOS Amplifier. <i>IEEE Microwave and Wireless Components Letters</i> , <b>2009</b> , 19, 245-247.	2.6	28
328	A modified architecture used for input matching in CMOS low-noise amplifiers. <i>IEEE Transactions on Circuits and Systems Part 2: Express Briefs</i> , <b>2005</b> , 52, 784-788		28
327	0.77 fJ/bit/search Content Addressable Memory Using Small Match Line Swing and Automated Background Checking Scheme for Variation Tolerance. <i>IEEE Journal of Solid-State Circuits</i> , <b>2014</b> , 49, 1487-1498	5.5	27
326	Miniaturized 3-bit Phase Shifter for 60 GHz Phased-Array in 65 nm CMOS Technology. <i>IEEE Microwave and Wireless Components Letters</i> , <b>2014</b> , 24, 50-52	2.6	26
325	Design of High-Q Millimeter-Wave Oscillator by Differential Transmission Line Loaded With Metamaterial Resonator in 65-nm CMOS. <i>IEEE Transactions on Microwave Theory and Techniques</i> , <b>2013</b> , 61, 1892-1902	4.1	26
324	Power-Efficient Explicit-Pulsed Dual-Edge Triggered Sense-Amplifier Flip-Flops. <i>IEEE Transactions on Very Large Scale Integration (VLSI) Systems</i> , <b>2011</b> , 19, 1-9	2.6	25
323	Design of a low power wide-band high resolution programmable frequency divider. <i>IEEE Transactions on Very Large Scale Integration (VLSI) Systems</i> , <b>2005</b> , 13, 1098-1103	2.6	25
322	A 65 nm CMOS Power Amplifier With Peak PAE above 18.9% From 57 to 66 GHz Using Synthesized Transformer-Based Matching Network. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , <b>2015</b> , 62, 2533-2543	3.9	24
321	Fully Symmetrical Monolithic Transformer (True 1 : 1) for Silicon RFIC. <i>IEEE Transactions on Microwave Theory and Techniques</i> , <b>2008</b> , 56, 2301-2311	4.1	24
320	A Low-Power Single-Phase Clock Multiband Flexible Divider. <i>IEEE Transactions on Very Large Scale Integration (VLSI) Systems</i> , <b>2012</b> , 20, 376-380	2.6	23
319	Design of a Ku-band Low-Phase-Noise VCO Using the Dual SLC Tanks. <i>IEEE Transactions on Circuits and Systems II: Express Briefs</i> , <b>2012</b> , 59, 262-266	3.5	23
318	Design and Sensitivity Analysis of a New Current-Mode Sense Amplifier for Low-Power SRAM. <i>IEEE Transactions on Very Large Scale Integration (VLSI) Systems</i> , <b>2011</b> , 19, 196-204	2.6	23
317	Ultra-Wideband Low-Loss Switch Design in High-Resistivity Trap-Rich SOI With Enhanced Channel Mobility. <i>IEEE Transactions on Microwave Theory and Techniques</i> , <b>2017</b> , 65, 3937-3949	4.1	22
316	Imparting electroactivity to polycaprolactone fibers with heparin-doped polypyrrole: Modulation of hemocompatibility and inflammatory responses. <i>Acta Biomaterialia</i> , <b>2015</b> , 23, 240-249	10.8	22
315	Design of a 60-GHz Quasi-Yagi Antenna With Novel Ladder-Like Directors for Gain and Bandwidth Enhancements. <i>IEEE Antennas and Wireless Propagation Letters</i> , <b>2016</b> , 15, 682-685	3.8	21
314	A High Speed Low Power CAM With a Parity Bit and Power-Gated ML Sensing. <i>IEEE Transactions on Very Large Scale Integration (VLSI) Systems</i> , <b>2013</b> , 21, 151-156	2.6	21
313	Physical layout design optimization of integrated spiral inductors for silicon-based RFIC applications. <i>IEEE Transactions on Electron Devices</i> , <b>2005</b> , 52, 2559-2567	2.9	21

312	Experimentally-based analytical model of deep-submicron LDD pMOSFETs in a Bi-MOS hybrid-mode environment. <i>IEEE Transactions on Electron Devices</i> , <b>1997</b> , 44, 1473-1482	2.9	20
311	Impact of technology scaling on the 1/f noise of thin and thick gate oxide deep submicron NMOS transistors. <i>IET Circuits, Devices and Systems</i> , <b>2004</b> , 151, 415		20
310	\$K\$-band High-PAE Wide-Tuning-Range VCO Using Triple-Coupled \$LC\$ Tanks. <i>IEEE Transactions on Circuits and Systems II: Express Briefs</i> , <b>2013</b> , 60, 736-740	3.5	18
309	Development of a miniaturized stimulation device for electrical stimulation of cells. <i>Journal of Biological Engineering</i> , <b>2015</b> , 9, 14	6.3	18
308	Bidirectional Diode-Triggered Silicon-Controlled Rectifiers for Low-Voltage ESD Protection. <i>IEEE Electron Device Letters</i> , <b>2012</b> , 33, 1360-1362	4.4	18
307	An Ultra-Compact Hairpin Band Pass Filter With Additional Zero Points. <i>IEEE Microwave and Wireless Components Letters</i> , <b>2007</b> , 17, 262-264	2.6	18
306	Class-D Amplifier Power Stage With PWM Feedback Loop. <i>IEEE Transactions on Power Electronics</i> , <b>2013</b> , 28, 3870-3881	7.2	17
305	A Reconfigurable K-/Ka-Band Power Amplifier With High PAE in 0.18- \$\mu\$m SiGe BiCMOS for Multi-Band Applications. <i>IEEE Transactions on Microwave Theory and Techniques</i> , <b>2015</b> , 63, 4395-4405	4.1	17
304	Low-power high-speed multiplier for error-tolerant application <b>2010</b> ,		17
303	A 220-285 GHz SPDT Switch in 65-nm CMOS Using Switchable Resonator Concept. <i>IEEE Transactions on Terahertz Science and Technology</i> , <b>2015</b> , 5, 649-651	3.4	16
302	Design of Ring-Oscillator-Based Injection-Locked Frequency Dividers With Single-Phase Inputs. <i>IEEE Microwave and Wireless Components Letters</i> , <b>2011</b> , 21, 559-561	2.6	16
301	Monolithic Sub-Terahertz SPDT Switches With Low Insertion Loss and Enhanced Isolation. <i>IEEE Transactions on Terahertz Science and Technology</i> , <b>2018</b> , 8, 192-200	3.4	15
300	A Compact 57-67 GHz Bidirectional LNPA in 65-nm CMOS Technology. <i>IEEE Microwave and Wireless Components Letters</i> , <b>2016</b> , 26, 628-630	2.6	15
299	Analysis and Design of Ultra-Wideband Low-Noise Amplifier With Input/Output Bandwidth Optimization and Single-Ended/Differential-Input Reconfigurability. <i>IEEE Transactions on Industrial Electronics</i> , <b>2014</b> , 61, 5672-5680	8.9	15
298	Internet of Things: Trends, challenges and applications <b>2014</b> ,		15
297	A Compact 2.1-3 GHz Self-Biased Low-Noise Amplifier in 65 nm CMOS Technology. <i>IEEE Microwave and Wireless Components Letters</i> , <b>2013</b> , 23, 662-664	2.6	15
296	Hybrid-Mode SRAM Sense Amplifiers: New Approach on Transistor Sizing. <i>IEEE Transactions on Circuits and Systems II: Express Briefs</i> , <b>2008</b> , 55, 986-990	3.5	15
295	Photoinduced intramolecular charge-transfer state of p-dimethylaminobenzoic acid in CdS and TiO <sub>2</sub> colloid solutions. <i>Journal of Photochemistry and Photobiology A: Chemistry</i> , <b>2000</b> , 132, 105-114	4.7	15

294	A low-power 16/spl times/16-b parallel multiplier utilizing pass-transistor logic. <i>IEEE Journal of Solid-State Circuits</i> , <b>1999</b> , 34, 1395-1399	5.5	15
293	Coupled Dual LC Tanks Based ILFD With Low Injection Power and Compact Size. <i>IEEE Microwave and Wireless Components Letters</i> , <b>2014</b> , 24, 105-107	2.6	14
292	Fully integrated CMOS fractional-N frequency divider for wide-band mobile applications with spurs reduction. <i>IEEE Transactions on Circuits and Systems Part 1: Regular Papers</i> , <b>2005</b> , 52, 1042-1048		14
291	Sub-1V bootstrapped CMOS driver for giga-scale-integration era. <i>Electronics Letters</i> , <b>1999</b> , 35, 392	1.1	14
290	Ultra low-power high-speed flexible Probabilistic Adder for Error-Tolerant Applications <b>2011</b> ,		13
289	A Weak-Inversion Low-Power Active Mixer for 2.4 GHz ISM Band Applications. <i>IEEE Microwave and Wireless Components Letters</i> , <b>2009</b> , 19, 719-721	2.6	13
288	A low-power static dual edge-triggered flip-flop using an output-controlled discharge configuration		13
287	Simple and accurate extraction methodology for RF MOSFET valid up to 20 GHz. <i>IET Circuits, Devices and Systems</i> , <b>2004</b> , 151, 587		13
286	A novel tap input coupling structure for a narrow bandpass filter using TM/sub 010/ mode of a microstrip circular-disk resonator. <i>IEEE Transactions on Microwave Theory and Techniques</i> , <b>2002</b> , 50, 1230-1232	4.1	13
285	Compact two-order bandpass filter with three finite zero points. <i>Electronics Letters</i> , <b>2005</b> , 41, 846	1.1	13
284	Comments on "Negative capacitance effect in semiconductor devices" [by M. Ershov et al., with reply]. <i>IEEE Transactions on Electron Devices</i> , <b>1999</b> , 46, 2357-2358	2.9	13
283	Design and Characterization of Micro-LED Matrix Display With Heterogeneous Integration of GaN and BCD Technologies. <i>IEEE Transactions on Electron Devices</i> , <b>2019</b> , 66, 4221-4227	2.9	12
282	A 4 GHz 60 dB Variable Gain Amplifier With Tunable DC Offset Cancellation in 65 nm CMOS. <i>IEEE Microwave and Wireless Components Letters</i> , <b>2015</b> , 25, 37-39	2.6	12
281	A Dual-Loop Clock and Data Recovery Circuit With Compact Quarter-Rate CMOS Linear Phase Detector. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , <b>2012</b> , 59, 1156-1167	3.9	12
280	A Scalable RFCMOS Noise Model. <i>IEEE Transactions on Microwave Theory and Techniques</i> , <b>2009</b> , 57, 1009-1019	4.0	12
279	. <i>IEEE Transactions on Very Large Scale Integration (VLSI) Systems</i> , <b>2005</b> , 13, 1060-1071	2.6	12
278	Fully integrated 10 GHz CMOS VCO. <i>Electronics Letters</i> , <b>2001</b> , 37, 1021	1.1	12
277	Low-power circuit implementation for partial-product addition using pass-transistor logic. <i>IET Circuits, Devices and Systems</i> , <b>1999</b> , 146, 124		12

276	A 30-GHz Power-Efficient PLL Frequency Synthesizer for 60-GHz Applications. <i>IEEE Transactions on Microwave Theory and Techniques</i> , <b>2017</b> , 65, 4165-4175	4.1	11
275	A 7.9-mW 5.6-GHz Digitally Controlled Variable Gain Amplifier With Linearization. <i>IEEE Transactions on Microwave Theory and Techniques</i> , <b>2012</b> , 60, 3482-3490	4.1	11
274	High-performance low-power current sense amplifier using a cross-coupled current-mirror configuration. <i>IET Circuits, Devices and Systems</i> , <b>2002</b> , 149, 308-314		11
273	Impact of velocity saturation and hot carrier effects on channel thermal noise model of deep sub-micron MOSFETs. <i>Solid-State Electronics</i> , <b>2012</b> , 72, 8-11	1.7	10
272	. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , <b>2013</b> , 60, 37-50	3.9	10
271	A 60-GHz Coplanar Waveguide-Based Bidirectional LNA in SiGe BiCMOS. <i>IEEE Microwave and Wireless Components Letters</i> , <b>2017</b> , 27, 742-744	2.6	10
270	A 2.4 GHz ultra low power subthreshold CMOS low-noise amplifier. <i>Microwave and Optical Technology Letters</i> , <b>2007</b> , 49, 743-744	1.2	10
269	Parasitic-compensated quadrature LC oscillator. <i>IET Circuits, Devices and Systems</i> , <b>2004</b> , 151, 45		10
268	Ultra-low-voltage bootstrapped CMOS driver for high performance applications. <i>Electronics Letters</i> , <b>2000</b> , 36, 706	1.1	10
267	THRU-Based Cascade De-embedding Technique for On-Wafer Characterization of RF CMOS Devices. <i>IEEE Transactions on Electron Devices</i> , <b>2013</b> , 60, 2892-2899	2.9	9
266	DC-30 GHz DPDT Switch Matrix Design in High Resistivity Trap-Rich SOI. <i>IEEE Transactions on Electron Devices</i> , <b>2017</b> , 64, 3548-3554	2.9	9
265	Predistortion Linearizer for Wideband AM/PM Cancellation With Left-Handed Delay Line. <i>IEEE Microwave and Wireless Components Letters</i> , <b>2017</b> , 27, 794-796	2.6	9
264	Sensing Margin Enhancement Techniques for Ultra-Low-Voltage SRAMs Utilizing a Bitline-Boosting Current and Equalized Bitline Leakage. <i>IEEE Transactions on Circuits and Systems II: Express Briefs</i> , <b>2012</b> , 59, 868-872	3.5	9
263	A 60GHz on-chip antenna in standard CMOS silicon Technology <b>2012</b> ,		9
262	Ultra-low power series input resonance differential common gate LNA. <i>Electronics Letters</i> , <b>2011</b> , 47, 703-704	1.1	9
261	Criterion to Evaluate Input-Offset Voltage of a Latch-Type Sense Amplifier. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , <b>2010</b> , 57, 83-92	3.9	9
260	Impact of device scaling on the 1/f noise performance of deep submicrometer thin gate oxide CMOS devices. <i>Solid-State Electronics</i> , <b>2006</b> , 50, 1219-1226	1.7	9
259	A 1 V switchable CMOS LNA for 802.11A/B WLAN applications. <i>Analog Integrated Circuits and Signal Processing</i> , <b>2006</b> , 48, 181-184	1.2	9

258	GHz programmable counter with low power consumption. <i>Electronics Letters</i> , <b>2003</b> , 39, 1572	1.1	9
257	A Wideband dB-Linear VGA With Temperature Compensation and Active Load. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , <b>2019</b> , 66, 3279-3287	3.9	8
256	Design of a Wideband Variable-Gain Amplifier With Self-Compensated Transistor for Accurate dB-Linear Characteristic in 65 nm CMOS Technology. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , <b>2020</b> , 67, 4187-4198	3.9	8
255	Design of Reconfigurable dB-Linear Variable-Gain Amplifier and Switchable-Order $g_m$ -C Filter in 65-nm CMOS Technology. <i>IEEE Transactions on Microwave Theory and Techniques</i> , <b>2019</b> , 67, 5148-5158	4.1	8
254	A new field dependent mobility model for high frequency channel thermal noise of deep submicron RFCMOS. <i>Solid-State Electronics</i> , <b>2012</b> , 68, 32-37	1.7	8
253	High-Frequency Noise Modeling of MOSFETs for Ultra Low-Voltage RF Applications. <i>IEEE Transactions on Microwave Theory and Techniques</i> , <b>2015</b> , 63, 141-154	4.1	8
252	A 26.8 dB Gain 19.7 dBm CMOS Power Amplifier Using 4-way Hybrid Coupling Combiner. <i>IEEE Microwave and Wireless Components Letters</i> , <b>2015</b> , 25, 43-45	2.6	8
251	DGS embedded transformed radial stub for ultra-wide stopband lowpass filter. <i>Electronics Letters</i> , <b>2012</b> , 48, 1473	1.1	8
250	MOSFET Drain Current Noise Modeling With Effective Gate Overdrive and Junction Noise. <i>IEEE Electron Device Letters</i> , <b>2012</b> , 33, 1117-1119	4.4	8
249	A 160 nW 25 kS/s 9-bit SAR ADC for neural signal recording applications <b>2012</b> ,		8
248	RADIAL LOADED TRANSFORMED RADIAL STUB FOR LPF STOPBAND EXTENSION. <i>Progress in Electromagnetics Research Letters</i> , <b>2012</b> , 30, 125-132	0.5	8
247	An Accurate Two-Port De-Embedding Technique for RF/Millimeter-Wave Noise Characterization and Modeling of Deep Submicrometer Transistors. <i>IEEE Transactions on Microwave Theory and Techniques</i> , <b>2011</b> , 59, 479-487	4.1	8
246	Dual-band bandpass filter using embedded spiral resonator and broadside-coupled meander slot-line. <i>Electronics Letters</i> , <b>2010</b> , 46, 1135	1.1	8
245	A 2.4 GHz ultra low-power high gain LNA utilizing $\Gamma$ match and capacitive feedback input network <b>2011</b> ,		8
244	Sub-1 V Low Power Wide Range Injection-Locked Frequency Divider. <i>IEEE Microwave and Wireless Components Letters</i> , <b>2007</b> , 17, 528-530	2.6	8
243	High Frequency Thick Film BST Ferroelectric Phase Shifter. <i>Integrated Ferroelectrics</i> , <b>2004</b> , 61, 65-70	0.8	8
242	New wideband dualband CMOS LC voltage-controlled oscillator. <i>IET Circuits, Devices and Systems</i> , <b>2003</b> , 150, 453		8
241	Accurate and scalable RF interconnect model for silicon-based RFIC applications. <i>IEEE Transactions on Microwave Theory and Techniques</i> , <b>2005</b> , 53, 3035-3044	4.1	8



240	Full-swing high speed CBiCMOS digital circuit for low-voltage applications. <i>IET Circuits, Devices and Systems</i> , <b>1995</b> , 142, 8		8
239	A New Millimeter-Wave Fixture Deembedding Method Based on Generalized Cascade Network Model. <i>IEEE Electron Device Letters</i> , <b>2013</b> , 34, 447-449	4.4	7
238	Ultra-wide rejection band lowpass cell. <i>Electronics Letters</i> , <b>2012</b> , 48, 99	1.1	7
237	MODELING AND LAYOUT OPTIMIZATION TECHNIQUES FOR SILICON-BASED SYMMETRICAL SPIRAL INDUCTORS. <i>Progress in Electromagnetics Research</i> , <b>2013</b> , 143, 1-18	3.8	7
236	A SPICE COMPATIBLE MODEL OF ON-WAFER COUPLED INTERCONNECTS FOR CMOS RFICs. <i>Progress in Electromagnetics Research</i> , <b>2010</b> , 102, 287-299	3.8	7
235	A novel planar multimode bandpass filter with radial perturbation. <i>Microwave and Optical Technology Letters</i> , <b>2009</b> , 51, 964-966	1.2	7
234	A Cross-Coupled LPF Topology and Design for Millimeter-Wave RFIC Applications. <i>IEEE Transactions on Electron Devices</i> , <b>2012</b> , 59, 2902-2909	2.9	7
233	Sensitivity Analysis of Coupled Interconnects for RFIC Applications. <i>IEEE Transactions on Electromagnetic Compatibility</i> , <b>2006</b> , 48, 607-613	2	7
232	A novel methodology for the design of LC tank VCO with low phase noise		7
231	1 V 10 GHz CMOS frequency divider with low power consumption. <i>Electronics Letters</i> , <b>2004</b> , 40, 467	1.1	7
230	Low power high-speed CMOS dual-modulus prescaler design with imbalanced phase-switching technique. <i>IET Circuits, Devices and Systems</i> , <b>2005</b> , 152, 127		7
229	AN ULTRA LOW-POWER CURRENT-MODE SENSE AMPLIFIER FOR SRAM APPLICATIONS. <i>Journal of Circuits, Systems and Computers</i> , <b>2005</b> , 14, 939-951	0.9	7
228	1.1 V full-swing double bootstrapped BiCMOS logic gates. <i>IET Circuits, Devices and Systems</i> , <b>1996</b> , 143, 41		7
227	Miniaturized 4080 GHz On-Chip Balun With Capacitive Loading Compensation. <i>IEEE Electron Device Letters</i> , <b>2014</b> , 35, 434-436	4.4	6
226	High-speed CMOS image sensor for high-throughput lensless microfluidic imaging system <b>2012</b> ,		6
225	. <i>IEEE Transactions on Microwave Theory and Techniques</i> , <b>2007</b> , 55, 1844-1853	4.1	6
224	0.9 V current-mode sense amplifier using concurrent bit- and data-line tracking and sensing techniques. <i>Electronics Letters</i> , <b>2007</b> , 43, 1421	1.1	6
223	A broadband CMOS LNA for WLAN applications		6

222	. <i>IEEE Transactions on Semiconductor Manufacturing</i> , <b>2003</b> , 16, 220-227	2.6	6
221	New small-signal model for HEMTs and MESFETs. <i>Microwave and Optical Technology Letters</i> , <b>2001</b> , 28, 375-378	1.2	6
220	Physically-based RF model for metal-oxide-metal capacitors. <i>Electronics Letters</i> , <b>2000</b> , 36, 425	1.1	6
219	Altering transistor positions: impact on the performance and power dissipation of dynamic latches and flip-flops. <i>IET Circuits, Devices and Systems</i> , <b>1999</b> , 146, 279		6
218	Heterogeneous Integration of GaN and BCD Technologies. <i>Electronics (Switzerland)</i> , <b>2019</b> , 8, 351	2.6	5
217	Modified Inductive Peaking Direct Injection ILFD With Multi-Coupled Coils. <i>IEEE Microwave and Wireless Components Letters</i> , <b>2015</b> , 25, 379-381	2.6	5
216	A VCO phase noise reduction technique to suppress the active device contribution <b>2016</b> ,		5
215	A 35-mW 30-dB Gain Control Range Current Mode Linear-in-Decibel Programmable Gain Amplifier With Bandwidth Enhancement. <i>IEEE Transactions on Microwave Theory and Techniques</i> , <b>2014</b> , 62, 3465-3475	4.1	5
214	Substrate-Induced Noise Model and Parameter Extraction for High-Frequency Noise Modeling of Sub-Micron MOSFETs. <i>IEEE Transactions on Microwave Theory and Techniques</i> , <b>2014</b> , 62, 1973-1985	4.1	5
213	Review of high efficiency integrated LED lighting <b>2017</b> ,		5
212	A DC-50 GHz SPDT switch with maximum insertion loss of 1.9 dB in a commercial 0.13- $\mu$ m SOI technology <b>2015</b> ,		5
211	A 60GHz VCO with 25.8% tuning range by switching return-path in 65nm CMOS <b>2012</b> ,		5
210	A CMOS LOW-POWER TEMPERATURE-ROBUST RSSI USING WEAK-INVERSION LIMITING AMPLIFIERS. <i>Journal of Circuits, Systems and Computers</i> , <b>2013</b> , 22, 1340034	0.9	5
209	High-frequency low-power LC divide-by-2/3 injection-locked frequency divider. <i>Microwave and Optical Technology Letters</i> , <b>2011</b> , 53, 337-340	1.2	5
208	A DC to 30-GHz ultra-wideband CMOS T/R switch. <i>Microwave and Optical Technology Letters</i> , <b>2011</b> , 53, 2072-2075	1.2	5
207	An ultra-wideband bandpass filter using hybrid structure of microstrip and CPW. <i>Microwave and Optical Technology Letters</i> , <b>2009</b> , 51, 2470-2473	1.2	5
206	A random number generator for low power cryptographic application <b>2010</b> ,		5
205	New current conveyor for high-speed low-power current sensing. <i>IET Circuits, Devices and Systems</i> , <b>1998</b> , 145, 85		5

204	Modeling and Layout Optimization of Differential Inductors for Silicon-Based RFIC Applications. <i>IEEE Transactions on Electron Devices</i> , <b>2008</b> , 55, 1058-1066	2.9	5
203	PCMOS-based Hardware Implementation of Bayesian Network <b>2007</b> ,		5
202	Equivalent circuit model of a stacked inductor for high-Q on-chip RF applications. <i>IET Circuits, Devices and Systems</i> , <b>2006</b> , 153, 525		5
201	Low-power high-performance explicit-pulsed flip-flop using static latch and dynamic pulse generator. <i>IET Circuits, Devices and Systems</i> , <b>2006</b> , 153, 253		5
200	. <i>IEEE Transactions on Advanced Packaging</i> , <b>2006</b> , 29, 770-776		5
199	1.5 V 1.8 GHz bandpass amplifier. <i>IET Circuits, Devices and Systems</i> , <b>2000</b> , 147, 331		5
198	Effects of polysilicon shield on spiral inductors for silicon-based RF ICs		5
197	A Monolithically Integrated Single-Input Load-Modulated Balanced Amplifier With Enhanced Efficiency at Power Back-Off. <i>IEEE Journal of Solid-State Circuits</i> , <b>2021</b> , 56, 1553-1564	5.5	5
196	A Hybrid Pad-Line-Finger De-Embedding Technique for Broadband Modeling of CMOS Transistor. <i>IEEE Microwave and Wireless Components Letters</i> , <b>2016</b> , 26, 507-509	2.6	5
195	A $\mathbb{F}$ -Band Wide Locking Range Divide-by-4 Injection-Locked Frequency Divider. <i>IEEE Microwave and Wireless Components Letters</i> , <b>2018</b> , 28, 1020-1022	2.6	5
194	A 60-GHz bi-directional variable gain amplifier with microstrip-line interconnect in 65 nm CMOS <b>2016</b> ,		4
193	A 35 mW 30 dB gain control range current mode programmable gain amplifier with DC offset cancellation <b>2014</b> ,		4
192	A low power low phase noise dual-band multiphase VCO. <i>Microelectronics Journal</i> , <b>2012</b> , 43, 1016-1022	1.8	4
191	Embedded Transformed Radial Stub Cell for BPF With Spurious-Free Above Ten Octaves. <i>IEEE Transactions on Components, Packaging and Manufacturing Technology</i> , <b>2013</b> , 3, 1597-1603	1.7	4
190	. <i>IEEE Transactions on Circuits and Systems II: Express Briefs</i> , <b>2011</b> , 58, 729-733	3.5	4
189	A low-power CAM with efficient power and delay trade-off <b>2011</b> ,		4
188	Analysis and Design of RC Polyphase Network for Quadrature Signal Generation in the 2.45GHz ISM Band <b>2007</b> ,		4
187	A Low Power Fully Programmable 1MHz Resolution 2.4GHz CMOS PLL Frequency Synthesizer <b>2007</b> ,		4

186	VLSI Architectures for Lifting-Based Discrete Wavelet Packet Transform <b>2007</b> ,		4
185	RF equivalent-circuit model of interconnect bends based on S-parameter measurements. <i>Microwave and Optical Technology Letters</i> , <b>2005</b> , 45, 170-173	1.2	4
184	Small signal model and efficient parameter extraction technique for deep submicron MOSFETs for RF applications. <i>IET Circuits, Devices and Systems</i> , <b>2001</b> , 148, 35		4
183	Design of high performance double edge-triggered flip-flops. <i>IET Circuits, Devices and Systems</i> , <b>2000</b> , 147, 283		4
182	1.5 V high speed low power CMOS current sense amplifier. <i>Electronics Letters</i> , <b>1995</b> , 31, 1991-1993	1.1	4
181	New complementary BiCMOS digital gates for low-voltage environments. <i>Solid-State Electronics</i> , <b>1996</b> , 39, 681-687	1.7	4
180	A data-dependent energy reduction algorithm for SAR ADC using self-adaptive window. <i>Microelectronics Journal</i> , <b>2020</b> , 100, 104754	1.8	4
179	Decentralized and Lightweight Approach to Detect Eclipse Attacks on Proof of Work Blockchains. <i>IEEE Transactions on Network and Service Management</i> , <b>2021</b> , 18, 1659-1672	4.8	4
178	Heterogeneous Integration: A Promising Technology to Future Integrated Power Conversion Electronics. <i>IEEE Power Electronics Magazine</i> , <b>2021</b> , 8, 37-47	1.5	4
177	. <i>IEEE Transactions on Very Large Scale Integration (VLSI) Systems</i> , <b>2019</b> , 27, 2954-2958	2.6	3
176	2.3 A 130-to-180GHz 0.0035mm <sup>2</sup> SPDT switch with 3.3dB loss and 23.7dB isolation in 65nm bulk CMOS <b>2015</b> ,		3
175	A multi-mode 30 GHz 2 degree RMS power efficient phase-locked loop frequency synthesizer <b>2016</b> ,		3
174	Heterogeneous Integration of GaN LED on CMOS Driver Circuit for Mobile Phone Applications <b>2018</b> ,		3
173	Improved inverter-based read-out scheme for low-power ISFET sensing array. <i>Electronics Letters</i> , <b>2013</b> , 49, 1517-1518	1.1	3
172	A wideband BiCMOS variable gain amplifier with novel continuous dB-linear gain control and temperature compensation. <i>Analog Integrated Circuits and Signal Processing</i> , <b>2017</b> , 90, 499-506	1.2	3
171	Micro-LED arrays for display and communication: Device structure and driver architecture <b>2017</b> ,		3
170	A study of CMOS SOI for RF, Microwave and millimeter wave applications <b>2015</b> ,		3
169	A 1.2 V 2.4 GHz low spur CMOS PLL synthesizer with a gain boosted charge pump for a batteryless transceiver <b>2012</b> ,		3

168	0.6mW 6.3GHz 40nm CMOS divide-by-2/3 prescaler using heterodyne phase-locking technique. <i>Electronics Letters</i> , <b>2013</b> , 49, 471-472	1.1	3
167	A Cascade-Parallel Based Noise De-Embedding Technique for RF Modeling of CMOS Device. <i>IEEE Microwave and Wireless Components Letters</i> , <b>2011</b> , 21, 448-450	2.6	3
166	Novel hybrid type Automatic Amplitude Control loop VCO <b>2011</b> ,		3
165	6.1 GHz 4.6 mW CMOS divide-by-55/56 prescaler. <i>Electronics Letters</i> , <b>2008</b> , 44, 1402	1.1	3
164	Low Power Transmitter Design for BAN <b>2007</b> ,		3
163	Distortion of pulsed signals in carbon nanotube interconnects. <i>Microelectronics Journal</i> , <b>2007</b> , 38, 365-370		3
162	Characterization and modeling of on-wafer single and multiple vias for CMOS RFICS. <i>Microwave and Optical Technology Letters</i> , <b>2008</b> , 50, 713-715	1.2	3
161	High Self-Resonant and Area Efficient Monolithic Transformer Using Novel Intercoil-Crossing Structure for Silicon RFIC. <i>IEEE Electron Device Letters</i> , <b>2008</b> , 29, 1376-1379	4.4	3
160	Fully integrated CMOS limiting amplifier with offset compensation network. <i>Electronics Letters</i> , <b>2007</b> , 43, 1084	1.1	3
159	. <i>IEEE Transactions on Semiconductor Manufacturing</i> , <b>2005</b> , 18, 246-254	2.6	3
158	A 200-MHz CMOS Mixed-Mode Sample-and-Hold Circuit for Pipelined ADCs <b>2006</b> ,		3
157	Algorithm and architecture for a high density, low power scalar product macrocell. <i>IEE Proceedings: Computers and Digital Techniques</i> , <b>2004</b> , 151, 161		3
156	Equivalent circuit model of on-wafer interconnects for CMOS RFICS		3
155	A new 5 GHz CMOS dual-modulus prescaler		3
154	Non-sequential linear CMOS phase detector for CDR applications. <i>IET Circuits, Devices and Systems</i> , <b>2005</b> , 152, 667		3
153	Latchup characterization of 0.18-micron STI cobalt silicided test structures. <i>Microelectronics Journal</i> , <b>2001</b> , 32, 725-731	1.8	3
152	Impact of 0.25 [micro sign]m dual gate oxide thickness CMOS process on flicker noise performance of multifingered deep-submicron MOS devices. <i>IET Circuits, Devices and Systems</i> , <b>2001</b> , 148, 312		3
151	A new geometrical optimization technique for RF integrated inductors. <i>Microwave and Optical Technology Letters</i> , <b>2000</b> , 26, 39-41	1.2	3

150	A redundant-binary partial-product generator based on a five-bit recoding technique. <i>International Journal of Electronics</i> , <b>2000</b> , 87, 413-423	1.2	3
149	High temperature superconducting ferrite phase shifter with new latching structure. <i>IEEE Transactions on Applied Superconductivity</i> , <b>2001</b> , 11, 430-433	1.8	3
148	A charge-trapping-based technique to design low-voltage BiCMOS logic circuits. <i>IEEE Journal of Solid-State Circuits</i> , <b>1998</b> , 33, 164-168	5.5	3
147	Design of Differential Variable-Gain Transimpedance Amplifier in 0.18 $\mu\text{m}$ SiGe BiCMOS. <i>Electronics (Switzerland)</i> , <b>2020</b> , 9, 1058	2.6	3
146	Design of a Ka-Band U-Shaped Bandpass Filter with 20-GHz Bandwidth in 0.13- $\mu\text{m}$ BiCMOS Technology. <i>Electronics (Switzerland)</i> , <b>2020</b> , 9, 1608	2.6	3
145	Real-Time Audio Transmission Using Visible Light Communication <b>2018</b> ,		3
144	. <i>IEEE Microwave and Wireless Components Letters</i> , <b>2018</b> , 28, 1089-1091	2.6	3
143	A 45 $\mu\text{m}$ 5 GHz Vector Modulator MMIC With Built-In Voltage Converter. <i>IEEE Microwave and Wireless Components Letters</i> , <b>2017</b> , 27, 515-517	2.6	2
142	Wideband millimetre-wave CMOS power amplifier using transistor-based inductive source degeneration and specially shielded transformer. <i>IET Microwaves, Antennas and Propagation</i> , <b>2017</b> , 11, 410-416	1.6	2
141	6.2GHz 0.5mW two-dimensional oscillator array-based injection-locked frequency divider in 0.18 $\mu\text{m}$ CMOS. <i>Electronics Letters</i> , <b>2015</b> , 51, 62-63	1.1	2
140	92.5% Average Power Efficiency Fully Integrated Floating Buck Quasi-Resonant LED Drivers Using GaN FETs. <i>Electronics (Switzerland)</i> , <b>2020</b> , 9, 575	2.6	2
139	A 11.2 mW 48 $\mu\text{m}$ 2 GHz Low Noise Amplifier in 65 nm CMOS Technology. <i>Circuits, Systems, and Signal Processing</i> , <b>2016</b> , 35, 1531-1543	2.2	2
138	A Multi-Mode Multi-Coil Coupled Tuned Inductive Peaking ILFD for Low Injected Power With Compact Size. <i>IEEE Access</i> , <b>2019</b> , 7, 59059-59068	3.5	2
137	A Compact Coupling Controllable Elliptical Filter Based on Multilayer LTCC. <i>Microwave and Optical Technology Letters</i> , <b>2013</b> , 55, 1789-1792	1.2	2
136	A 12-mW 40 $\mu\text{m}$ 0-GHz 0.18- $\mu\text{m}$ BiCMOS Oscillator-Less Self-Demodulator for Short-Range Software-Defined Transceivers. <i>IEEE Journal on Emerging and Selected Topics in Circuits and Systems</i> , <b>2013</b> , 3, 521-530	5.2	2
135	Generalized multiple coupled tanks for silicon based RF/mm-wave IC (Invited) <b>2015</b> ,		2
134	Novel Q-factor enhancement technique for on-chip spiral inductors and its application to cmos low-noise amplifier designs. <i>Microwave and Optical Technology Letters</i> , <b>2015</b> , 57, 2883-2886	1.2	2
133	A 40 GHz 65 nm CMOS Phase-Locked Loop With Optimized Shunt-Peaked Buffer. <i>IEEE Microwave and Wireless Components Letters</i> , <b>2015</b> , 25, 34-36	2.6	2

132	A 32kb 9T SRAM with PVT-tracking read margin enhancement for ultra-low voltage operation <b>2015</b> ,		2
131	A 60-GHz on-chip antenna over an AMC using a standard 65-nm CMOS technology <b>2014</b> ,		2
130	A 1-V CMOS Ultralow-Power Receiver Front End for the IEEE 802.15.4 Standard Using Tuned Passive Mixer Output Pole. <i>International Federation for Information Processing</i> , <b>2012</b> , 1-21		2
129	Designs of a free-space white-LED mass-storage transceiver for SD-card file transfer <b>2012</b> ,		2
128	A low power millimetre-wave VCO in 0.18 $\mu$ m SiGe BiCMOS technology <b>2012</b> ,		2
127	Research and development of microwave & millimeter-wave technology in singapore <b>2012</b> ,		2
126	A fully integrated low power PAM multi-channel UWB transmitter. <i>Analog Integrated Circuits and Signal Processing</i> , <b>2011</b> , 68, 77-84	1.2	2
125	A 1.8-V 6.5-GHz low power wide band single-phase clock CMOS 2/3 prescaler <b>2010</b> ,		2
124	A double-quadrature down-conversion mixer in 0.18 $\mu$ m SiGe BiCMOS process <b>2011</b> ,		2
123	A 3.1-8 GHz CMOS UWB front-end receiver <b>2011</b> ,		2
122	A low power UWB direct conversion receiver with pulse detectors <b>2009</b> ,		2
121	An Ultra-Compact Planar Bandpass Filter With Open-Ground Spiral for Wireless Application. <i>IEEE Transactions on Advanced Packaging</i> , <b>2008</b> , 31, 285-291		2
120	CMOS EVEN HARMONIC SWITCHING MIXER FOR DIRECT CONVERSION RECEIVERS. <i>Journal of Circuits, Systems and Computers</i> , <b>2006</b> , 15, 183-196	0.9	2
119	Characterizing and Modeling Conductor-backed CPW Periodic Band Stop Filter with Miniaturized Size. <i>IEEE MTT-S International Microwave Symposium Digest IEEE MTT-S International Microwave Symposium</i> , <b>2007</b> ,		2
118	A Low-Voltage Low-Power High Linear and Wide-Band Mixer <b>2007</b> ,		2
117	A miniaturized silicon-based ground Ring Guarded patch resonator and filter. <i>IEEE Microwave and Wireless Components Letters</i> , <b>2005</b> , 15, 478-480	2.6	2
116	Design of a Fully Integrated Switchable Transistor CMOS LNA for 2.1 / 2.4 GHz Application <b>2006</b> ,		2
115	Novel 53/106 GHz Dual-Band MMW LC Oscillator Implemented in SiGe BiCMOS Technology. <i>Journal of Infrared, Millimeter and Terahertz Waves</i> , <b>2004</b> , 25, 55-62		2

114	A comprehensive study and modeling of centre-tap differentially driven single-turn integrated inductors for 10-GHz applications. <i>Microwave and Optical Technology Letters</i> , <b>2003</b> , 38, 182-185	1.2	2
113	A 52 GHz VCO with low-phase noise implemented in SiGe BiCMOS technology. <i>Microwave and Optical Technology Letters</i> , <b>2003</b> , 39, 414-418	1.2	2
112	A 2GHz programmable counter with new re-loadable D flip-flop		2
111	An Integrated SiGe Dual-band Low Noise Amplifier for Bluetooth, HiperLAN and Wireless LAN Applications <b>2003</b> ,		2
110	Extremely high-Q stacked transformer-type inductors for RF applications		2
109	Compact CMOS Baluns for the 40 GHz Band Applications. <i>Analog Integrated Circuits and Signal Processing</i> , <b>2005</b> , 45, 5-13	1.2	2
108	Effective channel length and external series resistance models of scaled LDD pMOSFETs operating in a Bi-MOS hybrid-mode environment. <i>IEEE Transactions on Electron Devices</i> , <b>2001</b> , 48, 1001-1004	2.9	2
107	High-Q Si-based inductor shielded with double-layer polysilicon for RF applications. <i>Microwave and Optical Technology Letters</i> , <b>2000</b> , 24, 366-367	1.2	2
106	Novel low-voltage BiCMOS digital circuits employing a lateral p-n-p BJT in a p-MOS structure. <i>IET Circuits, Devices and Systems</i> , <b>1996</b> , 143, 83		2
105	An Inductorless 5-GHz Differential Dual Regulated Cross-Cascode Transimpedance Amplifier using 40 nm CMOS <b>2019</b> ,		2
104	A high gain 60 GHz antipodal Fermi-tapered slot antenna based on robust synthesized dielectric. <i>Microwave and Optical Technology Letters</i> , <b>2019</b> , 61, 761-765	1.2	2
103	An Inductorless 6-GHz Variable Gain Differential Transimpedance Amplifier in 0.18- $\mu\text{m}$ SiGe BiCMOS <b>2019</b> ,		1
102	Design of a Voltage-Controlled Programmable-Gain Amplifier in 65-nm CMOS Technology <b>2019</b> ,		1
101	A 3 mW 54 GHz 0.18 $\mu\text{m}$ BiCMOS voltage controlled oscillator with supply injection locking. <i>Microwave and Optical Technology Letters</i> , <b>2015</b> , 57, 1912-1914	1.2	1
100	Low-Crosstalk Semi-Trench-Assisted Multicore Flat Fiber <b>2015</b> ,		1
99	Design of a hybrid neural spike detection algorithm for implantable integrated brain circuits <b>2015</b> ,		1
98	Design and optimization of the ring oscillator based injection locked frequency dividers. <i>Microelectronics Journal</i> , <b>2018</b> , 72, 40-48	1.8	1
97	A 2.4 mW 2.5 GHz multi-phase clock generator with duty cycle imbalance correction in 0.13 $\mu\text{m}$ CMOS. <i>The Integration VLSI Journal</i> , <b>2018</b> , 63, 87-92	1.4	1



96	A 24 GHz low power low phase noise dual-mode phase locked loop frequency synthesizer for 60 GHz applications <b>2014</b> ,		1
95	A 0.6-V high reverse-isolation through feedback self-cancellation for single-stage noncascode CMOS LNA. <i>Microwave and Optical Technology Letters</i> , <b>2012</b> , 54, 374-379	1.2	1
94	A 1-mW K-band gate AC-coupled VCO with 0.25 V supply voltage. <i>Analog Integrated Circuits and Signal Processing</i> , <b>2013</b> , 77, 87-91	1.2	1
93	The Investigation and Optimisation of Phase-Induced Amplitude Attenuation in the Injection-Locked Ring Oscillators-Based Receiver. <i>Circuits, Systems, and Signal Processing</i> , <b>2017</b> , 36, 1818-1835	2.3	1
92	Broadband 60GHz 32-way ring-cavity power combiner <b>2014</b> ,		1
91	Retention time characterization and optimization of logic-compatible embedded DRAM cells <b>2012</b> ,		1
90	Design of quarter-wavelength resonator filters with coupling controllable paths <b>2012</b> ,		1
89	Transformer based multiple coupled LC tanks for on-chip VCO design and applications <b>2012</b> ,		1
88	A 960 1V ultra-low power CMOS image sensor for biomedical application <b>2012</b> ,		1
87	Ultra low power active 60 GHz Bi-CMOS down-conversion mixer <b>2011</b> ,		1
86	SiGe BiCMOS power amplifiers for 60GHz ISM band applications <b>2011</b> ,		1
85	A divide-by-two injection-locked frequency divider with 13-GHz locking range in 0.18- $\mu\text{m}$ CMOS technology <b>2011</b> ,		1
84	Design and analysis of a WLAN CMOS power amplifier using multiple-gated transistor technique. <i>International Journal of RF and Microwave Computer-Aided Engineering</i> , <b>2011</b> , 21, 157-163	1.5	1
83	A 1.8-V 3.6-mW 2.4-GHz fully integrated CMOS frequency synthesizer for IEEE 802.15.4 <b>2010</b> ,		1
82	A compact UWB bandpass filter with ultra narrow notched band and competitive attenuation slope <b>2010</b> ,		1
81	Novel low cost compact size planar low pass filters with deep skirt selectivity and wide stopband rejection <b>2010</b> ,		1
80	Design of probabilistic-based Markov Random Field logic gates in 65nm CMOS technology <b>2010</b> ,		1
79	A possible reality on battery-less low-power portable electronics <b>2010</b> ,		1

78	A 1-V CMOS ultralow-power receiver front end for the IEEE 802.15.4 standard using tuned passive mixer output pole <b>2010</b> ,		1
77	Wide center-tape balun for 60 GHz silicon RF ICs <b>2011</b> ,		1
76	<b>2011</b> ,		1
75	A novel de-embedding technique for On-Wafer characterization of RF CMOS <b>2009</b> ,		1
74	Transformed radial stub cell embedded resonator for high performance filter applications <b>2012</b> ,		1
73	A new unified model for channel thermal noise of deep sub-micron RFCMOS <b>2009</b> ,		1
72	Delay time sensitivity analysis of multi-generation BiCMOS digital circuits. <i>IET Circuits, Devices and Systems</i> , <b>1997</b> , 144, 60		1
71	Sub-mW multi-GHz CMOS dual-modulus prescalers based on programmable injection-locked frequency dividers <b>2008</b> ,		1
70	Complex Shaped On-Wafer Interconnects Modeling for CMOS RFICs. <i>IEEE Transactions on Very Large Scale Integration (VLSI) Systems</i> , <b>2008</b> , 16, 922-926	2.6	1
69	INTEGRATED CIRCUIT DESIGN RESEARCH RANKING FOR WORLDWIDE UNIVERSITIES. <i>Journal of Circuits, Systems and Computers</i> , <b>2008</b> , 17, 141-167	0.9	1
68	Design of Driver Amplifiers for 2.4GHz Low Power Short-range Transceiver <b>2007</b> ,		1
67	A Novel Static Dual Edge-Trigger Flip-flop for High-Frequency Low-Power Application <b>2007</b> ,		1
66	Scalable model-based design of a tapped-line filter with common ground via resonators. <i>Microwave and Optical Technology Letters</i> , <b>2006</b> , 48, 96-99	1.2	1
65	A fully integrated 2.4-GHz receiver in a 0.18- $\mu\text{m}$ CMOS process for low-power body-area-network applications <b>2007</b> ,		1
64	A New Phase Noise Model for TSPC based divider <b>2006</b> ,		1
63	A comprehensive geometrical and biasing analysis for latchup in 0.18- $\mu\text{m}$ CoSi <sub>2</sub> STI CMOS structure. <i>Solid-State Electronics</i> , <b>2004</b> , 48, 2109-2114	1.7	1
62	Investigation of the wideband operation capability of Gilbert cell mixers. <i>Microwave and Optical Technology Letters</i> , <b>2002</b> , 33, 97-100	1.2	1
61	Design of an area-efficient CMOS multiple-valued current comparator circuit. <i>IET Circuits, Devices and Systems</i> , <b>2005</b> , 152, 151		1

60	Effective modeling of temperature-dependent body current for submicron devices under BiMOS hybrid-mode operation. <i>Microelectronics Journal</i> , <b>2001</b> , 32, 205-214	1.8	1
59	An interconnect optimized floorplanning of a scalar product macrocell		1
58	Modeling of the body current in a Bi-MOS hybrid-mode environment. <i>Solid-State Electronics</i> , <b>2000</b> , 44, 2199-2205	1.7	1
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