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List of Publications by Year in descending order

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67
citing authors

#	ARTICLE	IF	CITATIONS
1	A Scalable 20V Charge-Pump-Based Driver in 65nm CMOS Technology. IEEE Transactions on Circuits and Systems II: Express Briefs, 2022, 69, 55-59.	3.0	2
2	A 36-V 49% Efficient Hybrid Charge Pump in Nanometer-Scale Bulk CMOS Technology. IEEE Journal of Solid-State Circuits, 2017, 52, 781-798.	5.4	23
3	A 12-V charge pump-based square wave driver in 65-nm CMOS technology. , 2014, , .		8
4	A compact stacked-device output driver in low-voltage CMOS Technology. , 2014, , .		8