

Toru Nakura

List of Publications by Year in descending order

Source: <https://exaly.com/author-pdf/452340/publications.pdf>

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4
papers

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citations

2682572
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4
times ranked

28
citing authors

#	ARTICLE	IF	CITATIONS
1	A 16-bit 2.0-ps Resolution Two-Step TDC in 0.18- μm CMOS Utilizing Pulse-Shrinking Fine Stage With Built-In Coarse Gain Calibration. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2019, 27, 11-19.	3.1	21
2	Fault Detection of VLSI Power Supply Network Based on Current Estimation From Surface Magnetic Field. IEEE Transactions on Instrumentation and Measurement, 2019, 68, 2519-2530.	4.7	11
3	4-Cycle-Start-Up Reference-Clock-Less Digital CDR Utilizing TDC-Based Initial Frequency Error Detection with Frequency Tracking Loop. IEICE Transactions on Electronics, 2022, , .	0.6	0
4	Density Aware Cell Library Design for Design-Technology Co-Optimization. , 2022, , .		0