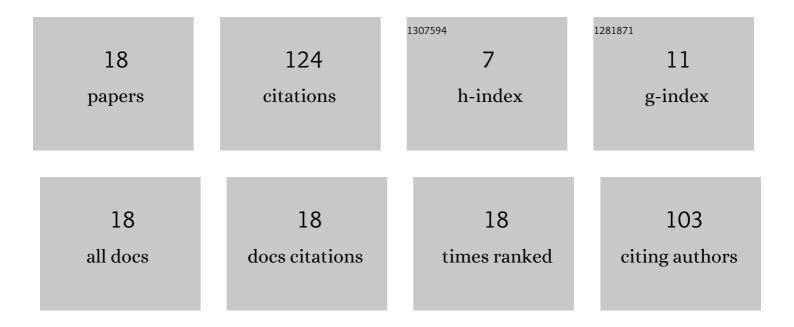
## Girija Shankar Sahoo

List of Publications by Year in descending order

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#	Article	IF	CITATIONS
1	Use of hetero intrinsic layer in GaAs P-I-N solar cell to improve the intermediate band performance. Materials Science and Engineering B: Solid-State Materials for Advanced Technology, 2021, 263, 114862.	3.5	5
2	Electrical, Optical, and Reliability Analysis of QD-Embedded Kesterite Solar Cell. IEEE Transactions on Electron Devices, 2021, , 1-7.	3.0	10
3	Extensive Study on Effects of Defects in CZTS/CZTSe Quantum Dots Kesterite Solar Cells. , 2021, , .		1
4	Preservation of open circuit voltage in a quantum dot solar cell using GaSb quantum confined superlattice. Optik, 2020, 212, 164678.	2.9	2
5	Incorporation of Different Quantum Superlattices in a Single Junction GaAs Solar Cell: A Comparative Study. , 2020, , 796-802.		Ο
6	Design and modelling of InGaP/GaSb tandem cell with embedded 1D GaAs quantum superlattice. IET Circuits, Devices and Systems, 2020, 14, 471-476.	1.4	2
7	Use of InGaAs/GaSb Quantum Ratchet in p-i-n GaAs Solar Cell for Voltage Preservation and Higher Conversion Efficiency. IEEE Transactions on Electron Devices, 2019, 66, 153-159.	3.0	15
8	Effect of impact ionization on the performance of quantum ratchet embedded intermediate band solar cell: An extensive simulation study. Optik, 2019, 199, 163382.	2.9	5
9	Extensive analysis of band alignment engineering on the open circuit voltage performance of a GaAs/GaSb hetero structure solar cell. , 2019, , .		Ο
10	Efficient Use of Low-Bandgap GaAs/GaSb to Convert More than 50% of Solar Radiation into Electrical Energy: A Numerical Approach. Journal of Electronic Materials, 2019, 48, 560-570.	2.2	10
11	Current switching ratio optimization using dual pocket doping engineering. Superlattices and Microstructures, 2018, 113, 791-798.	3.1	1
12	Design and modeling of an SJ infrared solar cell approaching upper limit of theoretical efficiency. International Journal of Modern Physics B, 2018, 32, 1850014.	2.0	9
13	Detail analysis of impact of graded tunnel diode on the performance of a dual Junction solar cell. Materials Today: Proceedings, 2017, 4, 12600-12605.	1.8	Ο
14	Improved Cut-off Frequency for Cylindrical Gate TFET Using Source Delta Doping. Procedia Technology, 2016, 25, 450-455.	1.1	21
15	Effect of Wideband Gap Tunnel Diode and Thickness of the Window Layer on the Performance of a Dual Junction Solar Cell. Procedia Technology, 2016, 25, 684-691.	1.1	13
16	Design and modeling of an efficient metamorphic dual-junction InGaP/GaAs solar cell. Optical and Quantum Electronics, 2016, 48, 1.	3.3	11
17	Subthreshold swing minimization of cylindrical tunnel FET using binary metal alloy gate. Superlattices and Microstructures, 2016, 91, 105-111.	3.1	18
18	Analysis of Band Alignment Engineering and Interface Defects on a GaAs/GaSb Heterostructure Solar Cell. Physica Status Solidi (A) Applications and Materials Science, 0, , 2200063.	1.8	1