Francisco J Alfaro-CortÃ©s

List of Publications by Year
in descending order

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51	284	1307594	1199594
papers	citations	7 h-index	12 g-index

52	52	52	166
all docs	docs citations	times ranked	citing authors

#	Article	IF	Citations
1	QoS in InfiniBand subnetworks. IEEE Transactions on Parallel and Distributed Systems, 2004, 15, 810-823.	5.6	38
2	OSR-Lite: Fast and deadlock-free NoC reconfiguration framework. , 2012, , .		22
3	Virtualizing network-on-chip resources in chip-multiprocessors. Microprocessors and Microsystems, 2011, 35, 230-245.	2.8	19
4	VEF Traces: A Framework for Modelling MPI Traffic in Interconnection Network Simulators. , 2015, , .		18
5	N-Dimensional Twin Torus Topology. IEEE Transactions on Computers, 2015, 64, 2847-2861.	3.4	16
6	A New Cost-Effective Technique for QoS Support in Clusters. IEEE Transactions on Parallel and Distributed Systems, 2007, 18, 1714-1726.	5.6	14
7	Self-related traces: An alternative to full-system simulation for NoCs. , 2011, , .		12
8	Building 3D Torus Using Low-Profile Expansion Cards. IEEE Transactions on Computers, 2014, 63, 2701-2715.	3.4	10
9	An open-source family of tools to reproduce MPI-based workloads in interconnection network simulators. Journal of Supercomputing, 2016, 72, 4601-4628.	3.6	10
10	A complete self-testing and self-configuring NoC infrastructure for cost-effective MPSoCs. Transactions on Embedded Computing Systems, 2013, 12, 1-29.	2.9	9
11	Decoupling the Bandwidth and Latency Bounding for Table-based Schedulers. , 0, , .		8
12	Obtaining the optimal configuration of high-radix Combined switches. Journal of Parallel and Distributed Computing, 2013, 73, 1239-1250.	4.1	8
13	Providing quality of service over advanced switching. , 2006, , .		7
14	A low-cost strategy to provide full QoS support in Advanced Switching networks. Journal of Systems Architecture, 2007, 53, 355-368.	4.3	6
15	Efficient Deadline-Based QoS Algorithms for High-Performance Networks. IEEE Transactions on Computers, 2008, 57, 928-939.	3.4	6
16	A Framework to Provide Quality of Service over Advanced Switching. IEEE Transactions on Parallel and Distributed Systems, 2008, 19, 1111-1123.	5.6	6
17	Providing QoS with the Deficit Table Scheduler. IEEE Transactions on Parallel and Distributed Systems, 2010, 21, 327-341.	5.6	6
18	Exploring NoC Virtualization Alternatives in CMPs. , 2012, , .		6

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19	Network-on-Chip virtualization in Chip-Multiprocessor Systems. Journal of Systems Architecture, 2012, 58, 126-139.	4.3	6
20	An integrated solution for QoS provision and congestion management in high-performance interconnection networks using deterministic source-based routing. Journal of Supercomputing, 2013, 66, 284-304.	3.6	6
21	A Switch Architecture Guaranteeing QoS Provision and HOL Blocking Elimination. IEEE Transactions on Parallel and Distributed Systems, 2009, 20, 13-24.	5.6	5
22	C-Switches: Increasing Switch Radix with Current Integration Scale. , 2011, , .		5
23	Performance evaluation of VBR traffic in InfiniBand. , 0, , .		4
24	Implementing the Advanced Switching Minimum Bandwidth Egress Link Scheduler. , 0, , .		4
25	A Formal Model to Manage the InfiniBand Arbitration Tables Providing QoS. IEEE Transactions on Computers, 2007, 56, 1024-1039.	3.4	4
26	Combining HoL-blocking avoidance and differentiated services in high-speed interconnects. , 2014, , .		4
27	NoC Reconfiguration for CMP Virtualization. , 2011, , .		3
28	Evaluation of an Alternative for Increasing Switch Radix. , 2011, , .		3
29	QoS provision in hierarchical and non-hierarchical switch architectures. Journal of Parallel and Distributed Computing, 2021, 148, 138-150.	4.1	3
30	A fast centralized computation routing algorithm for self-configuring NoC systems. , 2011, , .		2
31	Hardware implementation study of several new egress link scheduling algorithms. Journal of Parallel and Distributed Computing, 2012, 72, 975-989.	4.1	2
32	Formalization and configuration methodology for high-radix combined switches. Journal of Supercomputing, 2014, 69, 1410-1444.	3.6	2
33	Evaluating several implementations for the AS Minimum Bandwidth Egress Link Scheduler. Computer Communications and Networks (IC3N), Proceedings of the IEEE International Conference on, 2006, , .	0.0	1
34	Deadline-based QoS Algorithms for High-performance Networks. , 2007, , .		1
35	Optimal Configuration of High-Radix Combined Switches. , 2012, , .		1
36	Providing Differentiated Services, Congestion Management, and Deadlock Freedom in Dragonfly Networks. , 2016, , .		1

#	ARTICLE	IF	CITATIONS
37	Adaptive routing for n-Dimensional Twin torus. IEEE Transactions on Computers, 2016, , 1-1.	3.4	1
38	A methodology to enable QoS provision on InfiniBand hardware. Journal of Supercomputing, 2021, 77, 9934-9946.	3.6	1
39	UPR: deadlock-free dynamic network reconfiguration by exploiting channel dependency graph compatibility. Journal of Supercomputing, 2021, 77, 12826-12856.	3.6	1
40	Hardware Implementation Study of the SCFQ-CA and DRR-CA Scheduling Algorithms. Lecture Notes in Computer Science, 2009, , 1089-1100.	1.3	1
41	Full QoS Support with 2 VCs for Single-chip Switches. , 0, , .		0
42	Scalable low-cost QoS support for single-chip switches. , 2006, , .		0
43	Efficient Switches with QoS Support for Clusters. , 2007, , .		0
44	Comparing the latency performance of the DTable and DRR schedulers. , 2007, , .		0
45	Workshop 9 introduction: The workshop on communication architecture for clusters - CAC 2008. , 2008, , .		0
46	Hardware Implementation Study of the Deficit Table Egress Link Scheduling Algorithm. , 2009, , .		0
47	A new strategy to manage the InfiniBand arbitration tables. Journal of Parallel and Distributed Computing, 2009, 69, 508-520.	4.1	0
48	Deadlock-free routing mechanism for 3D twin torus networks. , 2014, , .		0
49	Optimal Configuration for N-Dimensional Twin Torus Networks. , 2014, , .		0
50	Optimizing the configuration of combined high-radix switches. Journal of Supercomputing, 2015, 71, 2614-2643.	3.6	0
51	Providing quality of service in omni-path networks. Journal of Supercomputing, 0, , 1.	3.6	0