Yu-Hsin Chen

List of Publications by Year
in descending order

13 papers	6,225 citations	1163117 8 h-index	1588992 8 g-index
13 all docs	13 docs citations	13 times ranked	4879 citing authors

#	Article	IF	Citations
1	Efficient Processing of Deep Neural Networks: A Tutorial and Survey. Proceedings of the IEEE, 2017, 105, 2295-2329.	21.3	2,217
2	Eyeriss: An Energy-Efficient Reconfigurable Accelerator for Deep Convolutional Neural Networks. IEEE Journal of Solid-State Circuits, 2017, 52, 127-138.	5.4	1,877
3	Eyeriss. Computer Architecture News, 2016, 44, 367-379.	2.5	833
4	Eyeriss v2: A Flexible Accelerator for Emerging Deep Neural Networks on Mobile Devices. IEEE Journal on Emerging and Selected Topics in Circuits and Systems, 2019, 9, 292-308.	3.6	609
5	Designing Energy-Efficient Convolutional Neural Networks Using Energy-Aware Pruning. , 2017, , .		374
6	Efficient Processing of Deep Neural Networks. Synthesis Lectures on Computer Architecture, 2020, 15, 1-341.	1.3	72
7	A Monolithically-Integrated Chip-to-Chip Optical Link in Bulk CMOS. IEEE Journal of Solid-State Circuits, 2015, 50, 828-844.	5.4	65
8	Heterogeneous Dataflow Accelerators for Multi-DNN Workloads. , 2021, , .		52
9	How to Evaluate Deep Neural Network Processors: TOPS/W (Alone) Considered Harmful. IEEE Solid-State Circuits Magazine, 2020, 12, 28-41.	0.4	40
10	Towards closing the energy gap between HOG and CNN features for embedded vision. , 2017, , .		35
11	A Deeply Pipelined CABAC Decoder for HEVC Supporting Level 6.2 High-Tier Applications. IEEE Transactions on Circuits and Systems for Video Technology, 2015, 25, 856-868.	8.3	34
12	Integration of silicon photonics in bulk CMOS. , 2014, , .		15
13	Scalable electrical-optical thermal simulator for multicores with optical interconnects. , 2013, , .		2