

Jagannathan Ramanujam

List of Publications by Year in descending order

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36
papers

1,088
citations

471509

17
h-index

454955

30
g-index

36
all docs

36
docs citations

36
times ranked

615
citing authors

#	ARTICLE	IF	CITATIONS
1	Strengthening spatial reasoning: elucidating the attentional and neural mechanisms associated with mental rotation skill development. <i>Cognitive Research: Principles and Implications</i> , 2020, 5, 20.	2.0	10
2	Coarse and Fine Visual Attention Strategies during a 3D Mental Rotation Task. <i>Journal of Vision</i> , 2018, 18, 1203.	0.3	0
3	Gaslight: A comprehensive fuzzing architecture for memory forensics frameworks. <i>Digital Investigation</i> , 2017, 22, S86-S93.	3.2	6
4	GeauxDock: Accelerating Structure-Based Virtual Screening with Heterogeneous Computing. <i>PLoS ONE</i> , 2016, 11, e0158898.	2.5	22
5	Assessing the similarity of ligand binding conformations with the Contact Mode Score. <i>Computational Biology and Chemistry</i> , 2016, 64, 403-413.	2.3	45
6	GeauxDock: A novel approach for mixed-resolution ligand docking using a descriptor-based force field. <i>Journal of Computational Chemistry</i> , 2015, 36, 2013-2026.	3.3	10
7	DA-TC: a novel application execution model in multicluster systems. <i>Cluster Computing</i> , 2014, 17, 371-387.	5.0	1
8	Parallel tempering simulation of the three-dimensional Edwards-Anderson model with compact asynchronous multispin coding on GPU. <i>Computer Physics Communications</i> , 2014, 185, 2467-2478.	7.5	17
9	CODE SIZE REDUCTION FOR ARRAY INTENSIVE APPLICATIONS ON DIGITAL SIGNAL PROCESSORS. <i>Journal of Circuits, Systems and Computers</i> , 2012, 21, 1250015.	1.5	0
10	Empirical performance model-driven data layout optimization and library call selection for tensor contraction expressions. <i>Journal of Parallel and Distributed Computing</i> , 2012, 72, 338-352.	4.1	13
11	Loop transformations. <i>ACM SIGPLAN Notices</i> , 2011, 46, 549-562.	0.2	32
12	Decoupling interaction hardware design using libraries of reusable electronics. , 2009, , .		14
13	Compiler-assisted dynamic scheduling for effective parallelization of loop nests on multicore processors. <i>ACM SIGPLAN Notices</i> , 2009, 44, 219-228.	0.2	19
14	Performance Optimization of Tensor Contraction Expressions for Many-Body Methods in Quantum Chemistry. <i>Journal of Physical Chemistry A</i> , 2009, 113, 12715-12723.	2.5	24
15	An innovative application execution toolkit for multicluster grids. , 2009, , .		2
16	Automatic code generation for many-body electronic structure methods: the tensor contraction engine. <i>Molecular Physics</i> , 2006, 104, 211-228.	1.7	104
17	Compiler-directed scratch pad memory optimization for embedded multiprocessors. <i>IEEE Transactions on Very Large Scale Integration (VLSI) Systems</i> , 2004, 12, 281-287.	3.1	36
18	Reducing false sharing and improving spatial locality in a unified compilation framework. <i>IEEE Transactions on Parallel and Distributed Systems</i> , 2003, 14, 337-354.	5.6	4

#	ARTICLE	IF	CITATIONS
19	An I/O-Conscious Tiling Strategy for Disk-Resident Data Sets. <i>Journal of Supercomputing</i> , 2002, 21, 257-284.	3.6	8
20	A layout-conscious iteration space transformation technique. <i>IEEE Transactions on Computers</i> , 2001, 50, 1321-1335.	3.4	21
21	Static and dynamic locality optimizations using integer linear programming. <i>IEEE Transactions on Parallel and Distributed Systems</i> , 2001, 12, 922-941.	5.6	9
22	Compiler Algorithms for Optimizing Locality and Parallelism on Shared and Distributed-Memory Machines. <i>Journal of Parallel and Distributed Computing</i> , 2000, 60, 924-965.	4.1	8
23	A unified framework for optimizing locality, parallelism, and communication in out-of-core computations. <i>IEEE Transactions on Parallel and Distributed Systems</i> , 2000, 11, 648-668.	5.6	20
24	Minimizing data and synchronization costs in one-way communication. <i>IEEE Transactions on Parallel and Distributed Systems</i> , 2000, 11, 1232-1251.	5.6	9
25	A Matrix-Based Approach to Global Locality Optimization. <i>Journal of Parallel and Distributed Computing</i> , 1999, 58, 190-235.	4.1	15
26	Improving cache locality by a combination of loop and data transformations. <i>IEEE Transactions on Computers</i> , 1999, 48, 159-167.	3.4	96
27	Compilation techniques for out-of-core parallel computations. <i>Parallel Computing</i> , 1998, 24, 597-628.	2.1	19
28	Efficient algorithms for array redistribution. <i>IEEE Transactions on Parallel and Distributed Systems</i> , 1996, 7, 587-594.	5.6	61
29	Efficient Computation of Address Sequences in Data Parallel Programs Using Closed Forms for Basis Vectors. <i>Journal of Parallel and Distributed Computing</i> , 1996, 38, 188-203.	4.1	26
30	Compilation and Communication Strategies for Out-of-Core Programs on Distributed Memory Machines. <i>Journal of Parallel and Distributed Computing</i> , 1996, 38, 277-288.	4.1	10
31	Beyond unimodular transformations. <i>Journal of Supercomputing</i> , 1995, 9, 365-389.	3.6	27
32	Tiling multidimensional iteration spaces for multicomputers. <i>Journal of Parallel and Distributed Computing</i> , 1992, 16, 108-120.	4.1	138
33	Iteration Space Tiling for Distributed Memory Machines. <i>Advances in Parallel Computing</i> , 1992, 3, 255-270.	0.3	4
34	Compile-time techniques for data distribution in distributed memory machines. <i>IEEE Transactions on Parallel and Distributed Systems</i> , 1991, 2, 472-482.	5.6	130
35	Cluster partitioning approaches to mapping parallel programs onto a hypercube. <i>Parallel Computing</i> , 1990, 13, 1-16.	2.1	62
36	Task allocation onto a hypercube by recursive mincut bipartitioning. <i>Journal of Parallel and Distributed Computing</i> , 1990, 10, 35-44.	4.1	66