

# K Akshay

## List of Publications by Year in descending order

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9  
papers

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citations

2682572

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2272923

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#	ARTICLE	IF	CITATIONS
1	Quick Design of a Superjunction Considering Charge Imbalance Due to Process Variations. IEEE Transactions on Electron Devices, 2020, 67, 3024-3029.	3.0	7
2	Charge Sheet Super Junction in 4H-Silicon Carbide: Practicability, Modeling and Design. IEEE Journal of the Electron Devices Society, 2020, 8, 1129-1137.	2.1	4
3	An algorithm to design floating field rings in SiC and Si power diodes and MOSFETs. Solid-State Electronics, 2019, 156, 73-78.	1.4	2
4	Analytical Modeling of Response Time and Full Well Capacity of a Pinned Photo Diode. , 2018, , .		1
5	Optimum Aspect Ratio of Superjunction Pillars Considering Charge Imbalance. IEEE Transactions on Electron Devices, 2021, 68, 1798-1803.	3.0	1
6	Improved theoretical minimum of the specific on-resistance of a superjunction. Semiconductor Science and Technology, 2020, 36, 015021.	2.0	1
7	Optimum length of a pinned photo diode. , 2017, , .		0
8	System Level Design of a Novel CMOS Image Sensor with High Dynamic Range and Fill Factor. , 2019, , .		0
9	Note Clarifying the Paper, "Charge Sheet Super Junction in 4H-Silicon Carbide: Practicability, Modeling and Design" IEEE Journal of the Electron Devices Society, 2020, 8, 1315-1316.	2.1	0