

Kun-You Lin

List of Publications by Year in descending order

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111
papers

2,401
citations

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docs citations

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1515
citing authors

#	ARTICLE	IF	CITATIONS
1	Wideband Reconfigurable Power Divider/Combiner in 40-nm CMOS for 5G mmW Beamforming System. IEEE Transactions on Microwave Theory and Techniques, 2022, 70, 1410-1422.	2.9	5
2	38-GHz Phased Array Transmitter and Receiver Based on Scalable Phased Array Modules With Endfire Antenna Arrays for 5G MMW Data Links. IEEE Transactions on Microwave Theory and Techniques, 2021, 69, 980-999.	2.9	53
3	A 28-GHz Bidirectional Active Gilbert-Cell Mixer in 90-nm CMOS. IEEE Microwave and Wireless Components Letters, 2021, 31, 473-476.	2.0	21
4	A 28/39 GHz Dual-Band Power Amplifier Using Optimal Matching Contour in GaAs pHEMT. , 2021, , .		3
5	Modified Binomial Power Distribution Beamformer for Switched-Beam Circular Array. , 2020, , .		0
6	A 38-GHz 32-Element Phased-Array Transmitter Based on Scalable 8-Element Phased-Array Modules for 5G MMW Data Links. , 2020, , .		1
7	A Dual-Band CMOS Standing-Wave Digitally Controlled Oscillator for Automotive Radars. , 2019, , .		3
8	A Compact E- Mode GaAs pHEMT Phase Shifter MMIC for 5G Phased-Array Systems. , 2019, , .		6
9	A High-Gain Continuous Class- F Power Amplifier in 90-nm CMOS for 5G Communication. , 2019, , .		3
10	A Wideband, Low-Noise, and High-Resolution Digitally-Controlled Oscillator for SDR Applications. , 2018, , .		4
11	A Light Weight Transponder for Bee Searching Harmonic Radar. , 2018, , .		3
12	A K-Band High-Gain Linear CMOS Mixer with Current-Bleeding Neutralization Technique. , 2018, , .		8
13	Millimeter-wave system-on-chip advancement for fusion plasma diagnostics. Review of Scientific Instruments, 2018, 89, 10H108.	0.6	10
14	Modularized prototype of 5G mmWave base station system at 38 GHz. , 2018, , .		2
15	A 40-nm CMOS V-band single-pole quadruple-throw absorptive switch for phased-array applications. , 2017, , .		2
16	Application of harmonic radar on the research of bees' behavior. , 2016, , .		1
17	A 77-GHz 2T6R Transceiver With Injection-Lock Frequency Sextupler Using 65-nm CMOS for Automotive Radar System Application. IEEE Transactions on Microwave Theory and Techniques, 2016, 64, 3031-3048.	2.9	55
18	A 60 GHz variable-gain low-noise amplifier with low phase variation. , 2016, , .		15

#	ARTICLE	IF	CITATIONS
19	An X-band MMIC HBT high efficiency power amplifier with lossless feedback technique. , 2016, , .		2
20	A 20 GHz power amplifier with IM3 distortion cancellation by load-split derivative superposition. , 2016, , .		2
21	Bee Searching Radar With High Transmit/Receive Isolation Using Pulse Pseudorandom Code. IEEE Transactions on Microwave Theory and Techniques, 2016, 64, 4324-4335.	2.9	13
22	K-band active antenna integrated with CMOS adaptive-bias power amplifier. , 2015, , .		5
23	Portable 9.4/18.8 GHz harmonic radar system using pulse Pseudorandom code principle. , 2015, , .		6
24	A 24 GHz CMOS power amplifier with successive IM2 feed-forward IMD3 cancellation. , 2015, , .		8
25	Signal processing for harmonic pulse radar based on spread spectrum technology. IET Radar, Sonar and Navigation, 2014, 8, 242-250.	0.9	18
26	A 190-GHz amplifier with gain-boosting technique in 65-nm CMOS. , 2014, , .		3
27	A π -band High LO-to-RF Isolation Triple Cascode Mixer With Wide IF Bandwidth. IEEE Transactions on Microwave Theory and Techniques, 2014, 62, 1506-1514.	2.9	18
28	Phase-Delay Cold-FET Pre-Distortion Linearizer for Millimeter-Wave CMOS Power Amplifiers. IEEE Transactions on Microwave Theory and Techniques, 2013, 61, 4505-4519.	2.9	37
29	A High-Range-Accuracy and High-Sensitivity Harmonic Radar Using Pulse Pseudorandom Code for Bee Searching. IEEE Transactions on Microwave Theory and Techniques, 2013, 61, 666-675.	2.9	83
30	A 60 GHz CMOS power amplifier with modified pre-distortion linearizer. , 2013, , .		3
31	A 75.5-to-120.5-GHz, high-gain CMOS low-noise amplifier. , 2012, , .		14
32	A 57–64 GHz low-phase-variation variable-gain amplifier. , 2012, , .		5
33	MM-Wave Integration and Combinations. IEEE Microwave Magazine, 2012, 13, 49-57.	0.7	10
34	Design and Analysis of Down-Conversion Gate/Base-Pumped Harmonic Mixers Using Novel Reduced-Size 180° Hybrid With Different Input Frequencies. IEEE Transactions on Microwave Theory and Techniques, 2012, 60, 2473-2485.	2.9	23
35	60-GHz Four-Element Phased-Array Transmit/Receive System-in-Package Using Phase Compensation Techniques in 65-nm Flip-Chip CMOS Process. IEEE Transactions on Microwave Theory and Techniques, 2012, 60, 743-756.	2.9	177
36	Design considerations for radio frequency 3DICs. , 2012, , .		0

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37	A 27–34 GHz CMOS medium power amplifier with a flat power performance. , 2012, , .		5
38	An active CMOS one-to-four power splitter for 60-GHz phased-array transmitter. , 2012, , .		3
39	A V-band CMOS frequency quadrupler with 3-dBm output power. , 2012, , .		10
40	A high range resolution 9.4/18.8 GHz harmonic radar for bees searching. , 2012, , .		5
41	A Modified Wilkinson Power Divider With Isolation Bandwidth Improvement. IEEE Transactions on Microwave Theory and Techniques, 2012, 60, 2768-2780.	2.9	72
42	Development of millimeter-wave CMOS power amplifiers at National Taiwan University. , 2012, , .		7
43	A 17â€“35 GHz Broadband, High Efficiency PHEMT Power Amplifier Using Synthesized Transformer Matching Technique. IEEE Transactions on Microwave Theory and Techniques, 2012, 60, 112-119.	2.9	37
44	A 50–70 GHz I/Q modulator with improved sideband suppression using HPF/LPF based quadrature power splitter. , 2011, , .		5
45	Design and Analysis of Novel Linearization Technique of Cascode Cell in a 60-GHz CMOS Demodulator. IEEE Transactions on Microwave Theory and Techniques, 2011, 59, 456-465.	2.9	13
46	A 60-GHz Frequency Tripler With Gain and Dynamic-Range Enhancement. IEEE Transactions on Microwave Theory and Techniques, 2011, 59, 660-671.	2.9	27
47	Novel High Robustness RF ESD Protection Circuits Applied to 5.8-GHz GaAs-Based HBT Amplifiers. IEEE Transactions on Microwave Theory and Techniques, 2011, 59, 687-698.	2.9	3
48	A 60-GHz single-ended-to-differential vector sum phase shifter in CMOS for phased-array receiver. , 2011, , .		1
49	A 22-dBm 24-GHz power amplifier using 0.18-âµm CMOS technology. , 2010, , .		15
50	A 4â€“17 GHz Darlington Cascode Broadband Medium Power Amplifier in 0.18-âµm CMOS Technology. IEEE Microwave and Wireless Components Letters, 2010, 20, 43-45.	2.0	33
51	A Power Bus With Multiple Via Ground Surface Perturbation Lattices for Broadband Noise Isolation: Modeling and Application in RF-SiP. IEEE Transactions on Advanced Packaging, 2010, 33, 582-591.	1.7	15
52	A 2.655 GHz 3-stage Doherty power amplifier using envelope tracking technique. , 2010, , .		9
53	A High-Efficiency, Broadband CMOS Power Amplifier for Cognitive Radio Applications. IEEE Transactions on Microwave Theory and Techniques, 2010, , .	2.9	26
54	A 22.5-dB gain, 20.1-dBm output power K-band power amplifier in 0.18-âµm CMOS. , 2010, , .		15

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55	60 GHz Double-Balanced Gate-Pumped Down-Conversion Mixers With a Combined Hybrid on 130 nm CMOS Processes. IEEE Microwave and Wireless Components Letters, 2010, 20, 160-162.	2.0	13
56	A K-band CMOS low power modified colpitts VCO using transformer feedback. , 2009, , .		14
57	A compact low DC consumption 24-GHz Cascode HEMT VGA. , 2009, , .		4
58	Design and Analysis for a 60-GHz Low-Noise Amplifier With RF ESD Protection. IEEE Transactions on Microwave Theory and Techniques, 2009, 57, 298-305.	2.9	50
59	Design and Analysis of a 0.8–77.5-GHz Ultra-Broadband Distributed Drain Mixer Using 0.13- μm CMOS Technology. IEEE Transactions on Microwave Theory and Techniques, 2009, 57, 562-572.	2.9	43
60	Analysis and Design of Reduced-Size Marchand Rat-Race Hybrid for Millimeter-Wave Compact Balanced Mixers in 130-nm CMOS Process. IEEE Transactions on Microwave Theory and Techniques, 2009, 57, 1966-1977.	2.9	24
61	Ring-Based Triple-Push VCOs With Wide Continuous Tuning Ranges. IEEE Transactions on Microwave Theory and Techniques, 2009, 57, 2173-2183.	2.9	25
62	A Wide Tuning Range Voltage Controlled Oscillator Using Common-Base Configuration and Inductive Feedback. IEEE Microwave and Wireless Components Letters, 2009, 19, 653-655.	2.0	10
63	Millimeter-Wave Low Power and Miniature CMOS Multicascode Low-Noise Amplifiers with Noise Reduction Topology. IEEE Transactions on Microwave Theory and Techniques, 2009, 57, 3049-3059.	2.9	102
64	MMICs in the millimeter-wave regime. IEEE Microwave Magazine, 2009, 10, 99-117.	0.7	41
65	A Broadband Balanced Distributed Frequency Doubler With a Sharing Collector Line. IEEE Microwave and Wireless Components Letters, 2009, 19, 110-112.	2.0	21
66	A 2–40 GHz Active Balun Using 0.13 μm CMOS Process. IEEE Microwave and Wireless Components Letters, 2009, 19, 164-166.	2.0	44
67	A K -Band CMOS Distributed Doubler With Current-Reuse Technique. IEEE Microwave and Wireless Components Letters, 2009, 19, 308-310.	2.0	28
68	A 24 GHz low power VCO with transformer feedback. , 2009, , .		16
69	An ultra-low-power CMOS complementary VCO using three-coil transformer feedback. , 2009, , .		6
70	A 71–76 GHz chip set for wireless communication in 65-nm CMOS technology. , 2009, , .		7
71	A 50 to 70 GHz Power Amplifier Using 90 nm CMOS Technology. IEEE Microwave and Wireless Components Letters, 2009, 19, 45-47.	2.0	38
72	A 68–83 GHz power amplifier in 90 nm CMOS. , 2009, , .		14

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73	A miniature Q-band CMOS LNA with triple-cascode topology. , 2009, , .		6
74	A V-band power amplifier in 0.13-um CMOS (invited paper). , 2008, , .		0
75	A 10-35 GHz Low Power Bulk-Driven Mixer Using 0.13-um CMOS Process. IEEE Microwave and Wireless Components Letters, 2008, 18, 455-457.	2.0	25
76	A 60-GHz Single-Chip Transceiver for WPAN Applications. , 2008, , .		5
77	40-48 GHz Sub-harmonic Transceiver for High Data-Rate Communication System Applications. , 2008, , .		2
78	A 1.5-9.6 GHz Monolithic Active Quasi-Circulator in 0.18-um CMOS Technology. IEEE Microwave and Wireless Components Letters, 2008, 18, 797-799.	2.0	41
79	A Compact 60 GHz Integrated Up-Converter Using Miniature Transformer Couplers With 5 dB Conversion Gain. IEEE Microwave and Wireless Components Letters, 2008, 18, 641-643.	2.0	22
80	A novel ring-based triple-push 0.2-to-34 GHz VCO in 0.13-um CMOS technology. , 2008, , .		4
81	A GaAs-based HBT 31-GHz frequency doubler with an on-chip voltage. , 2008, , .		2
82	A Q-band low loss reduced-size filter-integrated SPDT switch using 0.15-um MHEMT technology. , 2008, , .		2
83	Low Insertion-Loss Single-Pole-Double-Throw Reduced-Size Quarter-Wavelength HEMT Bandpass Filter Integrated Switches. IEEE Transactions on Microwave Theory and Techniques, 2008, 56, 3028-3038.	2.9	20
84	A 14-23 GHz CMOS MMIC distributed doubler with a 22-dB fundamental rejection. , 2008, , .		14
85	A 40-to-76 GHz Balanced Distributed Doubler in 0.13-um CMOS Technology. , 2008, , .		10
86	A 60-GHz single-balance gate-pumped down-conversion mixer with reduced-size rat-race hybrid on 130-nm CMOS process. , 2008, , .		6
87	A 35-50 GHz IQ-Demodulator in 0.13-um CMOS Technology. , 2007, , .		4
88	A 10.8-GHz CMOS Low-Noise Amplifier Using Parallel-Resonant Inductor. , 2007, , .		13
89	A 60GHz Low-Power Six-Port Transceiver for Gigabit Software-Defined Transceiver Applications. , 2007, , .		65
90	Design and Analysis of Stacked Power Amplifier in Series-Input and Series-Output Configuration. IEEE Transactions on Microwave Theory and Techniques, 2007, 55, 2802-2812.	2.9	32

#	ARTICLE	IF	CITATIONS
91	40-GHz MMIC SPDT and Multiple-Port Bandpass Filter-Integrated Switches. IEEE Transactions on Microwave Theory and Techniques, 2007, 55, 2691-2699.	2.9	20
92	Analysis of Multiconductor Coupled-Line Marchand Baluns for Miniature MMIC Design. IEEE Transactions on Microwave Theory and Techniques, 2007, 55, 1190-1199.	2.9	80
93	Analysis and Design of Bandpass Single-Pole-Double-Throw FET Filter-Integrated Switches. IEEE Transactions on Microwave Theory and Techniques, 2007, 55, 1601-1610.	2.9	36
94	A noise optimization formulation for CMOS low-noise amplifiers with on-chip low-Q inductors. IEEE Transactions on Microwave Theory and Techniques, 2006, 54, 1554-1560.	2.9	40
95	A low-voltage and variable-gain distributed amplifier for 3.1-10.6 GHz UWB systems. IEEE Microwave and Wireless Components Letters, 2006, 16, 179-181.	2.0	24
96	Design and analysis for a miniature CMOS SPDT switch using body-floating technique to improve power performance. IEEE Transactions on Microwave Theory and Techniques, 2006, 54, 31-39.	2.9	182
97	FET-integrated CPW and the application in filter synthesis design method on traveling-wave switch above 100 GHz. IEEE Transactions on Microwave Theory and Techniques, 2006, 54, 2090-2097.	2.9	25
98	Low-Power Low-Noise Amplifiers for UWB Applications. , 2006, , .		1
99	A Ku-band CMOS low-noise amplifier. , 2005, , .		16
100	A 24-GHz 3.9-dB NF low-noise amplifier using 0.18 μm CMOS technology. IEEE Microwave and Wireless Components Letters, 2005, 15, 448-450.	2.0	85
101	Millimeter-Wave MMIC Passive HEMT Switches Using Traveling-Wave Concept. IEEE Transactions on Microwave Theory and Techniques, 2004, 52, 1798-1808.	2.9	82
102	Millimeter-wave MMIC single-pole-double-throw passive HEMT switches using impedance-transformation networks. IEEE Transactions on Microwave Theory and Techniques, 2003, 51, 1076-1085.	2.9	38
103	A W-band GCPW MMIC Diode Tripler. , 2002, , .		14
104	K-Band monolithic GaAs PHEMT amplifiers. , 0, , .		2
105	A V-Band MMIC SPDT passive HEMT switch using impedance transformation networks. , 0, , .		3
106	On the stability of millimeter-wave power amplifiers. , 0, , .		10
107	A Millimeter-Wave Wideband SPDT Switch with Traveling-Wave Concept Using 0.13-Microm CMOS Process. , 0, , .		11
108	18-26 GHz low-noise amplifiers using 130- and 90-nm bulk CMOS technologies. , 0, , .		16

#	ARTICLE	IF	CITATIONS
109	A 5.4-mW LNA using 0.35- μ m SiGe BiCMOS technology for 3.1-10.6-GHz UWB wireless receivers. , 0, , .		12
110	A 10.8-mW low-noise amplifier in 0.35- μ m SiGe BiCMOS for UWB wireless receivers. , 0, , .		2
111	Millimeter-Wave Integrated Circuits. , 0, , .		6