

Igor Aleksejev

List of Publications by Year in descending order

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17
papers

61
citations

3311381

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2550090

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17
all docs

17
docs citations

17
times ranked

34
citing authors

#	ARTICLE	IF	CITATIONS
1	Ways for board and system test to benefit from FPGA embedded instrumentation. , 2019, , .		1
2	Run-time reconfigurable instruments for advanced board-level testing. IEEE Instrumentation and Measurement Magazine, 2017, 20, 23-30.	1.6	1
3	Embedded instrumentation toolbox for screening marginal defects and outliers for production. , 2017, , .		1
4	Run-time reconfigurable instruments for advanced board-level testing. , 2016, , .		3
5	Optimization of Boundary Scan Tests Using FPGA-Based Efficient Scan Architectures. Journal of Electronic Testing: Theory and Applications (JETTA), 2016, 32, 245-255.	1.2	1
6	On coverage of timing related faults at board level. , 2016, , .		3
7	Complex delay fault reasoning with sequential 7-valued algebra. , 2015, , .		0
8	Virtual reconfigurable scan-chains on FPGAs for optimized board test. , 2015, , .		5
9	Embedded synthetic instruments for Board-Level testing. , 2012, , .		3
10	FPGA-based synthetic instrumentation for board test. , 2012, , .		17
11	Fast extended test access via JTAG and FPGAs. , 2009, , .		13
12	Turning JTAG inside out for fast extended test access. , 2009, , .		4
13	Teaching digital test with BIST analyzer. , 2008, , .		2
14	Reseeding using compaction of pre-generated LFSR sub-sequences. , 2008, , .		2
15	Application of Sequential Test Set Compaction to LFSR Reseeding. , 2008, , .		2
16	Optimization of the Store-and-Generate Based Built-in Self-Test. International Biennial Baltic Electronics Conference, 2006, , .	0.0	3
17	Sequential Test Set Compaction in LFSR Reseeding. Advances in Computer and Electrical Engineering Book Series, 0, , 476-493.	0.3	0