

Jagabar Sathik

List of Publications by Year in Descending Order

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The third column is the impact factor (IF) of the journal, and the fourth column is the number of citations of the article.

79
papers

1,013
citations

17
h-index

29
g-index

87
ext. papers

1,601
ext. citations

3.1
avg, IF

5.47
L-index

#	Paper	IF	Citations
79	A review on segregation of various high gain converter configurations for distributed energy sources. <i>AEJ - Alexandria Engineering Journal</i> , 2022 , 61, 675-700	6.1	3
78	Common ground type five level inverter with voltage boosting for PV applications.. <i>Scientific Reports</i> , 2022 , 12, 4924	4.9	1
77	Reduced component high voltage boost single-source switched capacitor inverter. <i>Sadhana - Academy Proceedings in Engineering Sciences</i> , 2022 , 47, 1	1	0
76	Dual Boost Five-Level Switched-Capacitor Inverter with Common Ground. <i>IEEE Transactions on Circuits and Systems II: Express Briefs</i> , 2022 , 1-1	3.5	0
75	Compact Quadratic Boost Switched-Capacitor Inverter. <i>IEEE Transactions on Industry Applications</i> , 2022 , 1-1	4.3	0
74	Dynamic Analysis and Reduced- Order Modeling Techniques for Power Converters in DC Microgrid 2022 , 241-272		
73	Multilevel Converters and Applications 2022 , 229-324		
72	A Multilevel Inverter Topology Using Diode Half-Bridge Circuit with Reduced Power Component. <i>Energies</i> , 2021 , 14, 7249	3.1	4
71	A new seven level boost-type ANPC inverter topology for photovoltaic applications. <i>Scientific Reports</i> , 2021 , 11, 22487	4.9	2
70	Experimental validation of nine-level switched-capacitor inverter topology with high voltage gain. <i>International Journal of Circuit Theory and Applications</i> , 2021 , 49, 2479	2	3
69	Experimental validation of new self-voltage balanced 9L-ANPC inverter for photovoltaic applications. <i>Scientific Reports</i> , 2021 , 11, 5067	4.9	5
68	A new generalized cascade multilevel converter topology and its improved modulation technique. <i>International Journal of Circuit Theory and Applications</i> , 2021 , 49, 1103-1120	2	5
67	Compact Seven-Level Boost Type Inverter Topology. <i>IEEE Transactions on Circuits and Systems II: Express Briefs</i> , 2021 , 68, 1358-1362	3.5	11
66	Design of nine step switched capacitor multilevel inverter and its cascaded extension. <i>International Journal of Circuit Theory and Applications</i> , 2021 , 49, 1182-1201	2	2
65	A Single-Stage Common Ground Type Transformerless Five-Level Inverter Topology. <i>IEEE Journal of Emerging and Selected Topics in Power Electronics</i> , 2021 , 1-1	5.6	7
64	Modified LUO High Gain DC-DC Converter With Minimal Capacitor Stress for Electric Vehicle Application. <i>IEEE Access</i> , 2021 , 9, 122335-122350	3.5	4
63	Evaluation of DG impacts on distribution networks 2021 , 195-209		

62	A case study with power quality analysis on building integrated PV (BIPV) system 2021 , 541-562		
61	Expandable transformer-less high-gain dc/dc converter based on quasi-Z source and multiplier cells. <i>Sadhana - Academy Proceedings in Engineering Sciences</i> , 2021 , 46, 1	1	0
60	A Five-Level Boosting Inverter for PV Application. <i>IEEE Journal of Emerging and Selected Topics in Power Electronics</i> , 2021 , 9, 5016-5025	5.6	4
59	A New Multilevel Inverter Topology with Reduced DC Sources. <i>Energies</i> , 2021 , 14, 4709	3.1	5
58	A Unique Unified Wind Speed Approach to Decision-Making for Dispersed Locations. <i>Sustainability</i> , 2021 , 13, 9340	3.6	3
57	Modified Selective Harmonics Mitigation PWM for a Switched Diode Multilevel Inverter. <i>ECS Journal of Solid State Science and Technology</i> , 2021 , 10, 091015	2	
56	Switched Capacitor-Based 13L Inverter Topology for High-Frequency AC Power Distribution System. <i>IEEE Journal of Emerging and Selected Topics in Power Electronics</i> , 2021 , 9, 5883-5894	5.6	6
55	Reliability Analysis of Power Components in Restructured DC/DC Converters. <i>IEEE Transactions on Device and Materials Reliability</i> , 2021 , 1-1	1.6	0
54	Seven Level T-Type Switched Capacitor Inverter Topology for PV Applications. <i>IEEE Access</i> , 2021 , 9, 85049-85059	4.3	3
53	A New Hybrid Zeta-Boost Converter With Active Quad Switched Inductor for High Voltage Gain. <i>IEEE Access</i> , 2021 , 9, 20022-20034	3.5	2
52	Performance improvement of solar PV power conversion system through low duty cycle DC-DC converter. <i>International Journal of Circuit Theory and Applications</i> , 2021 , 49, 267-282	2	9
51	Optimal Dynamic Scheduling of Electric Vehicles in a Parking Lot Using Particle Swarm Optimization and Shuffled Frog Leaping Algorithm. <i>Energies</i> , 2020 , 13, 6384	3.1	4
50	Improved R-Type seven-level switched capacitor inverter topology with Self-voltage balancing. <i>International Journal of Circuit Theory and Applications</i> , 2020 , 48, 1800-1819	2	9
49	Common-Ground-Type Five-Level Transformerless Inverter Topology With Full DC-Bus Utilization. <i>IEEE Transactions on Industry Applications</i> , 2020 , 1-1	4.3	9
48	A novel cross-connected multilevel inverter topology for higher number of voltage levels with reduced switch count. <i>International Transactions on Electrical Energy Systems</i> , 2020 , 30, e12381	2.2	16
47	Switched-capacitor multilevel inverter with self-voltage-balancing for high-frequency power distribution system. <i>IET Power Electronics</i> , 2020 , 13, 1807-1818	2.2	10
46	Modified R-Type multilevel inverter topology with reduced switches, DC sources, and power loss. <i>International Transactions on Electrical Energy Systems</i> , 2020 , 30, e12345	2.2	3
45	An Original Hybrid Multilevel DC-AC Converter Using Single-Double Source Unit for Medium Voltage Applications: Hardware Implementation and Investigation. <i>IEEE Access</i> , 2020 , 8, 71291-71301	3.5	7

44	Switched-capacitor-based boost multilevel inverter topology with higher voltage gain. <i>IET Power Electronics</i> , 2020 , 13, 3209-3212	2.2	9
43	A New Compact Multilevel Inverter Design with Less Power Electronics Component. <i>Advances in Intelligent Systems and Computing</i> , 2020 , 661-669	0.4	
42	A Reduced Switch Count Boost Inverter (RSC- BI) Topology with Triple Voltage Gain 2020 ,		2
41	New switched-capacitor-based boost inverter topology with reduced switch count. <i>Journal of Power Electronics</i> , 2020 , 20, 926-937	0.9	7
40	Design and implementation of a new unity gain nine-level active neutral point clamped multilevel inverter topology. <i>IET Power Electronics</i> , 2020 , 13, 3204-3208	2.2	10
39	A new generalized switched diode multilevel inverter topology with reduced switch count and voltage on switches. <i>International Journal of Circuit Theory and Applications</i> , 2020 , 48, 619-637	2	12
38	High Gain Active Neutral Point Clamped Seven-Level Self-Voltage Balancing Inverter. <i>IEEE Transactions on Circuits and Systems II: Express Briefs</i> , 2020 , 67, 2567-2571	3.5	27
37	A Single DC Source Nine-Level Switched-Capacitor Boost Inverter Topology With Reduced Switch Count. <i>IEEE Access</i> , 2020 , 8, 5840-5851	3.5	36
36	New Grid-Connected Multilevel Boost Converter Topology with Inherent Capacitors Voltage Balancing Using Model Predictive Controller 2020 ,		9
35	A New Multilevel Inverter Topology With Reduced Power Components for Domestic Solar PV Applications. <i>IEEE Access</i> , 2020 , 8, 187483-187497	3.5	22
34	A Hybrid Switched Capacitor Multi-Level Inverter with High Voltage Gain and Self-Voltage Balancing Ability. <i>Electric Power Components and Systems</i> , 2020 , 48, 755-768	1	7
33	Seven-level boosting active neutral point clamped inverter using cross-connected switched capacitor cells. <i>IET Power Electronics</i> , 2020 , 13, 1919-1924	2.2	8
32	Fast charging converter and control algorithm for solar PV battery and electrical grid integrated electric vehicle charging station. <i>Automatika</i> , 2020 , 61, 614-625	1.6	14
31	A Generalized Multilevel Inverter Topology With Reduction of Total Standing Voltage. <i>IEEE Access</i> , 2020 , 8, 168941-168950	3.5	8
30	A New Eight Switch Seven Level Boost Active Neutral Point Clamped (8S-7L-BANPC) Inverter. <i>IEEE Access</i> , 2020 , 8, 203972-203981	3.5	8
29	An Improved Seven-Level PUC Inverter Topology With Voltage Boosting. <i>IEEE Transactions on Circuits and Systems II: Express Briefs</i> , 2020 , 67, 127-131	3.5	35
28	A New Switched Capacitor 7L Inverter With Triple Voltage Gain and Low Voltage Stress. <i>IEEE Transactions on Circuits and Systems II: Express Briefs</i> , 2020 , 67, 1294-1298	3.5	56
27	Internet of Things based real-time electric vehicle load forecasting and charging station recommendation. <i>ISA Transactions</i> , 2020 , 97, 431-447	5.5	33

26	Reduced Switch Count Based Single Source 7L Boost Inverter Topology. <i>IEEE Transactions on Circuits and Systems II: Express Briefs</i> , 2020 , 67, 3252-3256	3.5	27
25	Cross Connected Compact Switched-Capacitor Multilevel Inverter (C3-SCMLI) Topology With Reduced Switch Count. <i>IEEE Transactions on Circuits and Systems II: Express Briefs</i> , 2020 , 67, 3287-3291	3.5	18
24	A new asymmetric dual source multilevel inverter topology with reduced power switches 2019 , 42, 460-472		17
23	Switched-Capacitor-Based Quadruple-Boost Nine-Level Inverter. <i>IEEE Transactions on Power Electronics</i> , 2019 , 34, 7147-7150	7.2	43
22	A New Asymmetric and Cascaded Switched Diode Multilevel Inverter Topology for Reduced Switches, DC Source and Blocked Voltage on Switches. <i>Journal of Circuits, Systems and Computers</i> , 2019 , 28, 1950064	0.9	5
21	Generalized Cascaded Symmetric and Level Doubling Multilevel Converter Topology with Reduced THD for Photovoltaic Applications. <i>Electronics (Switzerland)</i> , 2019 , 8, 161	2.6	7
20	2019 ,		2
19	A New Single Phase Single Switched-Capacitor Based Nine-Level Boost Inverter Topology With Reduced Switch Count and Voltage Stress. <i>IEEE Access</i> , 2019 , 7, 174178-174188	3.5	46
18	A New 5-Level ANPC Switched Capacitor Inverter Topology for Photovoltaic Applications 2019 ,		5
17	Compact Switched Capacitor Multilevel Inverter (CSCMLI) With Self-Voltage Balancing and Boosting Ability. <i>IEEE Transactions on Power Electronics</i> , 2019 , 34, 4009-4013	7.2	68
16	A Self-Balancing Five-Level Boosting Inverter With Reduced Components. <i>IEEE Transactions on Power Electronics</i> , 2019 , 34, 6020-6024	7.2	50
15	A New Generalized Multilevel Converter Topology With Reduced Voltage on Switches, Power losses, and Components. <i>IEEE Journal of Emerging and Selected Topics in Power Electronics</i> , 2019 , 7, 1094-1106	5.6	22
14	A New Generalized Multilevel Converter Topology Based on Cascaded Connection of Basic Units. <i>IEEE Journal of Emerging and Selected Topics in Power Electronics</i> , 2019 , 7, 2498-2512	5.6	42
13	Three-Dimensional Space Vector Modulation Strategy for Capacitor Balancing in Split Inductor Neutral-Point Clamped Multilevel Inverters. <i>Journal of Circuits, Systems and Computers</i> , 2018 , 27, 1850232	3.9	3
12	A new symmetric multilevel converter topology with reduced voltage on switches and DC source 2018 ,		3
11	An assessment of recent multilevel inverter topologies with reduced power electronics components for renewable applications. <i>Renewable and Sustainable Energy Reviews</i> , 2018 , 82, 3379-3399	16.2	59
10	Hybrid Multilevel Inverter Topology With Reduced Part Count 2018 ,		1
9	Switched-Capacitor-Based Three-Phase Five-Level Inverter Topology With Reduced Components 2018 ,		1

8	A New Switched DC-Link Capacitor-based Multi-level Converter (SDC2MLC). <i>Electric Power Components and Systems</i> , 2017 , 45, 1001-1015	1	17
7	Symmetric switched diode multilevel inverter structure with minimised switch count. <i>Journal of Engineering</i> , 2017 , 2017, 469-478	0.7	12
6	A New Hybrid Multilevel Inverter Topology for Medium and High Voltage Applications. <i>Applied Mathematics and Information Sciences</i> , 2017 , 11, 497-508	2.4	3
5	An assessment on performance of DCDC converters for renewable energy applications. <i>Renewable and Sustainable Energy Reviews</i> , 2016 , 58, 1475-1485	16.2	81
4	Investigation of novel symmetric and asymmetric multilevel converter topology with reduced power switches. <i>International Journal of Power Electronics</i> , 2015 , 7, 226	0.2	4
3	A New Symmetric Multilevel Inverter Topology Using Single and Double Source Sub-Multilevel Inverters. <i>Journal of Power Electronics</i> , 2015 , 15, 96-105	0.9	19
2	A New Symmetric Cascaded Multilevel Inverter Topology Using Single and Double Source Unit. <i>Journal of Power Electronics</i> , 2015 , 15, 951-963	0.9	15
1	A New Switched-Ladder Multilevel Converter Structure with Reduced Power Electronic Components. <i>Journal of Circuits, Systems and Computers</i> , 2150217	0.9	2