

Georges G E Gielen

List of Publications by Year in descending order

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538
papers

10,340
citations

53660

45
h-index

74018

75
g-index

572
all docs

572
docs citations

572
times ranked

4900
citing authors

#	ARTICLE	IF	CITATIONS
1	Improving the Accuracy of Spiking Neural Networks for Radar Gesture Recognition Through Preprocessing. IEEE Transactions on Neural Networks and Learning Systems, 2023, 34, 2869-2881.	7.2	10
2	A Compact, Low-Power Analog Front-End With Event-Driven Input Biasing for High-Density Neural Recording in 22-nm FDSOI. IEEE Transactions on Circuits and Systems II: Express Briefs, 2022, 69, 804-808.	2.2	10
3	Fail-Safe Human Detection for Drones Using a Multi-Modal Curriculum Learning Approach. IEEE Robotics and Automation Letters, 2022, 7, 303-310.	3.3	6
4	Methodology for Readout and Ring Oscillator Optimization Toward Energy-Efficient VCO-Based ADCs. IEEE Transactions on Circuits and Systems I: Regular Papers, 2022, 69, 985-998.	3.5	5
5	On the Use of Spiking Neural Networks for Ultralow-Power Radar Gesture Recognition. IEEE Microwave and Wireless Components Letters, 2022, 32, 222-225.	2.0	8
6	A 256-Channel Actively-Multiplexed $\Delta\Sigma$ CoG Implant with Column-Parallel Incremental $\Delta\Sigma$ ADCs Employing Bulk-DACs in 22-nm FDSOI Technology. , 2022, , .		4
7	A 96.9-dB-Resolution 109- μ W Second-Order Robust Closed-Loop VCO-Based Sensor Interface for Multiplexed Single-Ended Resistance Readout in 180-nm CMOS. IEEE Journal of Solid-State Circuits, 2022, 57, 2764-2777.	3.5	4
8	Exploring Cross-fusion and Curriculum Learning for Multi-modal Human Detection on Drones. , 2022, , .		0
9	A Low-Complexity Radar Detector Outperforming OS-CFAR for Indoor Drone Obstacle Avoidance. IEEE Journal of Selected Topics in Applied Earth Observations and Remote Sensing, 2021, 14, 9162-9175.	2.3	20
10	Analysis and Comparison of Readout Architectures and Analog-to-Digital Converters for 3D-Stacked CMOS Image Sensors. IEEE Transactions on Circuits and Systems I: Regular Papers, 2021, 68, 3117-3130.	3.5	9
11	Power-efficient VCO-based ADCs for Wireless Communication Systems. , 2021, , .		0
12	Efficient Offline Outer/Inner DAC Mismatch Calibration in Wideband $\Sigma\Delta$ ADCs. IEEE Transactions on Circuits and Systems I: Regular Papers, 2020, 67, 4259-4269.	3.5	1
13	Time-Encoding Analog-to-Digital Converters: Bridging the Analog Gap to Advanced Digital CMOS?Part 2: Architectures and Circuits. IEEE Solid-State Circuits Magazine, 2020, 12, 18-27.	0.5	15
14	A 16.1-bit Resolution 0.064-mm ² Compact Highly Digital Closed-Loop Single-VCO-Based 1-1 Sturdy-MASH Resistance-to-Digital Converter With High Robustness in 180-nm CMOS. IEEE Journal of Solid-State Circuits, 2020, 55, 2456-2467.	3.5	17
15	Time-Encoding Analog-to-Digital Converters: Bridging the Analog Gap to Advanced Digital CMOS-Part 1: Basic Principles. IEEE Solid-State Circuits Magazine, 2020, 12, 47-55.	0.5	41
16	Improving the EMI Robustness of Feedback-based Time-Encoding Readout Architectures for Resistive Sensor Interfaces. , 2020, , .		2
17	Machine Learning-based Defect Coverage Boosting of Analog Circuits under Measurement Variations. ACM Transactions on Design Automation of Electronic Systems, 2020, 25, 1-27.	1.9	4
18	Quick Analyses for Improving Reliability and Functional Safety of Mixed-Signal ICs. , 2020, , .		1

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19	Review of Methodologies for Pre- and Post-Silicon Analog Verification in Mixed-Signal SOCs. , 2019, , .		8
20	Introduction to the Special Issue on the 2019 IEEE European Solid-State Circuits Conference (ESSCIRC). IEEE Solid-State Circuits Letters, 2019, 2, 61-62.	1.3	0
21	From Open-Loop to Closed-Loop Single-VCO-Based Sensor-to-Digital Converter Architectures: theoretical analysis and comparison. , 2019, , .		6
22	Architectural Analysis of a Novel Closed-Loop VCO-Based 1 st Sturdy MASH Sensor-to-Digital Converter. , 2019, , .		1
23	A Robust BBPLL-Based 0.18- μm CMOS Resistive Sensor Interface With High Drift Resilience Over a ~ 40 $^{\circ}\text{C}$ ~ 175 $^{\circ}\text{C}$ Temperature Range. IEEE Journal of Solid-State Circuits, 2019, 54, 1862-1873.	3.5	15
24	An 85-MHz-BW ASAR-Assisted CT 4-0 MASH $\Delta\Sigma$ Modulator With Background Half-Range Dithering-Based DAC Calibration in 28-nm CMOS. IEEE Transactions on Circuits and Systems I: Regular Papers, 2019, 66, 2405-2414.	3.5	7
25	Applying V_{stress} and defect activation coverage to produce zero-defect mixed-signal automotive ICs. , 2019, , .		9
26	The fantastic voyage towards ultra-miniaturized sensing circuits. , 2019, , .		0
27	Performance Limitation Analysis of Highly-Digital Time-Based Closed-Loop Sensor-to-Digital Converter Architectures. IEEE Transactions on Circuits and Systems II: Express Briefs, 2019, 66, 1114-1118.	2.2	5
28	Understanding the Impact of Time-Dependent Random Variability on Analog ICs: From Single Transistor Measurements to Circuit Simulations. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2019, 27, 601-610.	2.1	18
29	A 16.1-b ENOB 0.064mm ² Compact Highly-Digital Closed-Loop Single-VCO-based 1 st SMASH Resistance-to-Digital Converter in 180nm CMOS. , 2019, , .		1
30	ADAGE: Automatic DfT-Assisted Generation of Test Stimuli for Mixed- Signal Integrated Circuits. IEEE Design and Test, 2018, 35, 24-30.	1.1	4
31	A 0.18- μm CMOS Image Sensor With Phase-Delay-Counting and Oversampling Dual-Slope Integrating Column ADCs Achieving $1_{\text{ext}}^{\text{e}}_{\text{rms}}$ Noise at 3.8- μs Conversion Time. IEEE Journal of Solid-State Circuits, 2018, 53, 515-526.	3.5	4
32	Methodology Towards Sub-ppm Testing of Analog and Mixed-Signal ICs for Cyber-Physical Systems. , 2018, , .		1
33	An Automated Low-Cost Analog and Mixed-Signal DfT Method Using Testing Diodes. IEEE Design and Test, 2018, 35, 15-23.	1.1	0
34	A/D Converters and Applications. Signals and Communication Technology, 2018, , 13-35.	0.4	0
35	Continuous-Time Delta-Sigma Modulators. Signals and Communication Technology, 2018, , 37-66.	0.4	0
36	VCO-Based ADCs. Signals and Communication Technology, 2018, , 67-81.	0.4	0

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37	CT DSM ADCs with VCO-Based Quantization. <i>Signals and Communication Technology</i> , 2018, , 83-108.	0.4	0
38	VCO-Based $\Delta\sigma$ MASH ADC. <i>Signals and Communication Technology</i> , 2018, , 127-151.	0.4	0
39	Fully-VCO-Based High-Order $\Delta\sigma$ ADC. <i>Signals and Communication Technology</i> , 2018, , 153-171.	0.4	0
40	Improving the robustness and drift resilience of CMOS BBPLL-based time-based sensor interfaces. , 2018, , .		0
41	A Single-Temperature-Calibration 0.18- μm CMOS Time-Based Resistive Sensor Interface with Low Drift over a $\sim 40^\circ\text{C}$ to 175°C Temperature Range. , 2018, , .		5
42	Controlled-Oscillator Optimization for Highly-Digital CMOS Time-Based Sensor-to-Digital Converter Architectures. , 2018, , .		2
43	Generalized mode solver for plasmonic transmission lines embedded in layered media based on the Method of Moments. <i>Computer Physics Communications</i> , 2018, 233, 1-15.	3.0	3
44	Fully-VCO-based $\Delta\sigma$ MASH CT $\Delta\sigma$ ADC. <i>Electronics Letters</i> , 2018, 54, 1018-1020.	0.5	6
45	Modeling and Analysis of Drift-Cancellation Techniques for Time-Based Integrated Resistive Sensor Interfaces. <i>IEEE Transactions on Components, Packaging and Manufacturing Technology</i> , 2018, 8, 1203-1212.	1.4	4
46	Design of Power-Efficient Highly Digital Analog-to-Digital Converters for Next-Generation Wireless Communication Systems. <i>Signals and Communication Technology</i> , 2018, , .	0.4	2
47	Low-Noise Detectors through Incremental $\Delta\sigma$ ADCs. , 2017, , 71-90.		0
48	A Review on Internet of Things Solutions for Intelligent Energy Control in Buildings for Smart City Applications. <i>Energy Procedia</i> , 2017, 111, 770-779.	1.8	147
49	Novel Wire-Grid Nano-Antenna Array With Circularly Polarized Radiation for Wireless Optical Communication Systems. <i>Journal of Lightwave Technology</i> , 2017, 35, 4700-4706.	2.7	14
50	Analysis and modeling of drift-resilient time-based integrated resistive sensor interfaces. , 2017, , .		3
51	Drift mitigation in integrated sensor interfaces. , 2017, , .		0
52	A fully-integrated method for RTN parameter extraction. , 2017, , .		3
53	A very low cost and highly parallel DFT method for analog and mixed-signal circuits. , 2017, , .		6
54	A 0.6-V, 0.015-mm ² , Time-Based ECG Readout for Ambulatory Applications in 40-nm CMOS. <i>IEEE Journal of Solid-State Circuits</i> , 2017, 52, 298-308.	3.5	44

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55	A 5Gb/s 7.1fj/b/mm 8Å— multi-drop on-chip 10mm data link in 14nm FinFET CMOS SOI at 0.5V. , 2017, , .		1
56	A power-efficient reconfigurable two-step VCO-based ADC for software-defined radio. , 2017, , .		0
57	Automatic testing of analog ICs for latent defects using topology modification. , 2017, , .		11
58	Non-intrusive detection of defects in mixed-signal integrated circuits using light activation. , 2017, , .		1
59	Spherical Wave Based Macromodels for Efficient System-Level EMC Analysis in Circuit Simulators Part II: Optimized Calculation of DUTâ€™DUT Interactions. IEEE Transactions on Electromagnetic Compatibility, 2016, 58, 1506-1516.	1.4	1
60	A surrogate model assisted evolutionary algorithm for computationally expensive design optimization problems with discrete variables. , 2016, , .		8
61	Comparative experimental analysis of time-dependent variability using a transistor test array. , 2016, , .		7
62	Analog fault coverage improvement using final-test dynamic part average testing. , 2016, , .		16
63	Effective DC fault models and testing approach for open defects in analog circuits. , 2016, , .		19
64	Automatic test signal generation for mixed-signal integrated circuits using circuit partitioning and interval analysis. , 2016, , .		5
65	Automatic generation of test infrastructures for analog integrated circuits by controllability and observability co-optimization. The Integration VLSI Journal, 2016, 55, 393-400.	1.3	10
66	Magnetic hysteresis at the domain scale of a multi-scale material model for magneto-elastic behaviour. Journal of Magnetism and Magnetic Materials, 2016, 414, 168-179.	1.0	23
67	Digital-domain chopping technique for high-resolution PLL-based sensor interfaces. Sensors and Actuators A: Physical, 2016, 249, 294-302.	2.0	11
68	Charge pumpâ€™and VCOâ€™based secondâ€™order deltaâ€™sigma ADC. Electronics Letters, 2016, 52, 1432-1434.	0.5	2
69	An integrated multi-electrode-optrode array for in vitro optogenetics. Scientific Reports, 2016, 6, 20353.	1.6	36
70	Low Loss CMOS-Compatible PECVD Silicon Nitride Waveguides and Grating Couplers for Blue Light Optogenetic Applications. IEEE Photonics Journal, 2016, 8, 1-11.	1.0	29
71	A column-and-row-parallel CMOS image sensor with thermal and 1/f noise suppression techniques. , 2016, , .		3
72	Spherical Wave Based Macromodels for Efficient System-Level EMC Analysis in Circuit Simulators Part I: Optimized Derivation and Truncation Criteria. IEEE Transactions on Electromagnetic Compatibility, 2016, 58, 1494-1505.	1.4	3

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73	Time-Based Sensor Interface Circuits in CMOS and Carbon Nanotube Technologies. IEEE Transactions on Circuits and Systems I: Regular Papers, 2016, 63, 577-586.	3.5	20
74	A Surrogate-Model-Assisted Evolutionary Algorithm for Computationally Expensive Design Optimization Problems with Inequality Constraints. Springer Proceedings in Mathematics and Statistics, 2016, , 347-370.	0.1	9
75	Digital-domain Chopping Technique for PLL-Based Sensor Interfaces. Procedia Engineering, 2015, 120, 507-510.	1.2	1
76	High-density optrode-electrode neural probe using SixNy photonics for in vivo optogenetics. , 2015, , .		15
77	A lowpass/bandpass reconfigurable continuous-time $\hat{\tau}$ ADC for software-defined radio. , 2015, , .		0
78	Towards Energy-Efficient CMOS Integrated Sensor-to-Digital Interface Circuits. , 2015, , 379-397.		2
79	Automatic generation of autonomous built-in observability structures for analog circuits. , 2015, , .		7
80	Grain scale hysteresis model embedded in a multi-scale material model. , 2015, , .		2
81	Embedding a Magnetoelastic Material Model in a Coupled Magnetomechanical Finite-Element Solver. IEEE Transactions on Magnetics, 2015, 51, 1-4.	1.2	4
82	Automatic generation of lightweight controllability and observability structures for analog circuits. , 2015, , .		3
83	A 42 fJ/Step-FoM Two-Step VCO-Based Delta-Sigma ADC in 40 nm CMOS. IEEE Journal of Solid-State Circuits, 2015, 50, 714-723.	3.5	61
84	Development of an Ultralow-Power Injection-Locked PSK Receiver Architecture. IEEE Transactions on Circuits and Systems II: Express Briefs, 2015, 62, 31-35.	2.2	2
85	Automated testing of mixed-signal integrated circuits by topology modification. , 2015, , .		16
86	Impact analysis of deep-submicron CMOS technologies on the voltage and temperature independence of a time-domain sensor interface. Analog Integrated Circuits and Signal Processing, 2015, 82, 285-296.	0.9	1
87	A 40 MHz-BW 12-bit continuous-time $\hat{\tau}$ modulator with digital calibration and 84.2 dB-SFDR in 90 nm CMOS. Analog Integrated Circuits and Signal Processing, 2015, 84, 137-148.	0.9	0
88	A presence-based control strategy solution for HVAC systems. , 2015, , .		5
89	Design of low-power sensor interfaces in the IoT era. , 2015, , .		1
90	A 40-MHz Bandwidth $0\hat{\epsilon}^2$ MASH VCO-Based Delta-Sigma ADC With 35-fJ/Step FoM. IEEE Transactions on Circuits and Systems II: Express Briefs, 2015, 62, 952-956.	2.2	22

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91	Time-based sensor interface circuits in carbon nanotube technology. , 2015, , .		1
92	Temperature- and Supply Voltage-Independent Time References for Wireless Sensor Networks. Analog Circuits and Signal Processing Series, 2015, , .	0.3	5
93	A Low-Energy Ultra-Wideband Internet-of-Things Radio System for Multi-Standard Smart-Home Energy Management. IEIE Transactions on Smart Processing and Computing, 2015, 4, 354-365.	0.3	6
94	SMAS: A Generalized and Efficient Framework for Computationally Expensive Electronic Design Optimization Problems. , 2015, , 251-275.		1
95	Behavioral study of the surrogate model-aware evolutionary search framework. , 2014, , .		18
96	Network on Chip optimization based on surrogate model assisted evolutionary algorithms. , 2014, , .		5
97	An Integrated a-IGZO UHF Energy Harvester for Passive RFID Tags. IEEE Transactions on Electron Devices, 2014, 61, 3289-3295.	1.6	44
98	Design and test of analog circuits towards sub-ppm level. , 2014, , .		19
99	“All Programmable SOC FPGA for networking and computing in big data infrastructure”; , 2014, , .		2
100	Design and implementation of a multi-standard event-driven energy management system for smart buildings. , 2014, , .		3
101	Integral equations formulation of plasmonic transmission lines. Optics Express, 2014, 22, 22388.	1.7	21
102	Finite–element discretisation of the eddy–current term in a 2D solver for radially symmetric models. International Journal of Numerical Modelling: Electronic Networks, Devices and Fields, 2014, 27, 505-516.	1.2	4
103	Fundamentals of Optimization Techniques in Analog IC Sizing. Studies in Computational Intelligence, 2014, , 19-40.	0.7	3
104	Automatic generation of electro-thermal models with TRAPPIST. , 2014, , .		0
105	0.35 V time–domain–based instrumentation amplifier. Electronics Letters, 2014, 50, 1511-1513.	0.5	5
106	A fully micromachined double-slot waveguide-to-GCPW transition for 180–230 GHz MM-wave applications. , 2014, , .		3
107	A 40MHz-BW 35fj/step-FoM nonlinearity-cancelling two-step ADC with dual-input VCO-based quantizer. , 2014, , .		8
108	Automated Design of Analog and High-frequency Circuits. Studies in Computational Intelligence, 2014, , .	0.7	31

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109	Electromagnetic Design Automation: Surrogate Model Assisted Evolutionary Algorithm. <i>Studies in Computational Intelligence</i> , 2014, , 133-151.	0.7	0
110	Efficient optimization of fully-integrated inductive DC-DC converters comprising tapered inductor layout synthesis and temperature effects. <i>Analog Integrated Circuits and Signal Processing</i> , 2014, 78, 111-121.	0.9	1
111	A Gaussian Process Surrogate Model Assisted Evolutionary Algorithm for Medium Scale Expensive Optimization Problems. <i>IEEE Transactions on Evolutionary Computation</i> , 2014, 18, 180-192.	7.5	379
112	Sparse ϵ -tube support vector regression by active learning. <i>Soft Computing</i> , 2014, 18, 1113-1126.	2.1	6
113	Deep-level transient spectroscopy on an amorphous InGaZnO Schottky diode. <i>Applied Physics Letters</i> , 2014, 104, 082112.	1.5	24
114	Nonlinear Magnetostatic Finite-Element Formulation for Models With Radial Symmetry. <i>IEEE Transactions on Magnetics</i> , 2014, 50, 85-88.	1.2	3
115	Two-Dimensional Magnetostatic Finite-Element Simulation for Devices With a Radial Symmetry. <i>IEEE Transactions on Magnetics</i> , 2014, 50, 1-4.	1.2	10
116	An Efficient Method for Antenna Design Optimization Based on Evolutionary Computation and Machine Learning Techniques. <i>IEEE Transactions on Antennas and Propagation</i> , 2014, 62, 7-18.	3.1	194
117	Sensor-to-Digital Interface Built Entirely With Carbon Nanotube FETs. <i>IEEE Journal of Solid-State Circuits</i> , 2014, 49, 190-201.	3.5	101
118	Stochastic Macromodeling of Nonlinear Systems Via Polynomial Chaos Expansion and Transfer Function Trajectories. <i>IEEE Transactions on Microwave Theory and Techniques</i> , 2014, 62, 1454-1460.	2.9	23
119	An Implantable 455-Active-Electrode 52-Channel CMOS Neural Probe. <i>IEEE Journal of Solid-State Circuits</i> , 2014, 49, 248-261.	3.5	208
120	Optimization of analog fault coverage by exploiting defect-specific masking. , 2014, , .		16
121	High-Performance a-IGZO Thin Film Diode as Selector for Cross-Point Memory Application. <i>IEEE Electron Device Letters</i> , 2014, 35, 642-644.	2.2	39
122	A Low-Noise High-Frame-Rate 1-D Decoding Readout Architecture for Stacked Image Sensors. <i>IEEE Sensors Journal</i> , 2014, 14, 1966-1973.	2.4	5
123	Low-energy UWB transceiver implementation for smart home energy management. , 2014, , .		7
124	PTC-Based Sigma-Delta ADCs for High-Speed, Low-Noise Imagers. <i>IEEE Sensors Journal</i> , 2014, 14, 2932-2933.	2.4	7
125	Design of a frequency reference based on a PVT-independent transmission line delay. , 2014, , .		0
126	Scalable Bang-Bang Phase-Locked-Loop-based integrated sensor interfaces. <i>Microelectronics Journal</i> , 2014, 45, 1641-1647.	1.1	4

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127	Carbon Nanotube Circuit Integration up to Sub-20 nm Channel Lengths. ACS Nano, 2014, 8, 3434-3443.	7.3	70
128	A remotely-powered, 20ÂMb/s, 5.35ÂpJ/bit impulse-UWB WSN tag for cm-accurate-localization sensor networks. Analog Integrated Circuits and Signal Processing, 2014, 80, 531-540.	0.9	1
129	GASPAD: A General and Efficient mm-Wave Integrated Circuit Synthesis Method Based on Surrogate Model Assisted Evolutionary Algorithm. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 2014, 33, 169-182.	1.9	79
130	A 132-dB Dynamic-Range Global-Shutter Stacked Architecture for High-Performance Imagers. IEEE Transactions on Circuits and Systems II: Express Briefs, 2014, 61, 398-402.	2.2	8
131	Transient Behavior and Phase Noise Performance of Pulsed-Harmonic Oscillators. IEEE Transactions on Circuits and Systems I: Regular Papers, 2014, 61, 2119-2128.	3.5	2
132	Closed-loop optical neural stimulation based on a 32-channel low-noise recording system with online spike sorting. Journal of Neural Engineering, 2014, 11, 046005.	1.8	43
133	Basic Concepts and Background. Studies in Computational Intelligence, 2014, , 1-17.	0.7	2
134	Process Variation-Aware Analog Circuit Sizing: Uncertain Optimization. Studies in Computational Intelligence, 2014, , 85-105.	0.7	0
135	Ordinal Optimization-Based Methods for Efficient Variation-Aware Analog IC Sizing. Studies in Computational Intelligence, 2014, , 107-131.	0.7	0
136	High-Performance Analog IC Sizing: Advanced Constraint Handling and Search Methods. Studies in Computational Intelligence, 2014, , 41-62.	0.7	0
137	Mixed-signal template-based reduction scheme for stimulus artifact removal in electrical stimulation. Medical and Biological Engineering and Computing, 2013, 51, 449-458.	1.6	1
138	Miniaturization of UWB Antennas and its Influence on Antenna-Transceiver Performance in Impulse-UWB Communication. Wireless Personal Communications, 2013, 71, 2913-2935.	1.8	2
139	An Efficient Evolutionary Algorithm for Chance-Constrained Bi-Objective Stochastic Optimization. IEEE Transactions on Evolutionary Computation, 2013, 17, 786-796.	7.5	43
140	Black-Box Modelling of AC-DC Rectifiers for RFID Applications Using Support Vector Regression Machines. , 2013, , .		2
141	An energy-efficient capacitance-controlled oscillator-based sensor interface for MEMS sensors. , 2013, , .		13
142	Extracting Analytical Nonlinear Models from Analog Circuits by Recursive Vector Fitting of Transfer Function Trajectories. , 2013, , .		6
143	Timing-based integrated sensor interfaces: Hype or promise?. , 2013, , .		0
144	A 40MHz-BW two-step open-loop VCO-based ADC with 42fj/step FoM in 40nm CMOS. , 2013, , .		7

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145	A 40nm-CMOS, 72 µW injection-locked timing reference and 1.8 Mbit/s coordination receiver for wireless sensor networks. , 2013, , .		0
146	Developing engineering-oriented educational workshops within a student branch. , 2013, , .		0
147	Calibration of DAC Mismatch Errors in $\Sigma\Delta$ ADCs Based on a Sine-Wave Measurement. IEEE Transactions on Circuits and Systems II: Express Briefs, 2013, 60, 567-571.	2.2	30
148	Gigahertz Operation of a-IGZO Schottky Diodes. IEEE Transactions on Electron Devices, 2013, 60, 3407-3412.	1.6	64
149	Stochastic Degradation Modeling and Simulation for Analog Integrated Circuits in Nanometer CMOS. , 2013, , .		9
150	A 40nm-CMOS, 18 μW, temperature and supply voltage independent sensor interface for RFID tags. , 2013, , .		7
151	Experimental demonstration of a fully digital capacitive sensor interface built entirely using carbon-nanotube FETs. , 2013, , .		18
152	Performance Analysis of Energy-Efficient BBPLL-Based Sensor-to-Digital Converters. IEEE Transactions on Circuits and Systems I: Regular Papers, 2013, 60, 2130-2138.	3.5	18
153	Efficient Reciprocity-Based Algorithm to Predict Worst Case Induced Disturbances on Multiconductor Transmission Lines due to Incoming Plane Waves. IEEE Transactions on Electromagnetic Compatibility, 2013, 55, 208-216.	1.4	42
154	Supply-Noise-Resilient Design of a BBPLL-Based Force-Balanced Wheatstone Bridge Interface in 130-nm CMOS. IEEE Journal of Solid-State Circuits, 2013, 48, 2618-2627.	3.5	52
155	A Gain-Adaptive Column Amplifier for Wide-Dynamic-Range CMOS Image Sensors. IEEE Transactions on Electron Devices, 2013, 60, 3601-3604.	1.6	2
156	Impact of transistor aging on RF low noise amplifier performance of 28nm technology: Reliability assessment. , 2013, , .		8
157	A Low-Power and Low-Voltage BBPLL-Based Sensor Interface in 130nm CMOS for Wireless Sensor Networks. , 2013, , .		2
158	Sacha. , 2013, , .		8
159	40ÂMHzâ€BW continuousâ€time $\hat{I}^{\hat{I}}$ modulator with capacitive local feedback and currentâ€sharing OTA. Electronics Letters, 2013, 49, 585-587.	0.5	2
160	Development of an open-source smart energy house for K-12 education. , 2013, , .		3
161	Integrated UHF a-IGZO energy harvester for passive RFID tags. , 2013, , .		12
162	An implantable 455-active-electrode 52-channel CMOS neural probe. , 2013, , .		30

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163	Efficient Synthesis Methods for High-Frequency Integrated Passive Components and Amplifiers. Lecture Notes in Electrical Engineering, 2013, , 27-52.	0.3	0
164	Predictive sensing in analog-to-digital converters for biomedical applications. , 2013, , .		0
165	A third-order complementary metal-oxide-semiconductor sigma-delta modulator operating between 4.2 K and 300 K. Review of Scientific Instruments, 2012, 83, 024708.	0.6	5
166	Wobble-based on-chip calibration circuit for temperature independent oscillators. Electronics Letters, 2012, 48, 1000-1001.	0.5	2
167	Efficient multi-objective synthesis for microwave components based on computational intelligence techniques. , 2012, , .		9
168	Impact of TSV area on the dynamic range and frame rate performance of 3D-integrated image sensors. , 2012, , .		2
169	UHF IGZO Schottky diode. , 2012, , .		21
170	Black-box modelling of conducted electromagnetic immunity by support vector machines. , 2012, , .		2
171	Towards a noise prediction model for in vivo neural recording. , 2012, 2012, 759-62.		13
172	Black-box modelling of conducted electromagnetic emissions by adjustable complexity support vector regression machines. , 2012, , .		1
173	High-performance a-In-Ga-Zn-O Schottky diode with oxygen-treated metal contacts. Applied Physics Letters, 2012, 101, .	1.5	81
174	An Efficient High-Frequency Linear RF Amplifier Synthesis Method Based on Evolutionary Computation and Machine Learning Techniques. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 2012, 31, 981-993.	1.9	53
175	Degradation-Resilient Design of a Self-Healing xDSL Line Driver in 90 nm CMOS. IEEE Journal of Solid-State Circuits, 2012, 47, 1757-1767.	3.5	9
176	Complementary DAC topology for reduced output impedance dependency and improved dynamic performance. Electronics Letters, 2012, 48, 1039-1041.	0.5	9
177	A Novel, Highly Linear, Voltage and Temperature Independent Sensor Interface using Pulse Width Modulation. Procedia Engineering, 2012, 47, 1215-1218.	1.2	8
178	A 127 μW exact timing reference for Wireless Sensor Networks based on injection locking. , 2012, , .		2
179	Offset measurement method for accurate characterization of BTI-induced degradation in opamps. , 2012, , .		2
180	Optimization of fully-integrated power converter circuits comprising tapered inductor layout and temperature effects. , 2012, , .		3

#	ARTICLE	IF	CITATIONS
181	Designing reliable analog circuits in an unreliable world. , 2012, , .		14
182	Miniaturized RFID/UWB Antenna Structure that Can be Optimized for Arbitrary Input Impedance. IEEE Antennas and Propagation Magazine, 2012, 54, 74-87.	1.2	5
183	Characterization of Analog Circuits Using Transfer Function Trajectories. IEEE Transactions on Circuits and Systems I: Regular Papers, 2012, 59, 1796-1804.	3.5	17
184	Miniaturized integrated antennas for far-field wireless powering. AEU - International Journal of Electronics and Communications, 2012, 66, 789-796.	1.7	3
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