M Priyadharshni

List of Publications by Year
in descending order

5 papers	27 citations	2258059 3 h-index	2053705 5 g-index
5 all docs	5 docs citations	5 times ranked	20 citing authors

#	Article	IF	Citations
1	Low power and area efficient error tolerant adder for image processing application. International Journal of Circuit Theory and Applications, 2020, 48, 696-708.	2.0	12
2	Design of Imprecise Multipliers by Using Approximate Technique for Error Resilient Applications. Journal of Circuits, Systems and Computers, 2020, , 2150114.	1.5	5
3	An error efficient and low complexity approximate multi‑bit adder for image processing applications. International Journal of Circuit Theory and Applications, 2021, 49, 2373-2381.	2.0	5
4	Logically Optimal Novel 4:2 Compressor Architectures for High-Performance Applications. Arabian Journal for Science and Engineering, 2020, 45, 6199-6209.	3.0	3
5	A Comparative Exploration About Approximate Full Adders for Error Tolerant Applications. Communications in Computer and Information Science, 2019, , 61-74.	0.5	2