

# Hu Xu

## List of Publications by Year in descending order

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10  
papers

51  
citations

2682572

2  
h-index

2917675

2  
g-index

10  
all docs

10  
docs citations

10  
times ranked

42  
citing authors

#	ARTICLE	IF	CITATIONS
1	Timing Uncertainty in 3-D Clock Trees Due to Process Variations and Power Supply Noise. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2013, 21, 2226-2239.	3.1	10
2	Effect of process variations in 3D global clock distribution networks. ACM Journal on Emerging Technologies in Computing Systems, 2012, 8, 1-25.	2.3	12
3	An accurate dynamic power model on FPGA routing resources. , 2012, , .		2
4	The combined effect of process variations and power supply noise on clock skew and jitter. , 2012, , .		11
5	Enhanced wafer matching heuristics for 3-D ICs. , 2012, , .		0
6	Skew variability in 3-D ICs with multiple clock domains. , 2011, , .		3
7	Process-induced skew variation for scaled 2-D and 3-D ICs. , 2010, , .		12
8	Synchronization and power integrity issues in 3-D ICs. , 2010, , .		1
9	Repeater Insertion for Two-Terminal Nets in Three-Dimensional Integrated Circuits. Lecture Notes of the Institute for Computer Sciences, Social-Informatics and Telecommunications Engineering, 2009, , 141-150.	0.3	0
10	A fast incremental clock skew scheduling algorithm for slack optimization. , 2008, , .		0