Hu Xu

List of Publications by Year
in descending order

10	51	2682572 2	2917675 2
papers	citations	h-index	g-index

10	10	10	42
all docs	docs citations	times ranked	citing authors

#	Article	IF	Citations
1	Process-induced skew variation for scaled 2-D and 3-D ICs. , 2010, , .		12
2	Effect of process variations in 3D global clock distribution networks. ACM Journal on Emerging Technologies in Computing Systems, 2012, 8, 1-25.	2.3	12
3	The combined effect of process variations and power supply noise on clock skew and jitter. , 2012, , .		11
4	Timing Uncertainty in 3-D Clock Trees Due to Process Variations and Power Supply Noise. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2013, 21, 2226-2239.	3.1	10
5	Skew variability in 3-D ICs with multiple clock domains. , 2011, , .		3
6	An accurate dynamic power model on FPGA routing resources. , 2012, , .		2
7	Synchronization and power integrity issues in 3-D ICs. , 2010, , .		1
8	A fast incremental clock skew scheduling algorithm for slack optimization. , 2008, , .		0
9	Repeater Insertion for Two-Terminal Nets in Three-Dimensional Integrated Circuits. Lecture Notes of the Institute for Computer Sciences, Social-Informatics and Telecommunications Engineering, 2009, , 141-150.	0.3	0
10	Enhanced wafer matching heuristics for 3-D ICs. , 2012, , .		0