

# Yang-Hsin Fan

## List of Publications by Year in descending order

Source: <https://exaly.com/author-pdf/4316135/publications.pdf>

Version: 2024-02-01

25  
papers

47  
citations

2258059

3  
h-index

2053705

5  
g-index

25  
all docs

25  
docs citations

25  
times ranked

37  
citing authors

#	ARTICLE	IF	CITATIONS
1	Adaptive On-Time Control Buck Converter with a Novel Virtual Inductor Current Circuit. Electronics (Switzerland), 2021, 10, 2143.	3.1	8
2	Multiple-Embedded-System Optimization Layout for Electromagnetic Wave Power Density in Complex Environments. Energies, 2020, 13, 4758.	3.1	1
3	Embedded Systems Layout for Routing Planning in Smart Home. , 2019, , .		0
4	Fast routing verification with complexity effect for SOC. Applied Computing and Informatics, 2019, 15, 144-152.	5.9	2
5	Befitting continuous-time model for energy saving object tracking networks. , 2018, , .		0
6	Energy-Efficient Clusters for Object Tracking Networks. Energies, 2018, 11, 2015.	3.1	1
7	Energy-aware path strategy for embedded network. International Journal of Distributed Sensor Networks, 2017, 13, 155014771773617.	2.2	1
8	Clique-first adaptive routes for high performance IoT networks. , 2016, , .		0
9	The scheduling and placement strategies for FPGA dynamic reconfigurable system. , 2016, , .		1
10	Grey relational clustering associated with CAPRI applied to FPGA placement. International Journal of Electronics, 2016, 103, 722-735.	1.4	0
11	Routing-aware power saving for IoT networks. , 2016, , 201-202.		1
12	Energy Efficiency of Task Allocation for Embedded JPEG Systems. Scientific World Journal, The, 2014, 2014, 1-8.	2.1	3
13	A MULTI-BAND LOW NOISE AMPLIFIER WITH GAIN FLATNESS AND BANDWIDTH ENHANCEMENT. Journal of Circuits, Systems and Computers, 2014, 23, 1450017.	1.5	5
14	Efficient energy exploration for embedded systems. , 2014, , .		2
15	FPGA placement methodology based on grey relational clustering. , 2012, , .		0
16	Software synthesis of middleware for heterogeneous embedded systems. , 2012, , .		2
17	Middleware Software for Embedded Systems. , 2012, , .		3
18	Grey decision of optimal simultaneous mapping and clustering to improve FPGA performance. , 2011, , .		1

#	ARTICLE	IF	CITATIONS
19	Grey relational grade applied to FPGA placement with minimal wire length. , 2011, , .		0
20	Sophisticated Computation of Hardware-Software Partition for Embedded Multiprocessor FPGA Systems. , 2008, , .		2
21	Partitioning Strategy for Embedded Multiprocessor FPGA Systems. Lecture Notes in Electrical Engineering, 2008, , 395-408.	0.4	0
22	Hardware-oriented Partition for Embedded Multiprocessor FPGA Systems. , 2007, , .		3
23	Enhancement of Hardware-Software Partition for Embedded Multiprocessor FPGA Systems. , 2007, , .		7
24	An Integrated Functional Verification Tool for FPGA Systems. , 2007, , .		4
25	Reduction of RLC Tree Delay Using Bidirectional Buffer Repeater Insertion. , 0, , .		0