

List of Publications by Citations

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The third column is the impact factor (IF) of the journal, and the fourth column is the number of citations of the article.

56 papers	813 citations	13 h-index	27 g-index
80 ext. papers	1,102 ext. citations	3.7 avg, IF	4.82 L-index

#	Paper	IF	Citations
56	State of Health Estimation of Lithium-Ion Batteries Using Capacity Fade and Internal Resistance Growth Models. <i>IEEE Transactions on Transportation Electrification</i> , 2018 , 4, 135-146	7.6	102
55	A Single-Inductor Multiple-Output Switcher With Simultaneous Buck, Boost, and Inverted Outputs. <i>IEEE Transactions on Power Electronics</i> , 2012 , 27, 1936-1951	7.2	100
54	Control Scheme for Reduced Cross-Regulation in Single-Inductor Multiple-Output DCDC Converters. <i>IEEE Transactions on Industrial Electronics</i> , 2013 , 60, 5095-5104	8.9	73
53	Online Estimation of the Electrochemical Impedance Spectrum and Remaining Useful Life of Lithium-Ion Batteries. <i>IEEE Transactions on Instrumentation and Measurement</i> , 2018 , 67, 1836-1849	5.2	65
52	ANN- and PSO-Based Synthesis of On-Chip Spiral Inductors for RF ICs. <i>IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems</i> , 2008 , 27, 188-192	2.5	64
51	A Current-Controlled Tristate Boost Converter With Improved Performance Through RHP Zero Elimination. <i>IEEE Transactions on Power Electronics</i> , 2009 , 24, 776-786	7.2	57
50	Identification of a class of nonlinear continuous-time systems using Hartley modulating functions. <i>International Journal of Control</i> , 1995 , 62, 1431-1451	1.5	35
49	Tradeoffs Aware Design Procedure for an Adaptively Biased Capacitorless Low Dropout Regulator Using Nested Miller Compensation. <i>IEEE Transactions on Power Electronics</i> , 2016 , 31, 369-380	7.2	31
48	A Hybrid-Mode Operational Transconductance Amplifier for an Adaptively Biased Low Dropout Regulator. <i>IEEE Transactions on Power Electronics</i> , 2017 , 32, 1245-1254	7.2	29
47	Impact of a Frequency Modulated Pulsewidth Modulation (PWM) Switching Converter on the Input Power System Quality. <i>IEEE Transactions on Power Electronics</i> , 2010 , 25, 1450-1459	7.2	27
46	Achieving Monotonic Variation of Spectral Composition in DCDC Converters Using Pulse Skipping Modulation. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , 2011 , 58, 1958-1966	3.9	20
45	Design and Analysis of an Adaptively Biased Low-Dropout Regulator Using Enhanced Current Mirror Buffer. <i>IEEE Transactions on Power Electronics</i> , 2016 , 31, 2324-2336	7.2	16
44	Analysis, Design, and Performance Evaluation of a Dynamically Slew Enhanced Adaptively Biased Capacitor-Less Low Dropout Regulator. <i>IEEE Transactions on Power Electronics</i> , 2016 , 31, 7016-7028	7.2	15
43	AGC study of a hydrothermal system with SMES and TCPS. <i>European Transactions on Electrical Power</i> , 2009 , 19, 487-498		12
42	A Formal Approach to On-Line Monitoring of Digital VLSI Circuits: Theory, Design and Implementation. <i>Journal of Electronic Testing: Theory and Applications (JETTA)</i> , 2005 , 21, 503-537	0.7	12
41	Noninvasive cuffless blood pressure measurement by vascular transit time 2015 ,		11
40	A Cascade Point of Load DCDC Converter With a Novel Phase Shifted Switched Capacitor Converter Output Stage. <i>IEEE Transactions on Power Electronics</i> , 2016 , 31, 353-368	7.2	11

39	Voltage-Mode Digital Pulse Skipping Control of a DCDC Converter With Stable Periodic Behavior and Improved Light-Load Efficiency. <i>IEEE Transactions on Power Electronics</i> , 2016 , 31, 3372-3379	7.2	10
38	. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , 2018 , 65, 1442-1453	3.9	8
37	A Single-Stage Low-Dropout Regulator With a Wide Dynamic Range for Generic Applications. <i>IEEE Transactions on Very Large Scale Integration (VLSI) Systems</i> , 2015 , 1-11	2.6	7
36	Chaos-Modulated Ramp IC for EMI Reduction in PWM Buck Converters Design and Analysis of Critical Issues 2008 ,		7
35	Modeling and analysis of DC-DC converters under Pulse Skipping Modulation 2008 ,		7
34	Remaining useful life estimation of lithium-ion batteries based on the internal resistance growth model 2017 ,		6
33	Design and implementation of a programmable solar photovoltaic simulator 2010 ,		6
32	Voltage controlled pulse skipping modulation: Extension towards the ultra light load 2009 ,		6
31	A Novel Control Technique for Single-Inductor Multiple-Output DC-DC Buck Converters 2006 ,		6
30	Modeling of cardiovascular circulation for the early detection of coronary arterial blockage. <i>Mathematical Biosciences</i> , 2018 , 304, 79-88	3.9	5
29	A novel current controlled tri-state boost converter with superior dynamic performance 2008 ,		5
28	Diagnosability of fair discrete event systems. <i>Asian Journal of Control</i> , 2008 , 10, 651-665	1.7	5
27	Estimation of Arterial Blood Pressure Waveform from Photoplethysmogram Signal using Linear Transfer Function Approach. <i>Annual International Conference of the IEEE Engineering in Medicine and Biology Society IEEE Engineering in Medicine and Biology Society Annual International Conference</i> , 2020 , 2020, 2691-2694	0.9	5
26	Saturation Generated Oscillations in Voltage-Mode Digital Control of DCDC Converters. <i>IEEE Transactions on Power Electronics</i> , 2016 , 31, 4549-4564	7.2	4
25	A Capacitor-less Low Drop-out (LDO) Regulator with Improved Transient Response for System-on-Chip Applications 2013 ,		4
24	AGC of a hydrothermal system with SMES unit 2006 ,		4
23	Distribution network reconfiguration using distributed generation unit considering variations of load 2016 ,		4
22	Particle filtering based estimation of remaining useful life of lithium-ion batteries employing power fading data 2017 ,		3

21	A Fast Exploration Procedure for Analog High-Level Specification Translation. <i>IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems</i> , 2008 , 27, 1493-1497	2.5	3
20	On-chip implementation of a multi-output voltage regulator based on single inductor Buck Converter topology 2007 ,		3
19	Fractional pulse skipping in digitally controlled DC-DC converters for improved light-load efficiency and power spectrum 2016 ,		2
18	ONE-DIMENSIONAL DISCONTINUOUS MAP ANALYSIS OF DCDC CONVERTERS UNDER VOLTAGE CONTROLLED PULSE SKIPPING MODULATION. <i>International Journal of Bifurcation and Chaos in Applied Sciences and Engineering</i> , 2012 , 22, 1250049	2	2
17	A New Approach for Estimation of On-Resistance and Current Distribution in Power Array Layouts 2008 ,		2
16	Broadband scalable model for Si-RF on-chip spiral inductors with substrate eddy current effect. <i>International Journal of RF and Microwave Computer-Aided Engineering</i> , 2007 , 17, 560-573	1.5	2
15	Embedded Reconfigurable Augmented DC-DC Boost Converter for Fast Transient Recovery 2013 ,		1
14	Bifurcation behavior of a boost converter under voltage controlled pulse skipping modulation in the light of 1-D discontinuous map model 2010 ,		1
13	RHP zero elimination with near optimum regulation in a current controlled tri-state boost converter through inductor current filtering 2010 ,		1
12	A new approach for estimation of RDS(on) of power arrays: Extensions and experimental results 2009 ,		1
11	Resistance Estimation for Lateral Power Arrays Through Accurate Netlist Generation. <i>IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems</i> , 2009 , 28, 837-845	2.5	1
10	A Nondeterministic Extension over Finitely Recursive Process Model. <i>Discrete Event Dynamic Systems: Theory and Applications</i> , 1997 , 7, 243-274	1	1
9	Resistance Estimation of Lateral Power Arrays through Accurate Netlist Generation 2007 ,		1
8	A Structured Approach for Rapid Identification of Fault-Sensitive Nets in Analog Circuits 2019 ,		1
7	An approach to early stage detection of atherosclerosis using arterial blood pressure measurements. <i>Biomedical Signal Processing and Control</i> , 2021 , 68, 102594	4.9	1
6	A Methodology for Identification of Internal Nets for Improving Fault Coverage in Analog and Mixed Signal Circuits. <i>Journal of Electronic Testing: Theory and Applications (JETTA)</i> , 2020 , 36, 719-730	0.7	0
5	Logical models of discrete event systems: A comparative exposition. <i>Sadhana - Academy Proceedings in Engineering Sciences</i> , 1996 , 21, 683-718	1	0
4	Interference rejection in quadrature band-pass (varSigma varDelta) modulators. <i>Analog Integrated Circuits and Signal Processing</i> , 2015 , 83, 95-101	1.2	

- 3 Estimation of dc Performance of a Lateral Power MOSFET Using Distributed Cell Model. *IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems*, **2012**, 31, 1452-1456 2.5
- 2 PMDAn Algorithm for Finding Determinant of a Polynomial Matrix with New Data Structure. *IETE Journal of Research*, **1993**, 39, 51-53 0.9
- 1 A Lumped Parameter Model for Cardiac Output Estimation Using Arterial Blood Pressure Waveform. *Annual International Conference of the IEEE Engineering in Medicine and Biology Society IEEE Engineering in Medicine and Biology Society Annual International Conference*, **2021**, 2021, 5523-5526 0.9