

Xing Yang

List of Publications by Year in descending order

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papers

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1684188
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1872680
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7
docs citations

7
times ranked

19
citing authors

#	ARTICLE	IF	CITATIONS
1	Noniterative WLS design of digital FIR filters for aliasing errors cancellation in bandwidth interleaving digital-to-analog converter. IEICE Electronics Express, 2022, 19, 20210507-20210507.	0.8	0
2	Minimax and WLS Designs of Digital FIR Filters Using SOCP for Aliasing Errors Reduction in BI-DAC. IEEE Access, 2019, 7, 11722-11735.	4.2	8
3	A Sampling Rate Selecting Algorithm for the Arbitrary Waveform Generator. IEEE Access, 2019, 7, 83761-83770.	4.2	6
4	Low-complexity minimax design of digital FIR filters utilising linear programming in BI-DAC. Electronics Letters, 2019, 55, 408-411.	1.0	3
5	The design of a wide bandwidth time marker generator. Review of Scientific Instruments, 2018, 89, 115103.	1.3	5
6	Minimax design of digital FIR filters using linear programming in bandwidth interleaving digital-to-analog converter. IEICE Electronics Express, 2018, 15, 20180565-20180565.	0.8	8
7	Estimation and compensation methods of time delay and phase offset in hybrid filter bank DACs. Electronics Letters, 2018, 54, 806-808.	1.0	10