

Saeid Gorgin

List of Publications by Year in descending order

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Version: 2024-02-01

51
papers

620
citations

933447

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677142

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54
all docs

54
docs citations

54
times ranked

563
citing authors

#	ARTICLE	IF	CITATIONS
1	High-performance and low-energy approximate full adder design for error-resilient image processing. International Journal of Electronics, 2022, 109, 1059-1079.	1.4	1
2	A Practical Energy/Power Reduction Approach for Parallel Decimal Multiplier. IEEE Access, 2022, 10, 11372-11381.	4.2	1
3	A TSX-Based KASLR Break: Bypassing UMIP and Descriptor-Table Exiting. Lecture Notes in Computer Science, 2022, , 38-54.	1.3	1
4	An Efficient FPGA Implementation of k-Nearest Neighbors via Online Arithmetic. , 2022, , .		4
5	Gene co-expression network analysis reveals immune cell infiltration as a favorable prognostic marker in non-uterine leiomyosarcoma. Scientific Reports, 2021, 11, 2339.	3.3	12
6	Identification of Prognostic Genes in Her2-enriched Breast Cancer by Gene Co-Expression Network Analysis. Iranian Journal of Breast Diseases, 2021, 14, 49-63.	0.3	0
7	ai-corona: Radiologist-assistant deep learning framework for COVID-19 diagnosis in chest CT scans. PLoS ONE, 2021, 16, e0250952.	2.5	44
8	High-Performance Deterministic Stochastic Computing Using Residue Number System. IEEE Design and Test, 2021, 38, 60-68.	1.2	3
9	On Using Monte-Carlo Tree Search to Solve Puzzles. , 2021, , .		2
10	A High-throughput Parallel Viterbi Algorithm via Bitslicing. ACM Transactions on Parallel Computing, 2021, 8, 1-25.	1.4	2
11	Unlucky Explorer: A Complete non-Overlapping Map Exploration. , 2021, , .		1
12	A fuzzy irregular cellular automata-based method for the vertex colouring problem. Connection Science, 2020, 32, 37-52.	3.0	2
13	A single layer fractional orthogonal neural network for solving various types of Lane's Emden equation. New Astronomy, 2020, 75, 101307.	1.8	43
14	Decentralized Communication-less Multi-Agent Task Assignment with Cooperative Monte-Carlo Tree Search. , 2020, , .		1
15	On the Resilience of Deep Learning for Reduced-voltage FPGAs. , 2020, , .		12
16	BSRNG: A High Throughput Parallel BitSliced Approach for Random Number Generators. , 2020, , .		2
17	A Modified Grey Wolf Optimization for Energy Efficiency and Resource Wastage Balancing in Cloud Data-Centers. , 2020, , .		0
18	Accuracy and availability modeling of social networks for Internet of Things event detection applications. Wireless Networks, 2019, 25, 4299-4317.	3.0	0

#	ARTICLE	IF	CITATIONS
19	Multi-Agent non-Overlapping Pathfinding with Monte-Carlo Tree Search. , 2019, , .		9
20	Improvement of the Recognition of Relationships in Social Networks Using Complementary Graph Coloring Based on Cellular Automata. , 2019, , .		2
21	Determination of Substructures in Social Networks by Graph Colouring Using Fuzzy Irregular Cellular Automata (FICA). , 2019, , .		1
22	Using Residue Number Systems to Accelerate Deterministic Bit-stream Multiplication. , 2019, , .		4
23	Fast AES Implementation: A High-Throughput Bitsliced Approach. IEEE Transactions on Parallel and Distributed Systems, 2019, 30, 2211-2222.	5.6	33
24	Data-Parallel Computational Model for Next Generation Sequencing on Commodity Clusters. Lecture Notes in Computer Science, 2019, , 273-288.	1.3	2
25	The Influence of Memory-Aware Computation on Distributed BLAST. Current Bioinformatics, 2019, 14, 157-163.	1.5	2
26	What are the drivers of the occurrence of illegal fishing and conservation barriers of sturgeons in the Caspian Sea?. Aquatic Conservation: Marine and Freshwater Ecosystems, 2018, 28, 690-701.	2.0	10
27	A high-performance and energy-efficient exhaustive key search approach via GPU on DES-like cryptosystems. Journal of Supercomputing, 2018, 74, 160-182.	3.6	13
28	Title is missing!. Turkish Journal of Fisheries and Aquatic Sciences, 2018, 18, .	0.9	1
29	Design of non-restoring divider in quantum-dot cellular automata technology. IET Circuits, Devices and Systems, 2017, 11, 135-141.	1.4	25
30	Sign-Magnitude Encoding for Efficient VLSI Realization of Decimal Multiplication. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2017, 25, 75-86.	3.1	10
31	A low-power hybrid non-volatile cache with asymmetric coding. , 2017, , .		0
32	An efficient design of full adder in quantum-dot cellular automata (QCA) technology. Microelectronics Journal, 2016, 50, 35-43.	2.0	100
33	Efficient continuous skyline computation on multi-core processors based on Manhattan distance. , 2015, , .		2
34	High performance GPU implementation of k-NN based on Mahalanobis distance. , 2015, , .		6
35	Comment on "High Speed Parallel Decimal Multiplication With Redundant Internal Encodings". IEEE Transactions on Computers, 2015, 64, 293-294.	3.4	4
36	A fast emulator for ARM-based embedded systems. , 2014, , .		4

#	ARTICLE	IF	CITATIONS
37	Cost-efficient implementation of k-NN algorithm on multi-core processors. , 2014, , .		6
38	A comparative study of energy/power consumption in parallel decimal multipliers. Microelectronics Journal, 2014, 45, 775-780.	2.0	5
39	Efficient ASIC and FPGA Implementation of Binary-Coded Decimal Digit Multipliers. Circuits, Systems, and Signal Processing, 2014, 33, 3883-3899.	2.0	7
40	A Review on Modern Distributed Computing Paradigms: Cloud Computing, Jungle Computing and Fog Computing. Journal of Computing and Information Technology, 2014, 22, 69.	0.3	74
41	Shelf-Life Extension of Fish Samples by Using Enriched Chitosan Coating with Thyme Essential Oil. Journal of Aquatic Food Product Technology, 2013, 22, 3-10.	1.4	29
42	GPU-based NoC simulator. , 2011, , .		7
43	A Family of High Radix Signed Digit Adders. , 2011, , .		10
44	An improved maximally redundant signed digit adder. Computers and Electrical Engineering, 2010, 36, 491-502.	4.8	10
45	Redundant-Digit Floating-Point Addition Scheme Based on a Stored Rounding Value. IEEE Transactions on Computers, 2010, 59, 694-706.	3.4	6
46	Design and evaluation of decimal array multipliers. , 2009, , .		7
47	A fully redundant decimal adder and its application in parallel decimal multipliers. Microelectronics Journal, 2009, 40, 1471-1481.	2.0	33
48	Fully Redundant Decimal Arithmetic. , 2009, , .		24
49	Distribution of Macrobrachium nipponense (De Haan, 1849) In Iran (Decapoda, Palaemonidae). Crustaceana, 2008, 81, 943-948.	0.3	4
50	A Nonspeculative Maximally Redundant Signed Digit Adder. Communications in Computer and Information Science, 2008, , 235-242.	0.5	6
51	Reversible Barrel Shifters. , 2007, , .		22