

Taeho Lee

List of Publications by Year in descending order

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citing authors

#	ARTICLE	IF	CITATIONS
1	A 5-Gb/s Digital Clock and Data Recovery Circuit With Reduced DCO Supply Noise Sensitivity Utilizing Coupling Network. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2017, 25, 380-384.	3.1	10
2	An integrated time register and arithmetic circuit with combined operation for time-domain signal processing. , 2015, , .		7
3	A 21%-Jitter-Improved Self-Aligned Dividerless Injection-Locked PLL With a VCO Control Voltage Ripple-Compensated Phase Detector. IEEE Transactions on Circuits and Systems II: Express Briefs, 2016, 63, 733-737.	3.0	7