

Shouri Chatterjee

List of Publications by Year in descending order

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53
papers

945
citations

933447

10
h-index

752698

20
g-index

53
all docs

53
docs citations

53
times ranked

668
citing authors

#	ARTICLE	IF	CITATIONS
1	Edge Intelligence Framework for Data-Driven Dynamic Priority Sensing and Transmission. IEEE Transactions on Green Communications and Networking, 2022, 6, 376-390.	5.5	4
2	PUF Implementation Using Competing OTPM Devices in Novel 5T-2OTPM Differential Configuration. IEEE Transactions on Electron Devices, 2022, 69, 96-102.	3.0	0
3	A Low Cost Outdoor Air Pollution Monitoring Device With Power Controlled Built-In PM Sensor. IEEE Sensors Journal, 2022, 22, 13682-13695.	4.7	11
4	Design of Gate Drive Circuit for Thyristor Stack in Electromagnetic Railgun by Load-Line Analysis. IEEE Transactions on Plasma Science, 2021, 49, 383-388.	1.3	5
5	A 4.4-mA ESD-Safe 900-MHz LNA With 0.9-dB Noise Figure. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2021, 29, 297-306.	3.1	7
6	Comprehensive Design Methodology of Switch Stack in Pulsed Power Supply for EML. IEEE Transactions on Plasma Science, 2021, 49, 1489-1499.	1.3	2
7	Learning-based Smart Sensing for Energy-Sustainable WSN. , 2021, , .		3
8	Learning-Based Adaptive Sensor Selection Framework for Multi-Sensing WSN. IEEE Sensors Journal, 2021, 21, 13551-13563.	4.7	14
9	Analysis on Performance Improvement of Current Maintainable Meat Grinder Driving a Railgun. IEEE Transactions on Plasma Science, 2021, 49, 3963-3969.	1.3	0
10	Prototype Implementation of Dynamic Data Pruning in Smart Energy Meter. , 2021, , .		1
11	A Novel Electromagnetic Launcher Configuration With Improved System and Barrel Efficiencies. IEEE Transactions on Plasma Science, 2020, 48, 3429-3434.	1.3	6
12	Energy Harvesting-enabled 5G Advanced Air Pollution Monitoring Device. , 2020, , .		11
13	Smart IoT Communication: Circuits and Systems. , 2020, , .		10
14	An N-Path Band-Pass Filter With Parametric Gain-Boosting. IEEE Transactions on Circuits and Systems I: Regular Papers, 2019, 66, 3700-3712.	5.4	3
15	On the Output Impedance of Integrated LNAs. , 2019, , .		0
16	Analysis of an Electromagnetic Railgun with Tapered Rails and Concave Armature using 3-D FEM. , 2019, , .		3
17	On-chip learning for domain wall synapse based Fully Connected Neural Network. Journal of Magnetism and Magnetic Materials, 2019, 489, 165434.	2.3	34
18	A 200-pA Under-Voltage Lockout Circuit for Ultra-Low Power Applications. , 2019, , .		2

#	ARTICLE	IF	CITATIONS
19	A 900-MHz 1.25-dB Noise-Figure Differential-Output LNA with 12.5 dB/mW FoM. , 2019, , .		3
20	Design of a Monolithic Inductor and Its Influence on the Oscillator Design. IETE Journal of Research, 2019, , 1-8.	2.6	0
21	Verilog-A SPICE Model of PECVD SiO ₂ OTP Memory Device. , 2019, , .		1
22	A 99% Current Efficient Three-Transistor Regulator With Built-In 80 ppm/°C Reference, for 10 mA Loads. IEEE Solid-State Circuits Letters, 2018, 1, 26-29.	2.0	10
23	A Compact PrMnO ₃ Based Oscillator as an Alternative to CMOS Ring Oscillator in a Smart Temperature Sensor. , 2018, , .		0
24	Charge-Controlled Oscillators and Their Application in Frequency Synthesis. IEEE Transactions on Circuits and Systems II: Express Briefs, 2017, 64, 1127-1131.	3.0	0
25	77 GHz integrated patch antennae in 0.18 μm CMOS technology. , 2016, , .		0
26	A 100-nW Sensitive RF-to-DC CMOS Rectifier for Energy Harvesting Applications. , 2016, , .		9
27	A 24 mW, 80 dB SNR, 50 MHz multi-bit continuous time $\hat{\epsilon}$ ADC in 28 nm FD-SOI. , 2016, , .		2
28	An 18 nA, 87% Efficient Solar, Vibration and RF Energy-Harvesting Power Management System With a Single Shared Inductor. IEEE Journal of Solid-State Circuits, 2016, 51, 2501-2513.	5.4	87
29	Design and Implementation of Computationally Efficient Image Compressor for Wireless Capsule Endoscopy. Circuits, Systems, and Signal Processing, 2016, 35, 1677-1703.	2.0	28
30	A variable interval enhanced jitter tolerant programmable bandwidth blind-oversampling CDR for multi-gigabit rates. , 2015, , .		1
31	A 300-nW Sensitive, 50-nA DC-DC Converter for Energy Harvesting Applications. IEEE Transactions on Circuits and Systems I: Regular Papers, 2015, 62, 2674-2684.	5.4	31
32	A technique to linearize the discrete-time parametric amplifier. Microelectronics Journal, 2015, 46, 1033-1038.	2.0	0
33	On the use of frequency transformations in the design of broad-band and concurrent multi-band power amplifiers. , 2015, , .		0
34	A Low-Power Color Mosaic Image Compressor Based on Optimal Combination of 1-D Discrete Wavelet Packet Transform and DPCM for Wireless Capsule Endoscopy. , 2015, , .		2
35	An inductorless continuous time equalizer with programmability for gigabit links. , 2014, , .		0
36	A 25.5mW 10Gb/s inductorless receiver with an adaptive front-end in 0.13 μm CMOS.. , 2014, , .		0

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37	An Adaptive Inductorless Continuous Time Equalizer for Gigabit Links in 0.13 um CMOS. , 2014, , .		0
38	Multi-Band Frequency Transformations, Matching Networks and Amplifiers. IEEE Transactions on Circuits and Systems I: Regular Papers, 2013, 60, 1635-1647.	5.4	55
39	A 44 GHz Quadrature Traveling Wave Oscillator. , 2013, , .		0
40	Clock and data recovery module in 90nm for 10Gbps serial link with −18dB channel attenuation. , 2013, , .		2
41	A 110-dB Dynamic Range, 76-dB Peak SNR Comanding Continuous-Time ?S Modulator for Audio Applications. , 2012, , .		2
42	Heterogeneous coupled ring oscillator arrays for reduced phase noise at lower power consumption. , 2012, , .		0
43	Network Energy Driven Wireless Sensor Networks. , 2012, , 145-157.		0
44	Third order harmonic cancellation technique for a parametric amplifier. , 2011, , .		2
45	11 GHz UGBW Op-amp with feed-forward compensation technique. , 2011, , .		8
46	Design of concurrent multi-band matching networks. , 2011, , .		8
47	Distortion Analysis of a Three-Terminal MOS-Based Discrete-Time Parametric Amplifier. IEEE Transactions on Circuits and Systems II: Express Briefs, 2011, 58, 902-905.	3.0	12
48	A 0.5-V 74-dB SNDR 25-kHz Continuous-Time Delta-Sigma Modulator With a Return-to-Open DAC. IEEE Journal of Solid-State Circuits, 2007, 42, 496-507.	5.4	104
49	A 0.5-V 1-Msps Track-and-Hold Circuit With 60-dB SNDR. IEEE Journal of Solid-State Circuits, 2007, 42, 722-729.	5.4	18
50	Correction to "A 0.5-V 74-dB SNDR 25-kHz Continuous-Time Delta-Sigma Modulator With a Return-to-Open DAC" IEEE Journal of Solid-State Circuits, 2007, 42, 2315-2315.	5.4	1
51	Comments on "A General Theory of Phase Noise in Electrical Oscillators" IEEE Journal of Solid-State Circuits, 2007, 42, 2314-2314.	5.4	8
52	0.5 V ANALOG INTEGRATED CIRCUITS. , 2006, , 329-350.		1
53	0.5-V analog circuit techniques and their application in OTA and filter design. IEEE Journal of Solid-State Circuits, 2005, 40, 2373-2387.	5.4	434