

Evangelos Eleftheriou

List of Publications by Year in descending order

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Version: 2024-02-01

28
papers

3,947
citations

586496

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22
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all docs

28
docs citations

28
times ranked

3869
citing authors

#	ARTICLE	IF	CITATIONS
1	Online Spatio-Temporal Learning in Deep Neural Networks. IEEE Transactions on Neural Networks and Learning Systems, 2023, 34, 8894-8908.	7.2	16
2	HERMES-Core: A 1.59-TOPS/mm ² PCM on 14-nm CMOS In-Memory Compute Core Using 300-ps/LSB Linearized CCO-Based ADCs. IEEE Journal of Solid-State Circuits, 2022, 57, 1027-1038.	3.5	49
3	Introducing principles of synaptic integration in the optimization of deep neural networks. Nature Communications, 2022, 13, 1885.	5.8	13
4	Speech Recognition Using Biologically-Inspired Neural Networks. , 2022, , .		3
5	An SRAM-Based Multibit In-Memory Matrix-Vector Multiplier With a Precision That Scales Linearly in Area, Time, and Power. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2021, 29, 372-385.	2.1	11
6	Efficient Pipelined Execution of CNNs Based on In-Memory Computing and Graph Homomorphism Verification. IEEE Transactions on Computers, 2021, 70, 922-935.	2.4	9
7	Convergence Behavior of DNNs with Mutual-Information-Based Regularization. Entropy, 2020, 22, 727.	1.1	9
8	Accurate deep neural network inference using computational phase-change memory. Nature Communications, 2020, 11, 2473.	5.8	263
9	Mixed-Precision Deep Learning Based on Computational Memory. Frontiers in Neuroscience, 2020, 14, 406.	1.4	61
10	Experimental Demonstration of Supervised Learning in Spiking Neural Networks with Phase-Change Memory Synapses. Scientific Reports, 2020, 10, 8080.	1.6	48
11	Deep learning incorporating biologically inspired neural dynamics and in-memory computing. Nature Machine Intelligence, 2020, 2, 325-336.	8.3	86
12	Memory devices and applications for in-memory computing. Nature Nanotechnology, 2020, 15, 529-544.	15.6	968
13	Accelerating Spiking Neural Networks using Memristive Crossbar Arrays. , 2020, , .		1
14	Low-Power Neuromorphic Hardware for Signal Processing Applications: A Review of Architectural and System-Level Design Approaches. IEEE Signal Processing Magazine, 2019, 36, 97-110.	4.6	88
15	Mixed-precision in-memory computing. Nature Electronics, 2018, 1, 246-253.	13.1	315
16	A phase-change memory model for neuromorphic computing. Journal of Applied Physics, 2018, 124, .	1.1	96
17	Tutorial: Brain-inspired computing using phase-change memory devices. Journal of Applied Physics, 2018, 124, .	1.1	206
18	Compressed Sensing With Approximate Message Passing Using In-Memory Computing. IEEE Transactions on Electron Devices, 2018, 65, 4304-4312.	1.6	78

#	ARTICLE	IF	CITATIONS
19	Neuromorphic computing with multi-memristive synapses. Nature Communications, 2018, 9, 2514.	5.8	566
20	Mixed-precision architecture based on computational memory for training deep neural networks. , 2018, , .		42
21	Unsupervised Learning Using Phase-Change Synapses and Complementary Patterns. Lecture Notes in Computer Science, 2017, , 281-288.	1.0	11
22	Neuromorphic system with phase-change synapses for pattern learning and feature extraction. , 2017, , .		5
23	Fatiguing STDP: Learning from spike-timing codes in the presence of rate codes. , 2017, , .		7
24	Learning spatio-temporal patterns in the presence of input noise using phase-change memristors. , 2016, , .		12
25	Stochastic phase-change neurons. Nature Nanotechnology, 2016, 11, 693-699.	15.6	799
26	Detecting Correlations Using Phase-Change Neurons and Synapses. IEEE Electron Device Letters, 2016, 37, 1238-1241.	2.2	54
27	All-memristive neuromorphic computing with level-tuned neurons. Nanotechnology, 2016, 27, 355205.	1.3	102
28	A 256-Mcell Phase-Change Memory Chip Operating at 2×10^8 Bit/Cell. IEEE Transactions on Circuits and Systems I: Regular Papers, 2013, 60, 1521-1533.	3.5	29