

# Bhaskara Rao Jammu

## List of Publications by Year in descending order

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11  
papers

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citations

2682572

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2053705

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11  
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docs citations

11  
times ranked

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citing authors

#	ARTICLE	IF	CITATIONS
1	FPGA implementation of rule optimization for stand-alone tunable fuzzy logic controller using GA. Complex & Intelligent Systems, 2016, 2, 83-98.	6.5	9
2	FPGA implementation of BASK-BFSK-BPSK-DPSK digital modulators using system generator. , 2017, , .		7
3	Hardware-efficient approximate logarithmic division with improved accuracy. International Journal of Circuit Theory and Applications, 2021, 49, 128-141.	2.0	5
4	VLSI implementation of LTSSM. , 2017, , .		2
5	Analytical Modeling of Threshold Voltage for Dual-Metal Double-Gate Gate-All-Around (DM-DG-GAA) MOSFET. Silicon, 2021, 13, 2869-2880.	3.3	2
6	A Low Error, Hardware Efficient Logarithmic Multiplier. Circuits, Systems, and Signal Processing, 2022, 41, 485-513.	2.0	2
7	Automatic diagnosis of pneumonia using backward elimination method based SVM and its hardware implementation. International Journal of Imaging Systems and Technology, 0, , .	4.1	2
8	VLSI Architecture of Reduced Rule Base Inference for Run-Time Configurable Fuzzy Logic Controllers. Advances in Intelligent Systems and Computing, 2013, , 77-88.	0.6	1
9	A low-error, memory-based fast binary antilogarithmic converter. International Journal of Circuit Theory and Applications, 2021, 49, 2214-2226.	2.0	0
10	An Efficient and Optimized Converter for Fast Binary to Decimal Conversion. International Journal of Electronics Letters, 2022, 10, 272-282.	1.2	0
11	Hardware efficient circuit for low error logarithmic converter. Journal of Computational Methods in Sciences and Engineering, 2022, 22, 511-527.	0.2	0