

# Marc Gonzalez Tallada

## List of Publications by Year in descending order

Source: <https://exaly.com/author-pdf/4205700/publications.pdf>

Version: 2024-02-01

6  
papers

87  
citations

2258059

3  
h-index

2053705

5  
g-index

6  
all docs

6  
docs citations

6  
times ranked

69  
citing authors

#	ARTICLE	IF	CITATIONS
1	Multi-GPU Parallelization of the NAS Multi-Zone Parallel Benchmarks. IEEE Transactions on Parallel and Distributed Systems, 2021, 32, 229-241.	5.6	1
2	Hardware-Software Coherence Protocol for the Coexistence of Caches and Local Memories. IEEE Transactions on Computers, 2015, 64, 152-165.	3.4	5
3	Counter-Based Power Modeling Methods: Top-Down vs. Bottom-Up. Computer Journal, 2013, 56, 198-213.	2.4	21
4	Automatic Prefetch and Modulo Scheduling Transformations for the Cell BE Architecture. IEEE Transactions on Parallel and Distributed Systems, 2010, 21, 494-505.	5.6	4
5	Automatic thread distribution for nested parallelism in OpenMP. , 2005, , .		29
6	NanosCompiler: supporting flexible multilevel parallelism exploitation in OpenMP. Concurrency and Computation: Practice and Experience, 2000, 12, 1205-1218.	0.5	27