

Jorge Echavarria

List of Publications by Year in descending order

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9
papers

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citations

3311381

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9
docs citations

9
times ranked

36
citing authors

#	ARTICLE	IF	CITATIONS
1	Design and error analysis of accuracy-configurable sequential multipliers via segmented carry chains. IT - Information Technology, 2022, .	0.9	0
2	Probabilistic Error Propagation through Approximated Boolean Networks. , 2020, , .		8
3	Efficient Arithmetic Error Rate Calculus for Visibility Reduced Approximate Adders. IEEE Embedded Systems Letters, 2018, 10, 37-40.	1.9	6
4	Can Approximate Computing Reduce Power Consumption on FPGAs?. , 2018, , .		4
5	AConFPGA: A Multiple-Output Boolean Function Approximation DSE Technique Targeting FPGAs. , 2018, , .		1
6	Self-Adaptive FPGA-Based Image Processing Filters Using Approximate Arithmetics. , 2017, , .		2
7	FAU: Fast and error-optimized approximate adder units on LUT-Based FPGAs. , 2016, , .		16
8	A LUT-Based Approximate Adder. , 2016, , .		14
9	FSM merging and reduction for IP cores watermarking using Genetic Algorithms. , 2014, , .		4