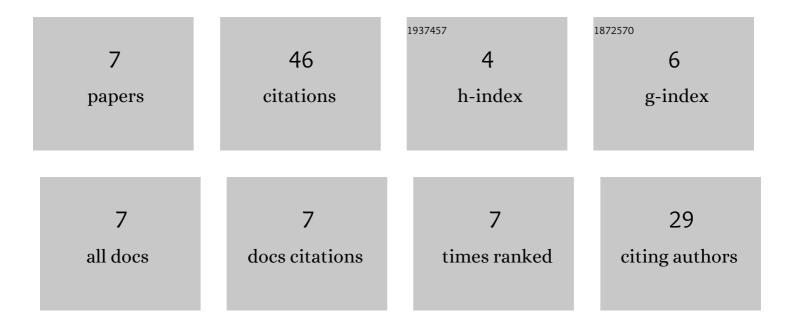
Rohini Srivastava

List of Publications by Year in descending order

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POHINI SDIVASTAVA

#	Article	IF	CITATIONS
1	Design of Anfis based pacemaker controller having improved transient response and its FPGA implementation. Biomedical Signal Processing and Control, 2022, 71, 103186.	3.5	6
2	Automatic Arrhythmia Detection Based on the Probabilistic Neural Network with FPGA Implementation. Mathematical Problems in Engineering, 2022, 2022, 1-11.	0.6	8
3	Comparative analysis of piezo electric and accelerometer sensor for the design of rate adaptive pacemaker. Measurement: Sensors, 2021, 16, 100053.	1.3	2
4	An FPGA-based design for power efficient low delay rate adaptive pacemaker using accelerometer and heart rate sensor. Journal of Medical Engineering and Technology, 2021, , 1-9.	0.8	1
5	Low delay rate adaptive pacemaker using FPGA embedded piezoelectric sensor. Journal of Medical Engineering and Technology, 2020, 44, 423-430.	0.8	5
6	Implementation of Low Delay Dual Chamber Pacemaker Using Verilog. , 2019, , .		3
7	Computationally efficient joint imperceptible image watermarking and JPEG compression: a green computing approach. Multimedia Tools and Applications, 2018, 77, 16447-16459.	2.6	21