

Qingzeng Yan

List of Publications by Year in descending order

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#	ARTICLE	IF	CITATIONS
1	A Novel Zero-Sequence-Voltage-Balanced SVPWM With the Analyses of Common-Mode Voltage and Differential-Mode Current. IEEE Journal of Emerging and Selected Topics in Power Electronics, 2022, 10, 2483-2496.	5.4	4
2	An Analytical Discontinuous Space-Vector PWM for Three-Level Inverters With Unbalanced DC-Link Voltages. IEEE Transactions on Power Electronics, 2022, 37, 7718-7728.	7.9	11
3	An Analytical Simplified Three-Level SVPWM With Unified Zero-Sequence Component Injection. IEEE Transactions on Power Delivery, 2022, 37, 2417-2420.	4.3	8
4	Gershgorin-Circle Based Low-Complexity Generalized Nyquist Stability Criterion for DFIG Driven Wind Turbines. IEEE Journal on Emerging and Selected Topics in Circuits and Systems, 2022, 12, 29-40.	3.6	4
5	Stability Assessment and Enhanced Control of DFIG-Based WTs During Weak AC Grid. IEEE Access, 2022, 10, 41371-41380.	4.2	2
6	A Simplified Analytical Algorithm in $\alpha\beta$ Coordinate for the Three-Level SVPWM. IEEE Transactions on Power Electronics, 2021, 36, 3622-3627.	7.9	21
7	Erratum to "A Double-Modulation-Wave PWM for Dead-Time-Effect Elimination and Synchronous Rectification in SiC-Device-Based High-Switching-Frequency Converters" [Dec 20 13500-13513]. IEEE Transactions on Power Electronics, 2021, 36, 2448-2448.	7.9	0
8	A Double-Modulation-Wave PWM With Reduced Dependency on Current Polarities for Dead-Time-Effect Elimination in Three-Level T-Type Converters. IEEE Transactions on Power Electronics, 2021, 36, 8413-8427.	7.9	4
9	A Practical Core Loss Estimation Method for Three-Phase Three-Level Grid-Connected Inverters. IEEE Transactions on Power Electronics, 2020, 35, 2263-2267.	7.9	13
10	Common-Mode Resonance Damping and DC Voltage Balancing Strategy for LCCL-Filtered Three-Level Photovoltaic Grid-Tied Inverters. IEEE Access, 2020, 8, 13228-13239.	4.2	4
11	Optimal Dispatching Strategy of an Electric-Thermal-Gas Coupling Microgrid Considering Consumer Satisfaction. IEEE Access, 2020, 8, 173169-173176.	4.2	9
12	A Double-Modulation-Wave PWM for Dead-Time-Effect Elimination and Synchronous Rectification in SiC-Device-Based High-Switching-Frequency Converters. IEEE Transactions on Power Electronics, 2020, 35, 13500-13513.	7.9	12
13	A Combined DPWM Based on DSOGI-FLL for Switching-Loss Reduction and Dead-Time Compensation. IEEE Access, 2020, 8, 34783-34793.	4.2	8
14	Optimization of Current Zero-Crossings in a Dead-Time-Effect Elimination PWM for SiC-Device-based High-Switching-Frequency Converters. , 2020, , .		1
15	A DSOGI-FLL-Based Dead-Time Elimination PWM for Three-Phase Power Converters. IEEE Transactions on Power Electronics, 2019, 34, 2805-2818.	7.9	40
16	An Iron Loss Estimation Method Based on Dual Current-Ripple Envelopes for Three-Phase Two-Level Grid-Connected Converters. IEEE Access, 2019, 7, 159994-160003.	4.2	2
17	SiC MOSFET Switching Waveform Profiling Through Passive Networks. , 2018, , .		4
18	Analysis and Compensation of Dead-Time Effect in SiC-Device-based High-Switching-Frequency Inverters. , 2018, , .		7

#	ARTICLE	IF	CITATIONS
19	Performance Evaluation of Split Output Converters With SiC MOSFETs and SiC Schottky Diodes. IEEE Transactions on Power Electronics, 2017, 32, 406-422.	7.9	67
20	A 100kHz 95.91% efficiency SiC-device-based split output converter with EMI reduction. , 2016, , .		1
21	An Improved Grid-Voltage Feedforward Strategy for High-Power Three-Phase Grid-Connected Inverters Based on the Simplified Repetitive Predictor. IEEE Transactions on Power Electronics, 2016, 31, 3880-3897.	7.9	99