Haoran Wang

List of Publications by Year in descending order

Source: https://exaly.com/author-pdf/4131966/publications.pdf

Version: 2024-02-01

759233 713466 46 691 12 21 h-index citations g-index papers 46 46 46 433 all docs docs citations times ranked citing authors

#	Article	IF	CITATIONS
1	A Robust Testing Method for DC and AC Capacitors With Minimum Required Power Supply. IEEE Transactions on Power Electronics, 2022, 37, 4942-4946.	7.9	7
2	Robust Stability Assessment of Single-Phase Inverter With Multiparameter Distributions. IEEE Transactions on Power Electronics, 2022, 37, 6062-6073.	7.9	6
3	An On-Line Calibration Method for TSEP-Based Junction Temperature Estimation. IEEE Transactions on Industrial Electronics, 2022, 69, 13616-13624.	7.9	7
4	Design of Common DC-Link Capacitor in Multiple-Drive System Based on Reduced DC-Link Current Harmonics Modulation. IEEE Transactions on Power Electronics, 2022, 37, 9703-9717.	7.9	8
5	An Off-line Parameter Estimation Method for High-order DC-link Filter. , 2022, , .		1
6	A Stress Emulation Method for Concurrent Testing of AC and DC Capacitors. , 2022, , .		1
7	An Adaptive Active Inductor for the AC Filter of Grid-connected Drive., 2022,,.		2
8	Realizing Constant Current and Constant Voltage Outputs and Input Zero Phase Angle of Wireless Power Transfer Systems With Minimum Component Counts. IEEE Transactions on Intelligent Transportation Systems, 2021, 22, 600-610.	8.0	61
9	Electrothermal Stress Analysis and Lifetime Evaluation of DC-Link Capacitor Banks in the Railway Traction Drive System. IEEE Journal of Emerging and Selected Topics in Power Electronics, 2021, 9, 4269-4284.	5.4	17
10	Lifetime Prediction of DC-Link Capacitors in Multiple Drives System Based on Simplified Analytical Modeling. IEEE Transactions on Power Electronics, 2021, 36, 844-860.	7.9	20
11	Analytical Modeling and Design of Capacitor Bank Considering Thermal Coupling Effect. IEEE Transactions on Power Electronics, 2021, 36, 2629-2640.	7.9	5
12	DC-Link Current Harmonic Mitigation via Phase-Shifting of Carrier Waves in Paralleled Inverter Systems. Energies, 2021, 14, 4229.	3.1	4
13	Overview of recent progress in condition monitoring for insulated gate bipolar transistor modules: Detection, estimation, and prediction. High Voltage, 2021, 6, 967-977.	4.7	5
14	Design and Benchmark of Passive and Active Inductors for a 7.5 kW Motor Drive., 2021,,.		1
15	An Overview of Capacitive DC-Links-Topology Derivation and Scalability Analysis. IEEE Transactions on Power Electronics, 2020, 35, 1805-1829.	7.9	83
16	A Thermal Modeling Method Considering Ambient Temperature Dynamics. IEEE Transactions on Power Electronics, 2020, 35, 6-9.	7.9	23
17	A Cost-Constrained Active Capacitor for a Single-Phase Inverter. IEEE Transactions on Power Electronics, 2020, 35, 6746-6760.	7.9	11
18	Model-Based Design and Optimization of Hybrid DC-Link Capacitor Banks. IEEE Transactions on Power Electronics, 2020, 35, 8910-8925.	7.9	26

#	Article	IF	Citations
19	Multitimescale Reliability Evaluation of DC-Link Capacitor Banks in Metro Traction Drive System. IEEE Transactions on Transportation Electrification, 2020, 6, 213-227.	7.8	28
20	A Two-Terminal Active Inductor With Minimum Apparent Power for the Auxiliary Circuit. IEEE Transactions on Power Electronics, 2019, 34, 1013-1016.	7.9	17
21	Lifetime Estimation of DC-Link Capacitors in Adjustable Speed Drives Under Grid Voltage Unbalances. IEEE Transactions on Power Electronics, 2019, 34, 4064-4078.	7.9	118
22	Performance Evaluation of a Two-terminal Active Inductor in the DC-link Filter of a Three-phase Diode Bridge Rectifier. , 2019, , .		2
23	Benchmark of DC-link LC Filters based on Passive Inductor and Two-terminal Active Inductor. , 2019, , .		2
24	Reliability Assessment of Hybrid Capacitor Bank Using Electrolytic- and Film-Capacitors in Three-Level Neutral-Point-Clamped Inverters. , 2019, , .		11
25	Lifetime Investigation of DC-link Capacitors in Multiple Slim Drives System. , 2019, , .		0
26	Design and Benchmark of Capacitive DC Links for the Hold-up Time Application. , 2019, , .		1
27	Reliability Evaluation of DC-link Capacitors in Multi-drive Systems. , 2019, , .		3
28	On the Practical Design of a Two-Terminal Active Capacitor. IEEE Transactions on Power Electronics, 2019, 34, 10006-10020.	7.9	23
29	Lifetime benchmarking of two DC-link passive filtering configurations in adjustable speed drives. , 2018, , .		11
30	Reliability Evaluation and Optimization of Capacitor Bank. , 2018, , .		4
31	An analytical circuit based nonlinear thermal model for capacitor banks. Microelectronics Reliability, 2018, 88-90, 524-527.	1.7	12
32	A Two-Terminal Active Capacitor. IEEE Transactions on Power Electronics, 2017, 32, 5893-5896.	7.9	67
33	Reliability-oriented design of a cost-effective active capacitor. , 2017, , .		3
34	A voltage control method for an active capacitive DC-link module with series-connected circuit. , 2017, , .		6
35	Practical considerations of the start-up procedure for an active capacitor. , 2017, , .		1
36	Impedance characteristics modeling of a two-terminal active capacitor. , 2017, , .		5

#	Article	lF	CITATIONS
37	System reliability evaluation considering parameter variations of a single-phase inverter with integrated active power decoupling. , $2017, \dots$		0
38	An active capacitor with self-power and internal feedback control signals. , 2017, , .		4
39	A reliability-oriented component sizing procedure for cost benchmarking of capacitive DC-links. , 2017,		1
40	Reliability evaluation of a single-phase H-bridge inverter with integrated active power decoupling. , 2016, , .		3
41	A generic topology derivation method for single-phase converters with active capacitive DC-links. , 2016, , .		6
42	Lifetime estimation of DC-link capacitors in a single-phase converter with an integrated active power decoupling module. , 2016 , , .		4
43	Power loss analysis and comparision of DC and AC side decoupling module in a H-bridge inverter. , 2016, , .		1
44	Cost assessment of three power decoupling methods in a single-phase power converter with a reliability-oriented design procedure. , 2016, , .		10
45	Enhanced Single-Phase Full-Bridge Inverter With Minimal Low-Frequency Current Ripple. IEEE Transactions on Industrial Electronics, 2016, 63, 937-943.	7.9	55
46	Waveform control method for mitigating harmonics of inverter systems with nonlinear load. , 2015, , .		5