

Jiangyi Li

List of Publications by Year in descending order

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8
papers

146
citations

1684188

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181
citing authors

| # | ARTICLE | IF | CITATIONS |
|---|--------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|-----|-----------|
| 1 | A Femto/Pico-Watt Feedforward Leakage Self-Suppression Logic Family in 180 nm to 28 nm Technologies. , 2019, , . | | 1 |
| 2 | An Area-Efficient Microprocessor-Based SoC With an Instruction-Cache Transformable to an Ambient Temperature Sensor and a Physically Unclonable Function. IEEE Journal of Solid-State Circuits, 2018, 53, 728-737. | 5.4 | 23 |
| 3 | A fW- and kHz-Class Feedforward Leakage Self-Suppression Logic Requiring No External Sleep Signal to Enter the Leakage Suppression Mode. IEEE Solid-State Circuits Letters, 2018, 1, 150-153. | 2.0 | 12 |
| 4 | \ln -Situ \ln -Field Technique for Monitoring and Decelerating NBTI in 6T-SRAM Register Files. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2018, 26, 2241-2253. | 3.1 | 6 |
| 5 | Triple-Mode, Hybrid-Storage, Energy Harvesting Power Management Unit: Achieving High Efficiency Against Harvesting and Load Power Variabilities. IEEE Journal of Solid-State Circuits, 2017, 52, 2550-2562. | 5.4 | 25 |
| 6 | A technique to transform 6T-SRAM arrays into robust analog PUF with minimal overhead. , 2017, , . | | 8 |
| 7 | Ultra-Compact and Robust Physically Unclonable Function Based on Voltage-Compensated Proportional-to-Absolute-Temperature Voltage Generators. IEEE Journal of Solid-State Circuits, 2016, 51, 2192-2202. | 5.4 | 70 |
| 8 | Energy-optimal voltage model supporting a wide range of nodal switching rates for early design-space exploration. , 2015, , . | | 1 |